

PJESDA5V6-4LCG SERIES

LOW CAPACITANCE QUAD ARREY FOR ESD PROTECTION

FEATURES

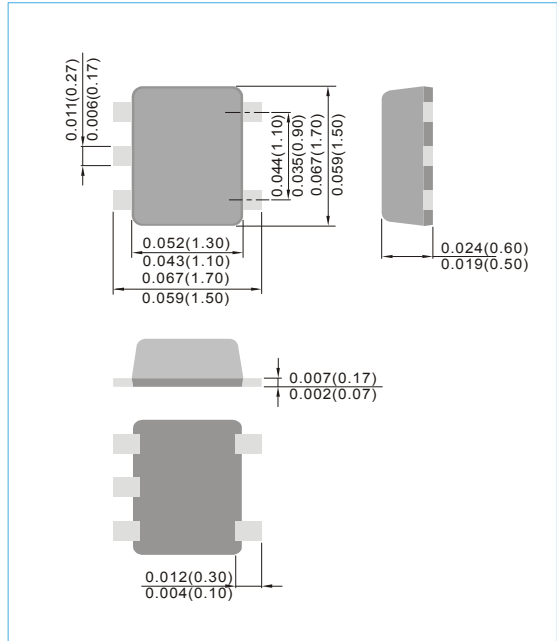
- ESD Protection : IEC61000-4-2
- Four Separate Unidirectional Configurations for Protection
- Low Leakage Current < 1 μ A @ 3Volts
- Power Dissipation : 380mW
- Low Capacitance
-

MECHANICAL DATA

- Case: SOT-553, Molded Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Polarity: See Diagram Below
- Approx. Weight: 0.002 gram
- Mounting Position: Any

SOT-553

Unit : inch(mm)



MAXIMUM RATINGS (TA=25°C unless otherwise noted)

Parameter	Symbol	Value	Units
Peak Power Dissipation (8x20 μ s@TA=25°C) (Note 1)	PPK	20	W
Steady State Power-1 Diode (Note 2)	Pd	380	mW
Thermal Resistance, Junction-to-Ambient Above 25°C Derate	R θ JA	327 3.05	°C/W mW/°C
Maximum Junction Temperature	TJMAX	150	°C
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 to +150	°C
Lead Solder Temperature (10 seconds duration)	TL	260	°C

ELECTRAICAL CHARACTERISTICS(TA=25°C)

Part Number	Breakdown Voltage VBR @ 1mA(Volts)			Leakage Current IRM@VRM		VcMAX@IPP		Typ Capacitance@0V Bias (pF)(Note 3)		Typ Capacitance@3V Bias (pF)(Note 3)		Marking Code
	Min.	Nom.	Max.	VRWM	IRWM(mA)	VC(V)	IPP(A)	Typ	Max	Typ	Max	
PJESDA5V6-4LCG	5.32	5.60	5.88	3.0	0.5	13	1.6	13	17	7.0	11.5	SH
PJESDA6V8-4LCG	6.37	6.71	7.04	5	0.5	13	1.6	12	15	6.7	9.5	SL

NOTES:

- 1.Non-repetitive current per Figure 1.
- 2.Only 1 diode under power. For all 4 diodes under power,P_D will be 25%.
Mounted on FR-4 board with min pad.
- 3.Capacitance of one diode at f=1MHz, VR=0V, TA=25°C

