

RKD702KL

Silicon Schottky Barrier Diode for High Speed Switching

REJ03G1542-0100

Rev.1.00

Sep 03, 2007

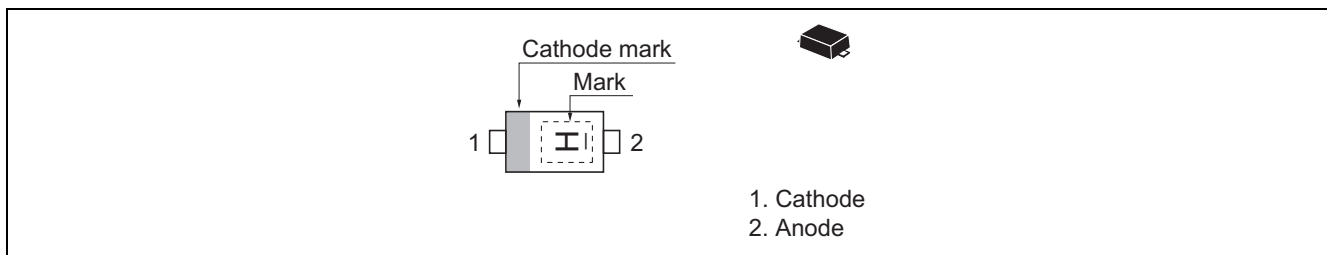
Features

- Low Power consumption (Low reverse leak current) and high speed (Low capacitance).
- Lineup of Environmental friendly Halogen free type (RKD702KL-N).
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code
RKD702KL	H	EFP	PXSF0002ZA-A
RKD702KL-N (Halogen-free type)			

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	30	V
Non-Repetitive Peak forward surge current	I_{FSM}^*	200	mA
Forward current	I_F	50	mA
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

Note: 10 ms Sine Wave 1 pulse

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	—	—	0.35	V	$I_F = 1 \text{ mA}$
	V_{F2}	—	—	0.40	V	$I_F = 5 \text{ mA}$
Reverse current	I_{R1}	—	—	100	nA	$V_R = 20 \text{ V}$
	I_{R2}	—	—	250	nA	$V_R = 30 \text{ V}$
Capacitance	C	—	—	2.50	pF	$V_R = 1 \text{ V}, f = 1 \text{ MHz}$

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic

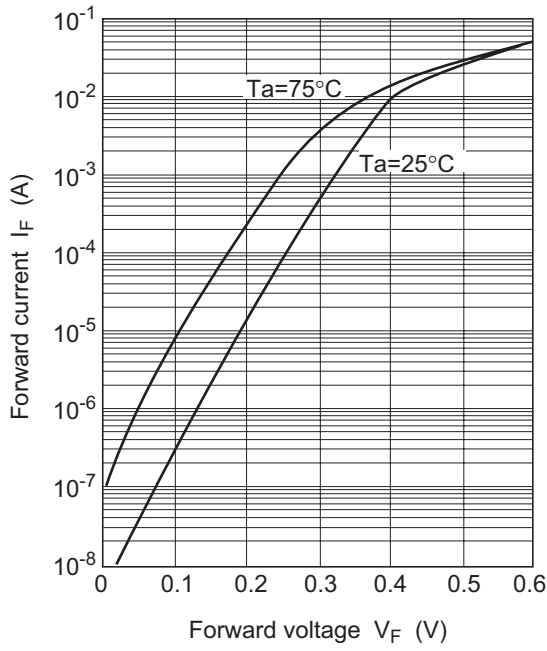


Fig.1 Forward current vs. Forward voltage

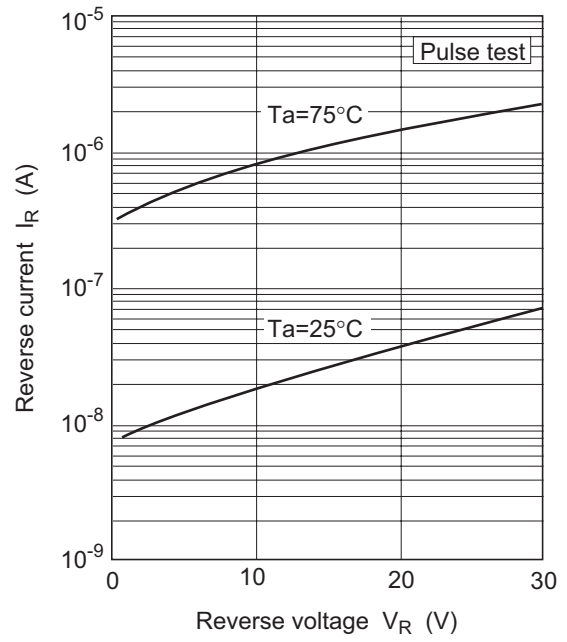


Fig.2 Reverse current vs. Reverse voltage

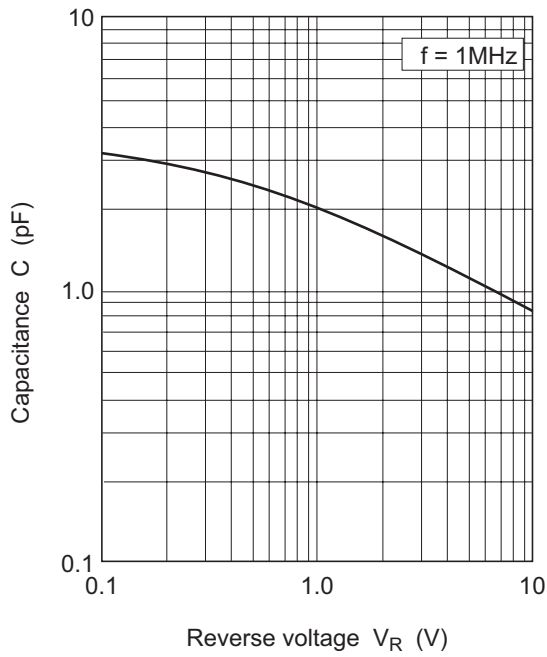


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions

Package Name EFP	JEITA Package Code —	RENESAS Code PXS F0002ZA-A	Previous Code EFP / EFPV	MASS[Typ.] 0.0007g
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Pattern of terminal position areas

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.44	0.47	0.50
b	0.25	0.30	0.35
c	0.08	0.13	0.18
D	0.55	0.60	0.65
E	0.75	0.80	0.85
H_E	0.95	1.00	1.05
ϕb	—	0.40	—
e_1	—	1.00	—

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Renesas Electronics Corporation

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