RJK60S4DPE

600V - 16A - SJ MOS FET
High Speed Power Switching

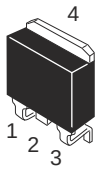
R07DS0733EJ0100
Rev.1.00
Apr 23, 2012

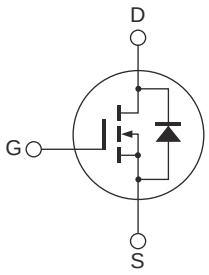
Features

- Superjunction MOSFET
- Low on-resistance
  $R_{DS(on)}$ = 0.23 Ω typ. (at I_D = 8 A, V_{GS} = 10 V, Ta = 25°C)
- High speed switching
  t_f = 21 ns typ. (at I_D = 8 A, V_{GS} = 10 V, R_L = 37.5 Ω, Rg = 10 Ω, Ta = 25°C)

Outline

RENESAS Package code: PRSS0004AE-B
(Package name: LDPAK(S)-(1))





1. Gate
2. Drain
3. Source
4. Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item		Symbol	Ratings	Unit
Drain to source voltage		V_{DSS}	600	V
Gate to source voltage		V_{GSS}	+30, –20	V
Drain current	Tc = 25°C	I_D Note1	16	A
	Tc = 100°C	I_D Note1	10.1	A
Drain peak current		$I_{D\ (pulse)}$ Note1	32	A
Body-drain diode reverse drain current		I_{DR} Note1	16	A
Body-drain diode reverse drain peak current		$I_{DR\ (pulse)}$ Note1	32	A
Channel dissipation		Pch Note2	104.1	W
Channel to case thermal impedance		θch-c	1.2	°C/W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	–55 to +150	°C

Notes: 1. Limited by Tch max.
2. Value at Tc = 25°C

Electrical Characteristics

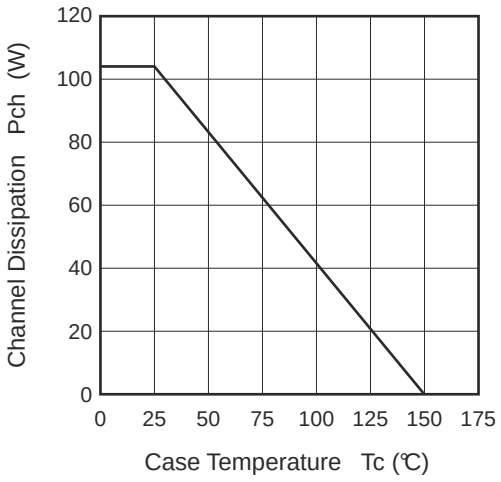
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	—	—	V	I_D = 10 mA, V_{GS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	1	mA	V_{DS} = 600 V, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±0.1	μA	V_{GS} = +30V, –20 V, V_{DS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	3	—	5	V	V_{DS} = 10 V, I_D = 1 mA
Static drain to source on state resistance	$R_{DS(on)}$	—	0.23	0.29	Ω	I_D = 8 A, V_{GS} = 10 V Note3
	$R_{DS(on)}$	—	0.57	—	Ω	Ta = 150°C I_D = 8 A, V_{GS} = 10 V Note3
Gate resistance	Rg	—	2.5	—	Ω	f = 1 MHz V_{DS} = 25 V, V_{GS} = 0
Input capacitance	Ciss	—	1020	—	pF	V_{DS} = 25 V V_{GS} = 0 f = 100kHz
Output capacitance	Coss	—	1440	—	pF	
Reverse transfer capacitance	Crss	—	5.5	—	pF	
Turn-on delay time	$t_{d(on)}$	—	26	—	ns	I_D = 8 A V_{GS} = 10 V R_L = 37.5 Ω Rg = 10 Ω Note3
Rise time	t_r	—	22	—	ns	
Turn-off delay time	$t_{d(off)}$	—	44	—	ns	
Fall time	t_f	—	21	—	ns	
Total gate charge	Qg	—	17.5	—	nC	V_{DD} = 480 V V_{GS} = 10 V I_D = 16 A Note3
Gate to source charge	Qgs	—	6	—	nC	
Gate to drain charge	Qgd	—	6	—	nC	
Body-drain diode forward voltage	V_{DF}	—	1.0	1.6	V	I_F = 16 A, V_{GS} = 0 Note3
Body-drain diode reverse recovery time	t_{rr}	—	380	—	ns	I_F = 16 A V_{GS} = 0 di_F/dt = 100 A/μs Note3
Body-drain diode reverse recovery current	I_{rr}	—	23	—	A	
Body-drain diode reverse recovery charge	Q_{rr}	—	4.9	—	μC	

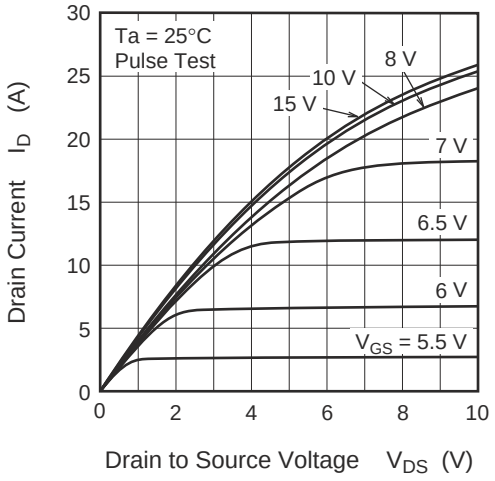
Notes: 3. Pulse test

Main Characteristics

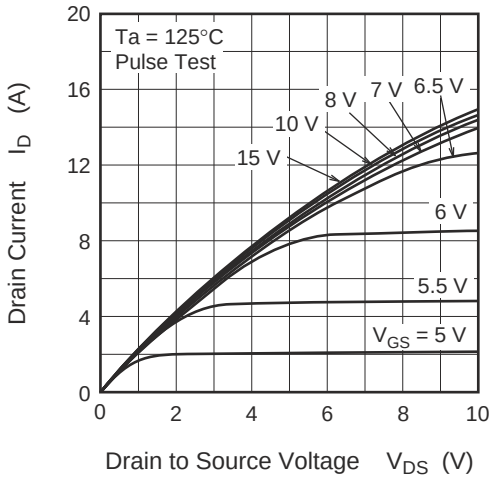
Channel Dissipation vs. Case Temperature



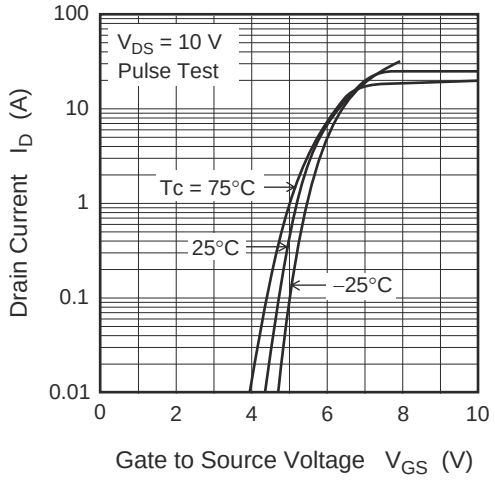
Typical Output Characteristics



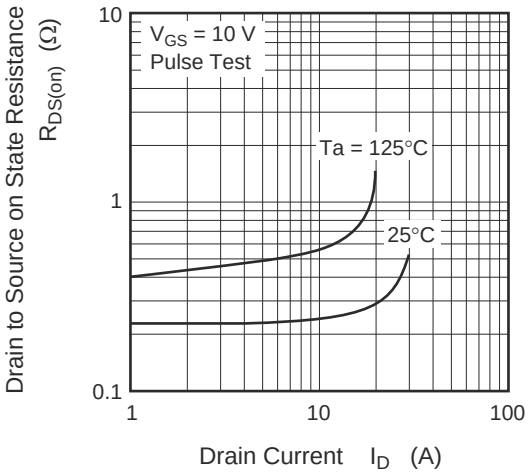
Typical Output Characteristics



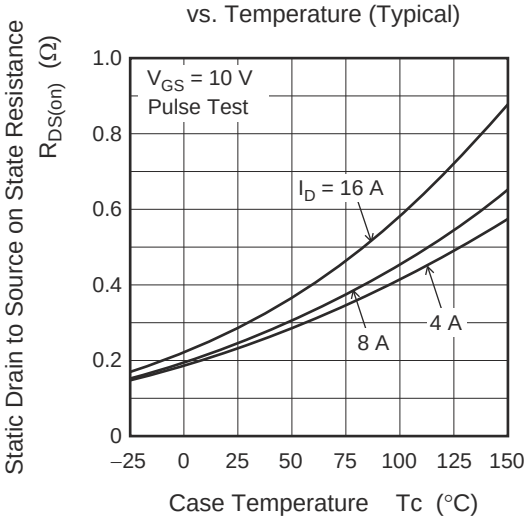
Typical Transfer Characteristics



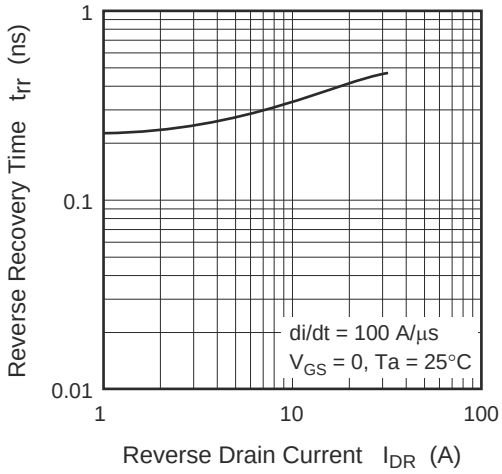
Static Drain to Source on State Resistance vs. Drain Current (Typical)



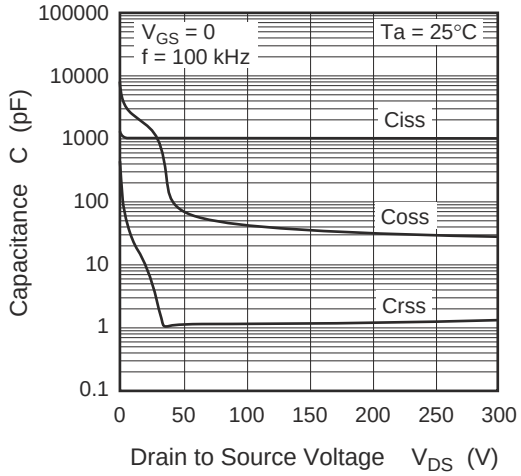
Static Drain to Source on State Resistance vs. Temperature (Typical)



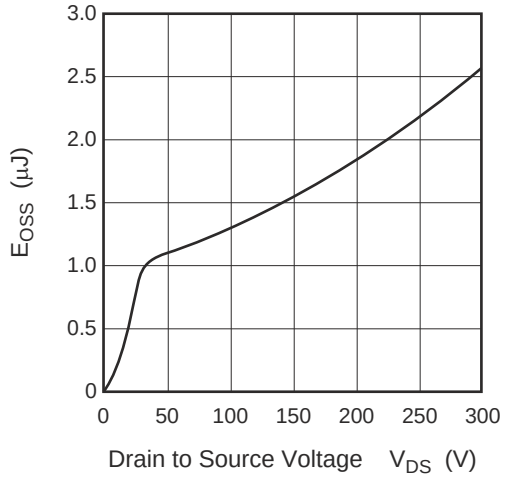
Body-Drain Diode Reverse Recovery Time (Typical)



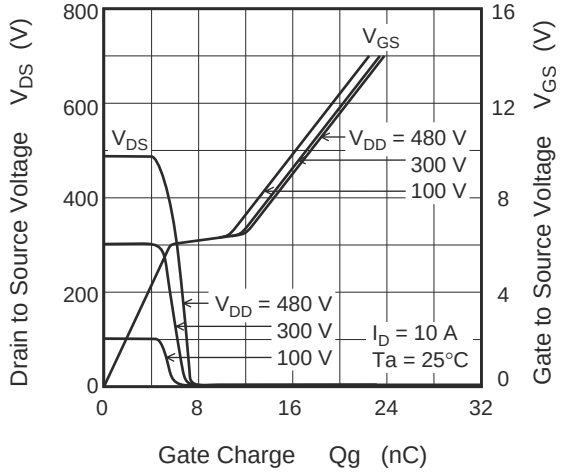
Typical Capacitance vs. Drain to Source Voltage



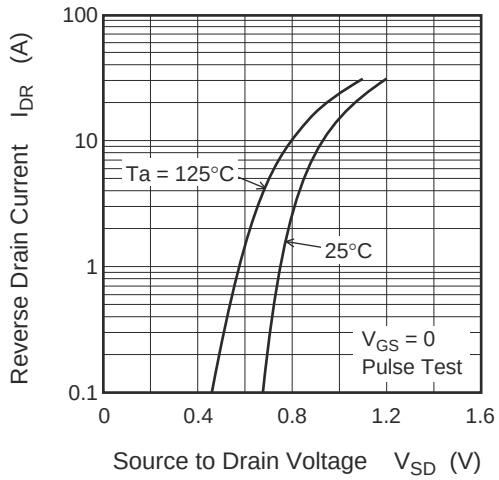
C_{OSS} Stored Energy (Typical)



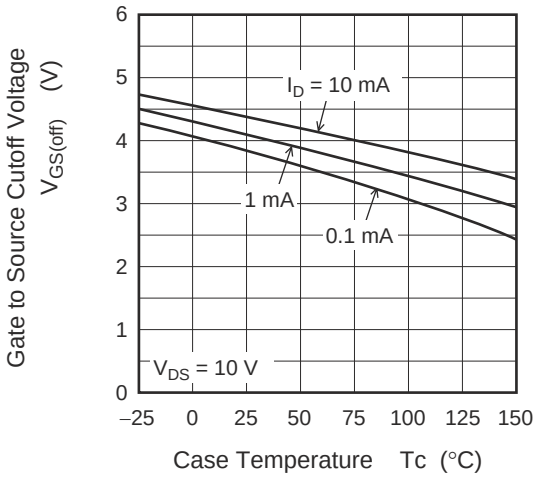
Dynamic Input Characteristics (Typical)



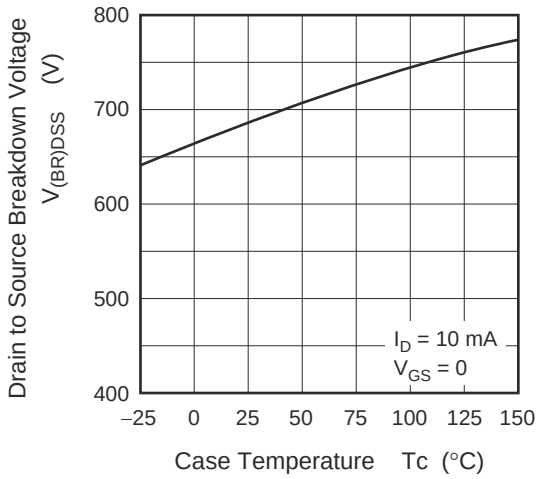
Reverse Drain Current vs. Source to Drain Voltage (Typical)



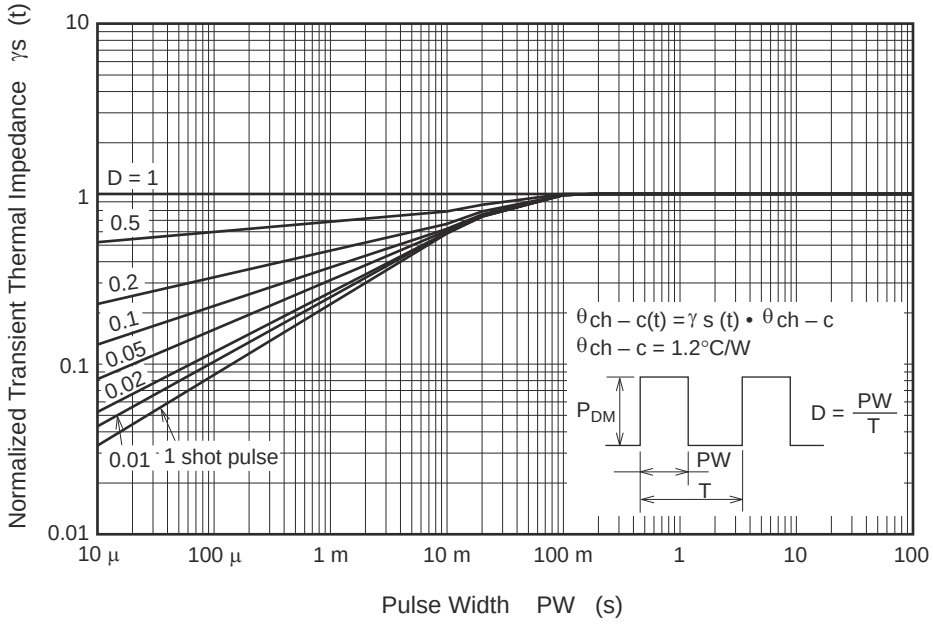
Gate to Source Cutoff Voltage vs. Case Temperature (Typical)



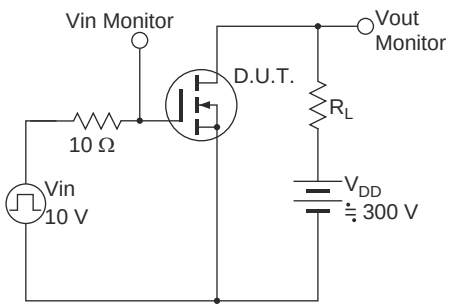
Drain to Source Breakdown Voltage
vs. Case Temperature (Typical)



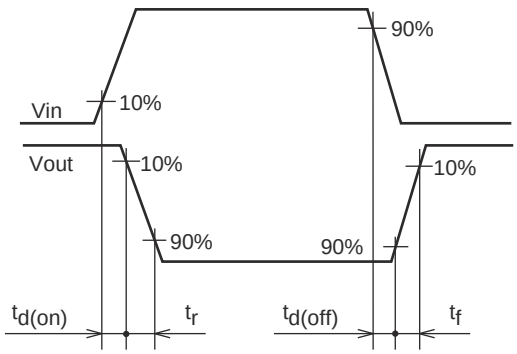
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



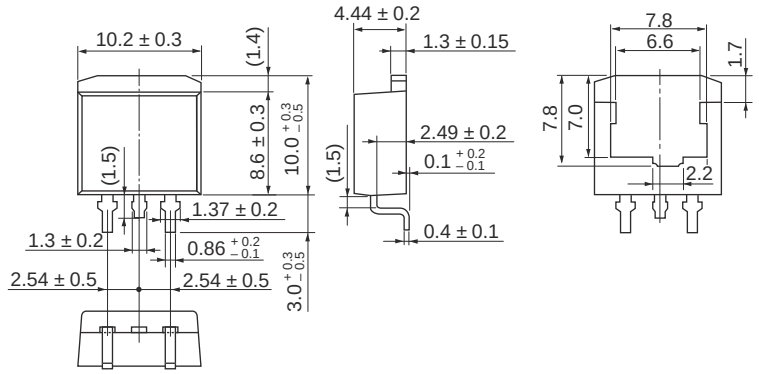
Waveform



Package Dimension

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDPAK(S)-(1)	SC-83	PRSS0004AE-B	LDPAK(S)-(1) / LDPAK(S)-(1)V	1.30g

Unit: mm



Ordering Information

Orderable Part No.	Quantity	Shipping Container
RJK60S4DPE-00#J3	1000 pcs	Taping

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