

RJK60S3DPE

600V - 12A - SJ MOS FET
High Speed Power Switching

R07DS0732EJ0100
Rev.1.00
Apr 23, 2012

Features

- Superjunction MOSFET
- Low on-resistance
 $R_{DS(on)} = 0.35 \Omega$ typ. (at $I_D = 6 A$, $V_{GS} = 10 V$, $T_a = 25^\circ C$)
- High speed switching
 $t_f = 21 ns$ typ. (at $I_D = 6 A$, $V_{GS} = 10 V$, $R_L = 50 \Omega$, $R_g = 10 \Omega$, $T_a = 25^\circ C$)

Outline

RENESAS Package code: PRSS0004AE-B
(Package name: LDKPAK(S)-(1))

Absolute Maximum Ratings

($T_a = 25^\circ C$)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V_{DSS}	600	V	
Gate to source voltage	V_{GSS}	+30, -20	V	
Drain current	$T_c = 25^\circ C$	I_D^{Note1}	12.0	A
	$T_c = 100^\circ C$	I_D^{Note1}	7.6	A
Drain peak current	$I_{D(pulse)}^{Note1}$	24	A	
Body-drain diode reverse drain current	I_{DR}^{Note1}	12	A	
Body-drain diode reverse drain peak current	$I_{DR(pulse)}^{Note1}$	24	A	
Channel dissipation	P_{ch}^{Note2}	83.3	W	
Channel to case thermal impedance	θ_{ch-c}	1.5	$^\circ C/W$	
Channel temperature	T_{ch}	150	$^\circ C$	
Storage temperature	T_{stg}	-55 to +150	$^\circ C$	

- Notes: 1. Limited by T_{ch} max.
2. Value at $T_c = 25^\circ C$

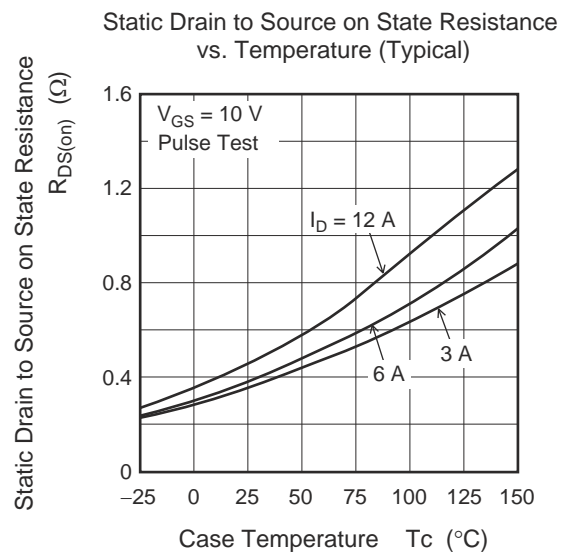
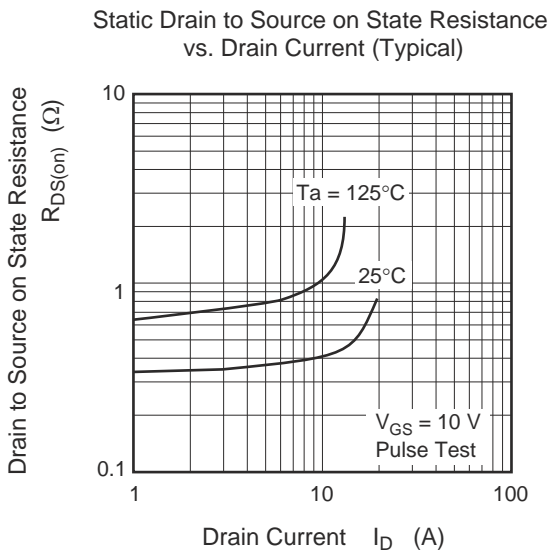
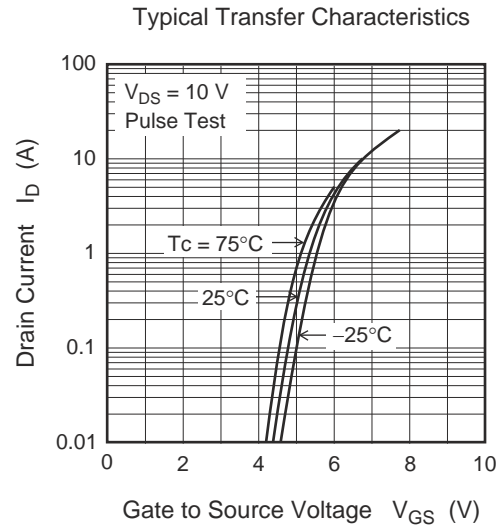
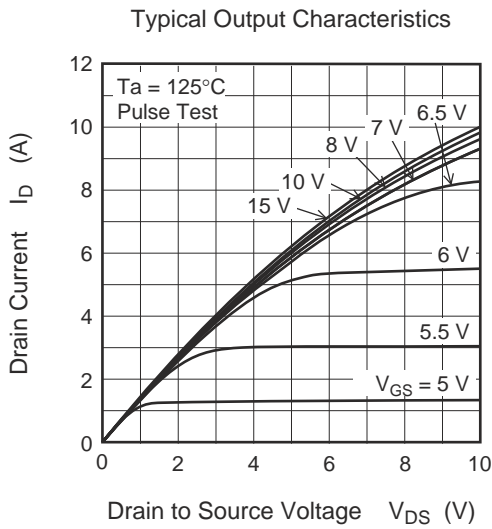
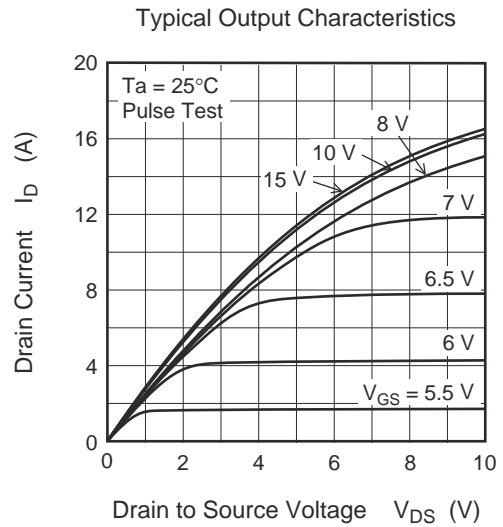
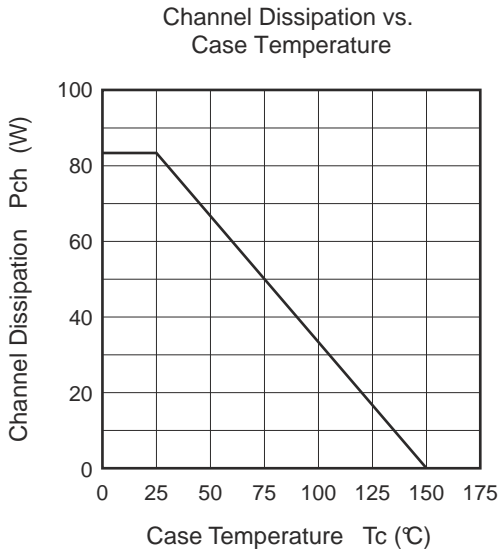
Electrical Characteristics

(Ta = 25°C)

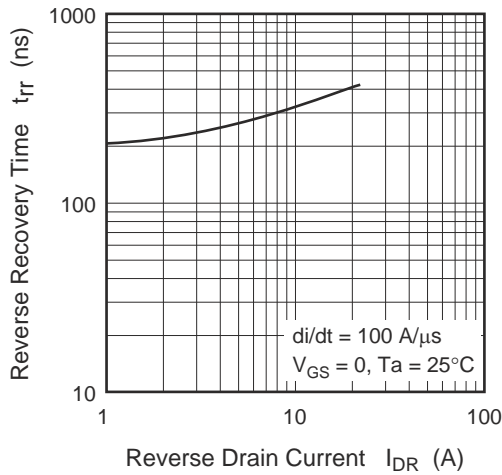
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	1	mA	$V_{DS} = 600 \text{ V}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = +30\text{V}$, -20 V , $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	3	—	5	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.35	0.44	Ω	$I_D = 6 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note3}
	$R_{DS(on)}$	—	0.87	—	Ω	Ta = 150°C $I_D = 6 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note3}
Gate resistance	Rg	—	2.5	—	Ω	f = 1 MHz $V_{DS} = 25 \text{ V}$, $V_{GS} = 0$
Input capacitance	Ciss	—	710	—	pF	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0$ f = 100 kHz
Output capacitance	Coss	—	1040	—	pF	
Reverse transfer capacitance	Crss	—	3.5	—	pF	
Turn-on delay time	$t_{d(on)}$	—	23	—	ns	$I_D = 6 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 50 \Omega$ Rg = 10 Ω ^{Note3}
Rise time	t_r	—	20	—	ns	
Turn-off delay time	$t_{d(off)}$	—	41	—	ns	
Fall time	t_f	—	21	—	ns	
Total gate charge	Qg	—	13	—	nC	$V_{DD} = 480 \text{ V}$
Gate to source charge	Qgs	—	4.6	—	nC	$V_{GS} = 10 \text{ V}$
Gate to drain charge	Qgd	—	5.7	—	nC	$I_D = 12 \text{ A}$ ^{Note3}
Body-drain diode forward voltage	V_{DF}	—	1.0	1.6	V	$I_F = 12 \text{ A}$, $V_{GS} = 0$ ^{Note3}
Body-drain diode reverse recovery time	t_{rr}	—	340	—	ns	$I_F = 12 \text{ A}$ $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$ ^{Note3}
Body-drain diode reverse recovery current	I_{rr}	—	21	—	A	
Body-drain diode reverse recovery charge	Q_{rr}	—	3.9	—	μC	

Notes: 3. Pulse test

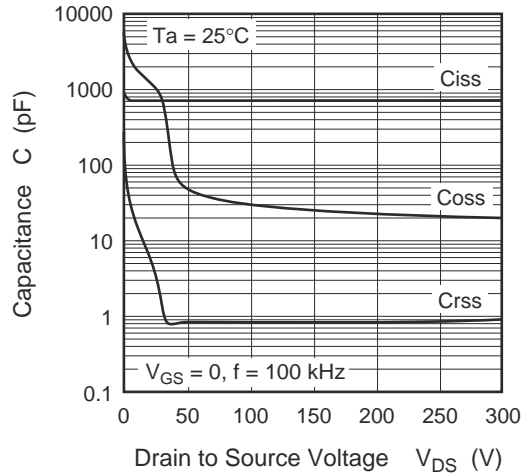
Main Characteristics



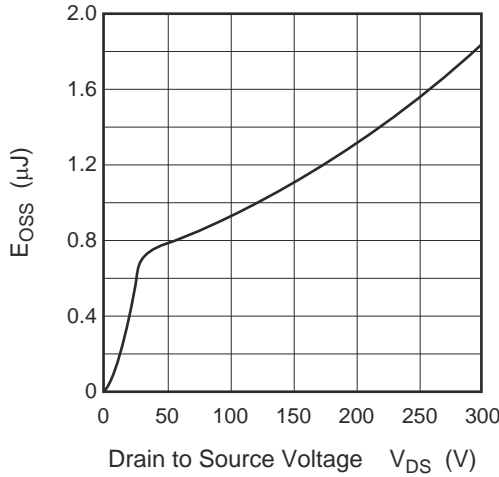
Body-Drain Diode Reverse Recovery Time (Typical)



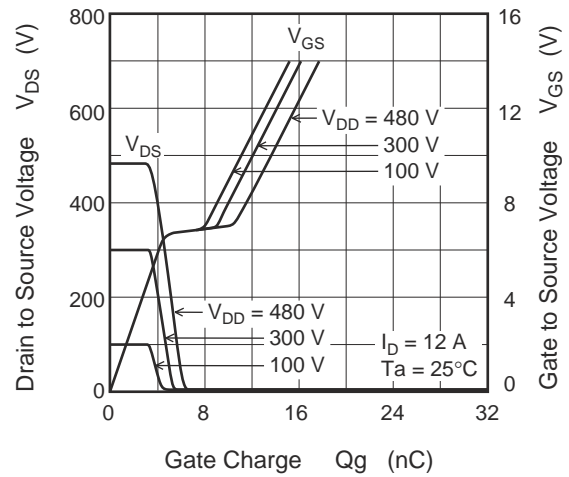
Typical Capacitance vs. Drain to Source Voltage



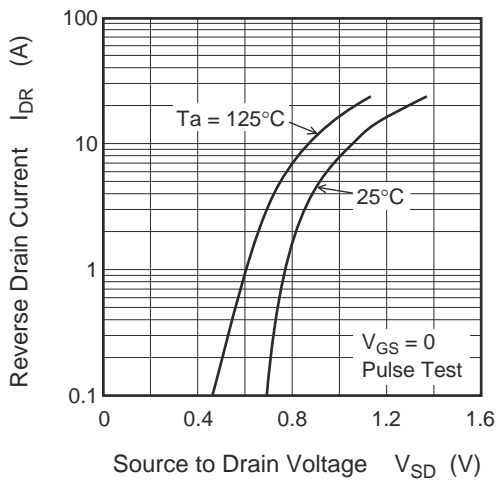
C_{OSS} Stored Energy (Typical)



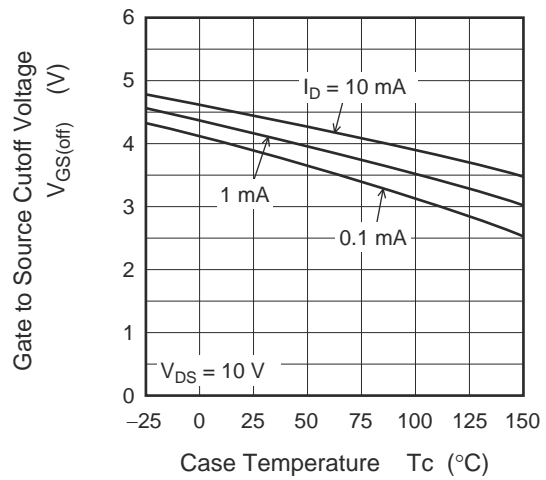
Dynamic Input Characteristics (Typical)



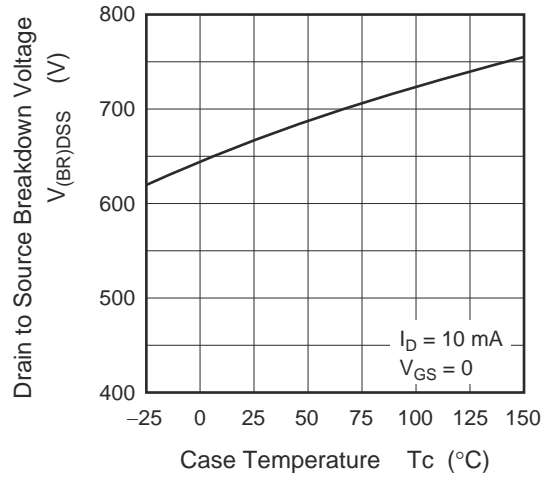
Reverse Drain Current vs. Source to Drain Voltage (Typical)



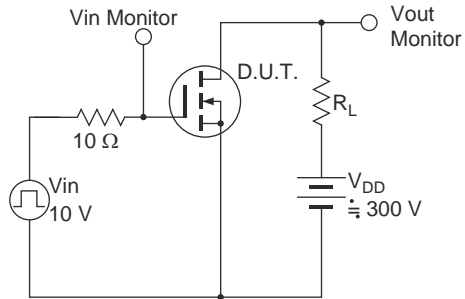
Gate to Source Cutoff Voltage vs. Case Temperature (Typical)



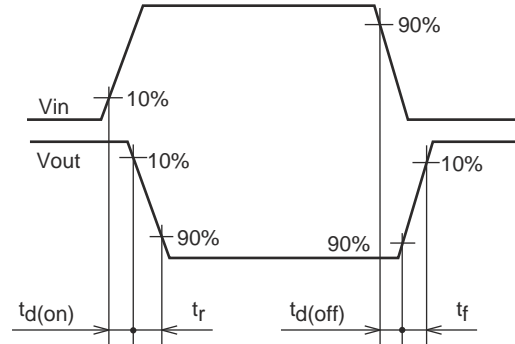
Drain to Source Breakdown Voltage vs. Case Temperature (Typical)



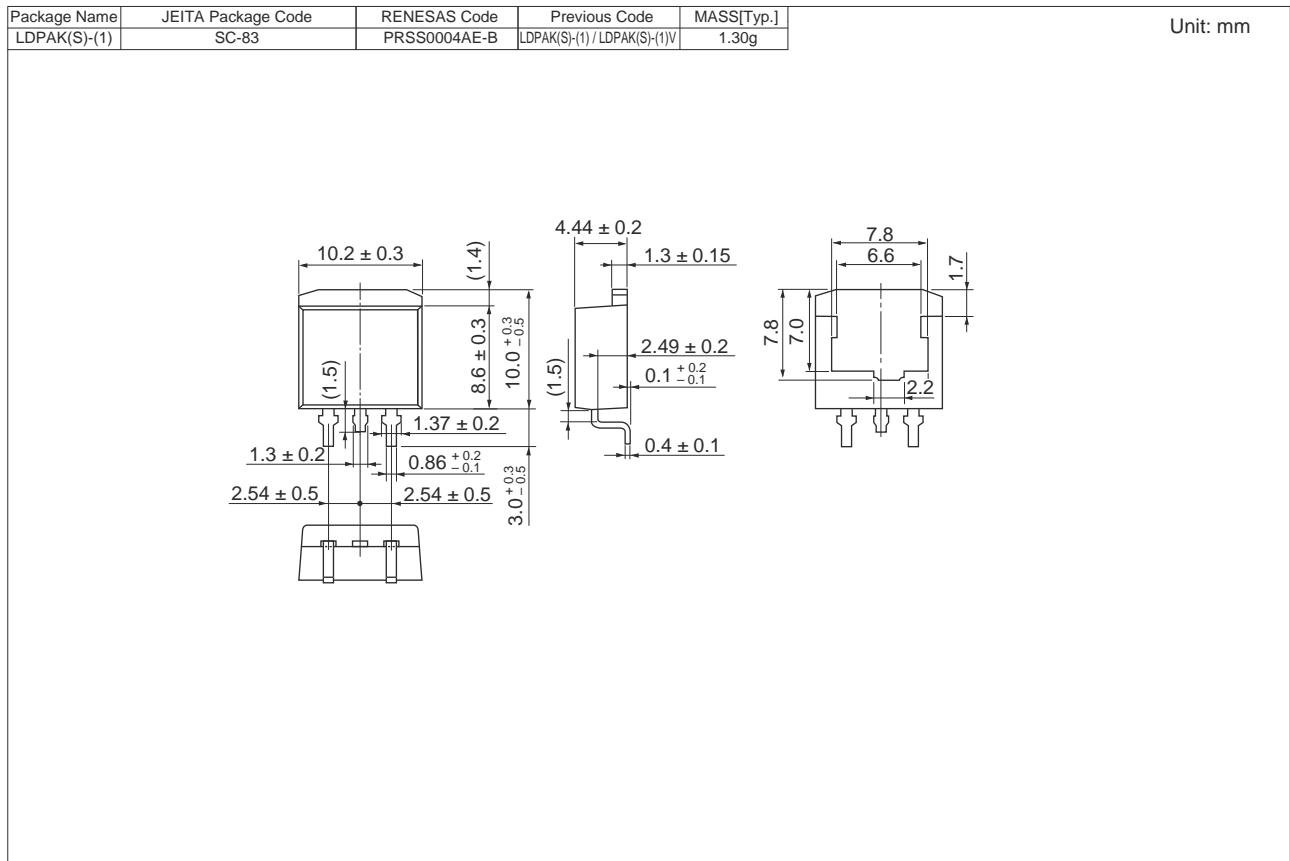
Switching Time Test Circuit



Waveform



Package Dimension



Ordering Information

Orderable Part No.	Quantity	Shipping Container
RJK60S3DPE-00#J3	1000 pcs	Taping

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