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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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R2A20050ANS

Lithium-Ion Battery Charger Control IC

REJ03F0287-0006

Rev.0.06

May 12, 2008

Description

R2S20050ANS is a semiconductor integrated circuit designed for Lithium-ion battery charger control IC.

Built-in constant current and constant voltage control circuit allows for charging Lithium-ion battery.

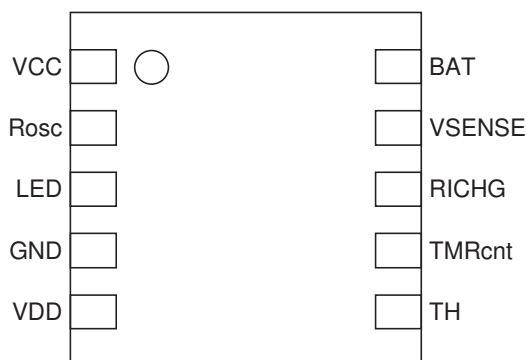
Features

- Available for 4.2 V battery
- Built-in high accuracy reference voltage (charge voltage) $4.20 \text{ V} \pm 30 \text{ mV}$ (at $T_a = 25^\circ\text{C}$)
- Constant current and constant voltage control function
- Battery connection detection function
- Built-in adaptor detection function
- Built-in recharge function
- Built-in thermistor interface for battery temperature detection
- Built-in chip temperature detection function
- Substantial protection functions
 - Safety timer
 - Overdischarge protection
 - Overvoltage protection
 - Charge prohibition protection for overvoltage and overdischarge battery.
 - Thermal shutdown function
 - Erroneous detection protection
- Built-in LED display function

Application

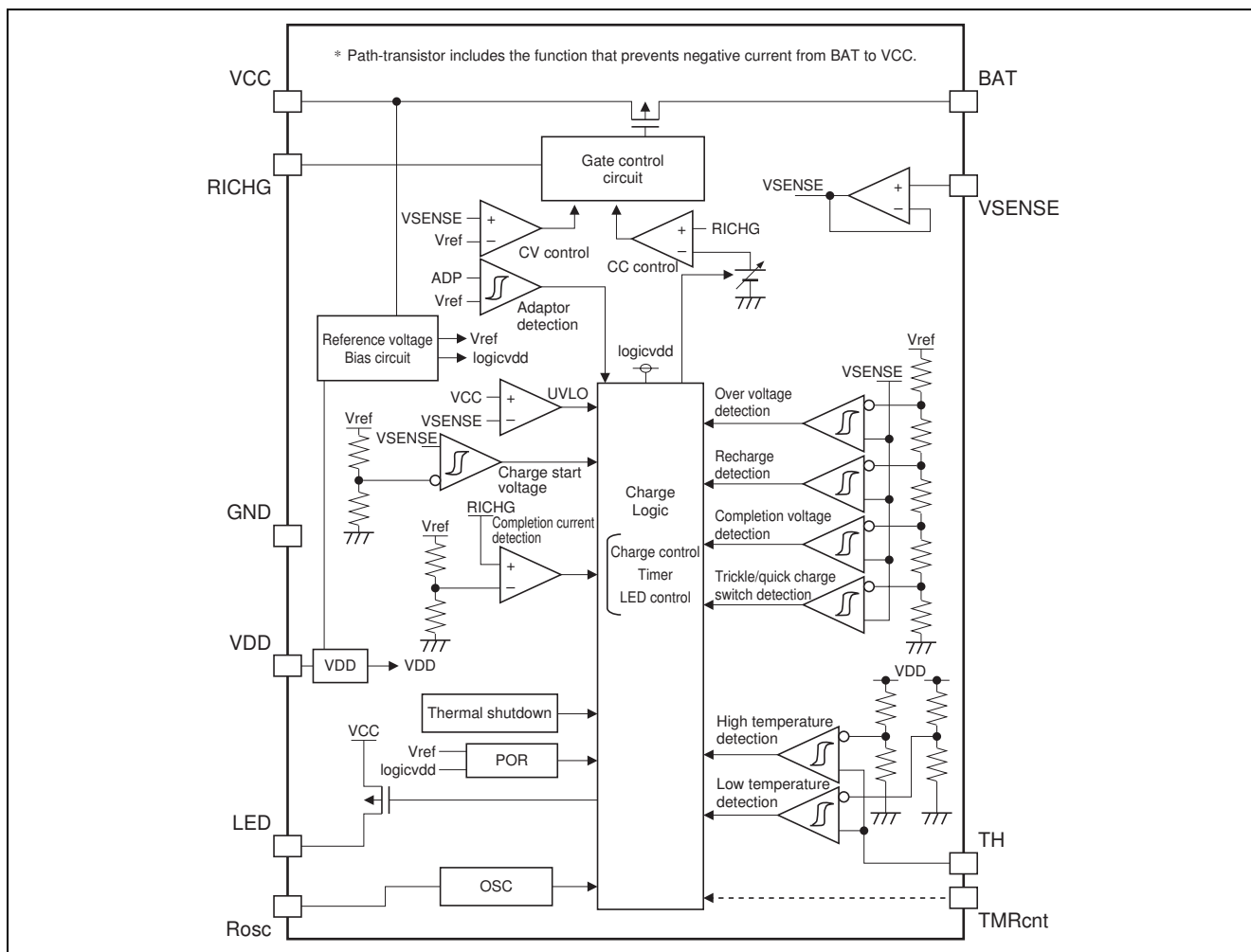
For DSC, cellular phone, PDA, portable memory audio player, handheld game machine, and others.

Pin Arrangement



(Top view)
Package: 10pin DFN

Block Diagram



Pin Description

No.	Pin Name	I/O	Function	Protection Diode	
				Positive	Negative
1	VCC	I	Power supply & charge transistor input	—	GND
2	Rosc	O	Oscillation frequency adjustment	VCC	GND
3	LED	O	LED drive (under charge : ON [H])	VCC	GND
4	GND	O	Ground	—	—
5	VDD	O	Reference voltage terminal for battery temperature detection	VCC	GND
6	TH	I	Battery temperature detection terminal (Thermistor connection terminal)	VCC	GND
7	TMRcnt	I	Timer ON/OFF control H: Timer stop L or OPEN: Timer permission	—	GND
8	RICHG	I	For setting the charge current value	VCC	GND
9	VSENCE	I	Battery voltage detection & constant voltage charge control	—	GND
10	BAT	O	Charge transistor output (charge current output)	—	GND

Absolute Maximum Ratings

(Ta = 25°C, VCC = 5 V, unless otherwise specified.)

Item	Symbol	Ratings	Unit	Remark
Maximum applied voltage	Vmax	−0.3 to 6.5	V	All terminal
Allowable power dissipation *	Pd	2250	mW	When IC is mounted on a board in the conditions below. This value will change depending upon the material of a board, wind speed, etc.
Operating temperature range	Topr	−20 to +75	°C	
Storage temperature range	Tstg	−40 to +125	°C	
Output current	Iout	1.5	A	
LED input current	Iled	20	mA	

Note: * Board material: FR-4
 Board size: 40 × 40 × 1.6 mm
 Cu share: 50%
 Wind velocity: 0m/sec.
 Ambient temperature: 25°C

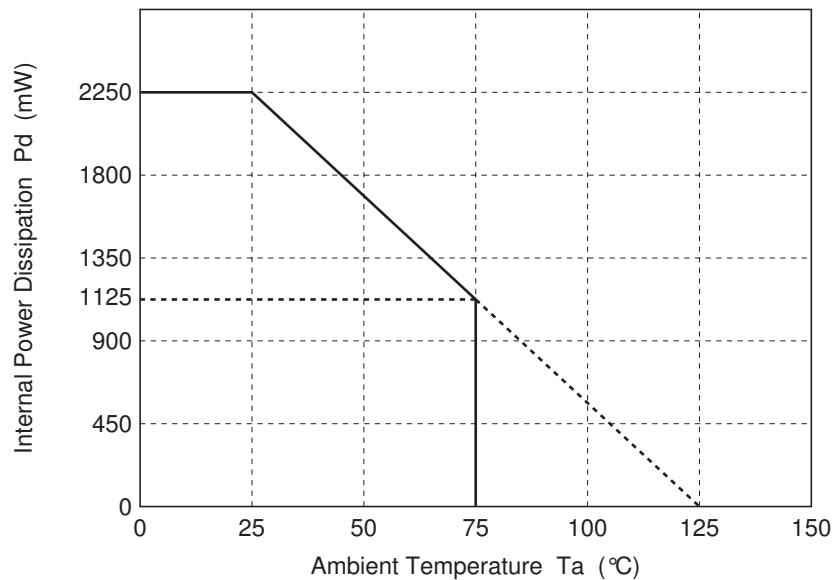


Figure 1 Thermal Derating Curve

Electrical Characteristics

(Ta = 25°C, VCC = 5 V, unless otherwise specified.)

Block	Item	Symbol	Rated value			Unit	Condition
			Min	Typ	Max		
All	Power supply voltage range	Vcc	4.0	5.0	5.8	V	
	Circuit current	Icc	—	3.0	4.5	mA	Under charge (charge current = 500mA)
	Adaptor detection voltage	Vadp	3.6	3.8	4.0	V	
	Battery discharge current *	Ibatout	—	1.0	2.0	μA	VBAT=1.1~4.2V:When adaptor is not connected or charge is completed (ADP=4.8V).
	VSENSE discharge current 1	Idischg 1	—	10	20	μA	VBAT=3.2V :In charge error mode
	Reference voltage for battery temperature detection	VDD	2.4	2.5	2.6	V	VDD terminal voltage
Battery voltage detection block	Charge start voltage (Overdischarge protection)	Vstart	1.0	1.1	1.2	V	Battery connection detection is shared. (Charge is not done to the battery under charge start voltage.)
	Charge start voltage hysteresis	Vstarthys	50	100	150	mV	Battery disconnection detection is shared.
	Quick charge start voltage	Vqchg	2.9	3.0	3.1	V	Detected voltage at battery voltage rise
	Quick charge start voltage hysteresis	Vqchghys	40	80	120	mV	Detected voltage at battery voltage drop
	Recharge start voltage	Vrechg	3.87	3.97	4.07	V	
	Charge control voltage	Vchg	4.17	4.20	4.23	V	
	Overvoltage detection voltage	Vov	4.27	4.35	4.43	V	VADP=Vov+70mV
Battery temperature detection block	Charge stop temperature detection voltage (Low temp.)	VthSL	VDD× 0.6966	VDD× 0.7142	VDD× 0.7313	V	Detected voltage at battery temperature falling (Lower temp. side): worth 2°C.
	Charge re-start temperature detection voltage (Low temp.)	VthRL	VDD× 0.6787	VDD× 0.6966	VDD× 0.7141	V	Detected voltage at battery temperature rising (Lower temp. side): worth 4°C.
	Charge stop temperature detection voltage (High temp.)	VthSH	VDD× 0.3296	VDD× 0.3448	VDD× 0.3604	V	Detected voltage at battery temperature rising (Higher temp. side): worth 43°C.
	Charge re-start temperature detection voltage (High temp.)	VthRH	VDD× 0.3684	VDD× 0.3847	VDD× 0.4014	V	Detected voltage at battery temperature falling (Higher temp. side): worth 38°C.
Charge current detection block	Quick charge current 1 (1.0C)	Irapchg1	520	560	600	mA	RICHG=2.32kΩ, VBAT=3.6V
	Trickle charge control current 1(0.1C)	Iprechg1	40	56	72	mA	RICHG=2.32kΩ, VBAT=2.6V
	Forced charge current (0.3C)	Istart	120	170	220	mA	RICHG=2.32kΩ
	Charge completion current	Ic	40	56	72	mA	RICHG=2.32kΩ, VBAT≥4.0V
Timer circuit block	Oscillation frequency	Foc	57.6	64	70.4	kHz	Rosc=100kΩ
	LED blinking cycle	Fled	0.922	1.024	1.126	sec	Rosc=100kΩ, in charge error mode (design guarantee)
	Quick charge timer	Tchg	270	300	330	min	Time from quick charge start to charge completion judgment (Rosc=100kΩ)
	Trickle charge timer	Tdchg	54	60	66	min	Time from trickle charge start to quick charge start (Rosc=100kΩ)
LED control block	LED terminal output "H" voltage	Vledh	Vcc-0.6	—	—	V	Isource=10mA
	LED terminal leakage current	Iledleak	-1	—	1	μA	Vled=5V
Others	TMRcnt terminal "L" voltage	Vtmrl	—	—	0.5	V	
	TMRcnt terminal "H" voltage	Vtmrh	2.0	—	—	V	
	TMRcnt terminal input current	Itmr	—	—	10	μA	Vin=5.0V
	Chip temperature detection	Ttreg	83	93	103	°C	Chip temperature (design guarantee)
	Chip temperature detection hysteresis	Ttreghys	5	10	15	°C	For releasing the thermal regulation
	Chip temperature reset detection	Ttregrst	110	120	130	°C	Chip temperature (design guarantee)
	Thermal shutdown temperature	Tsd	—	150	—	°C	Chip temperature (design guarantee)
	Charge Tr ON resistance	Ron	—	0.45	0.6	Ω	Contains package lead resistance

Each Detection Time (Rosc = 100 kΩ)

Item	Symbol	Reference value
Adaptor detection time 1 (when rising)	Tadp1	8 msec × 4 times coincidence = 32 msec
Adaptor detection time 2 (when falling)	Tadp2	8 msec × 4 times coincidence = 32 msec
VCC/BAT voltage comparison	—	Non detection time (judged immediately)
Battery connect detection time	Tcon	32 msec × 4 times coincidence = 128 msec
Quick charge start voltage detection time	Tqstart	32 msec × 4 times coincidence = 128 msec
Charge completion current detection time	Tifc	64 msec × 4 times coincidence = 256 msec
Charge completion voltage detection time *	Tvfc	32 msec × 4 times coincidence = 128 msec
Recharge start voltage detection time	Trechg	64 msec × 4 times coincidence = 256 msec
Battery overvoltage detection time	Tov	32 msec × 4 times coincidence = 128 msec
Forced charge time	Tistart	512 msec
OFF time after forced charge	Toff	128 msec
Battery temperature detection time	Tpro	32 msec × 4 times coincidence = 128 msec

Note: * In the case of Battery voltage ≥ 2.9 V (completed normally)

Reference: Oscillation Frequency Setting (subject to change)

Rosc External Resistor (kΩ)	Oscillation Frequency (kHz)
1,000	7.5
510	14.1
300	23.5
200	33.9
100	64
51	109.3

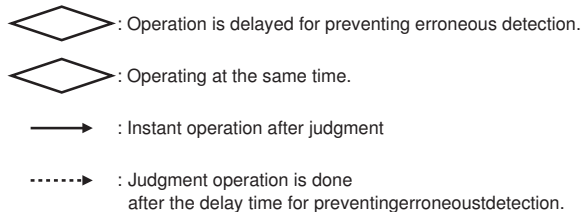
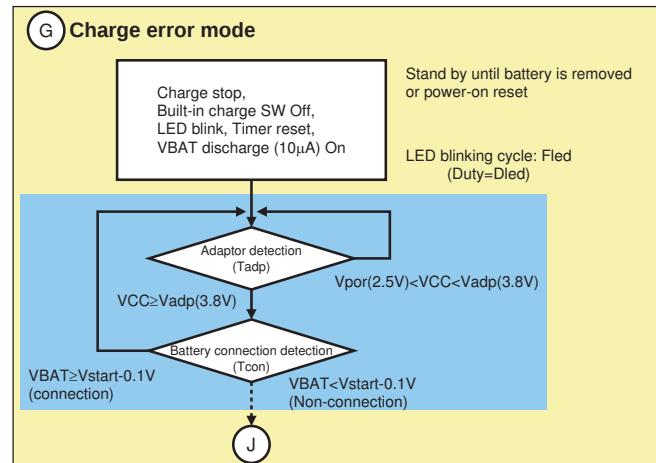
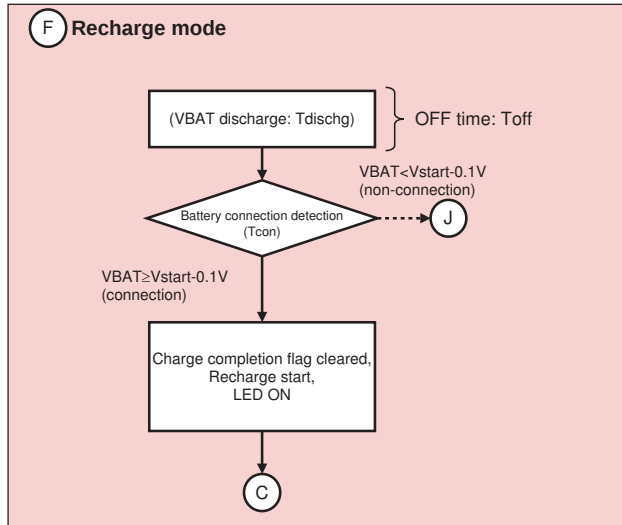
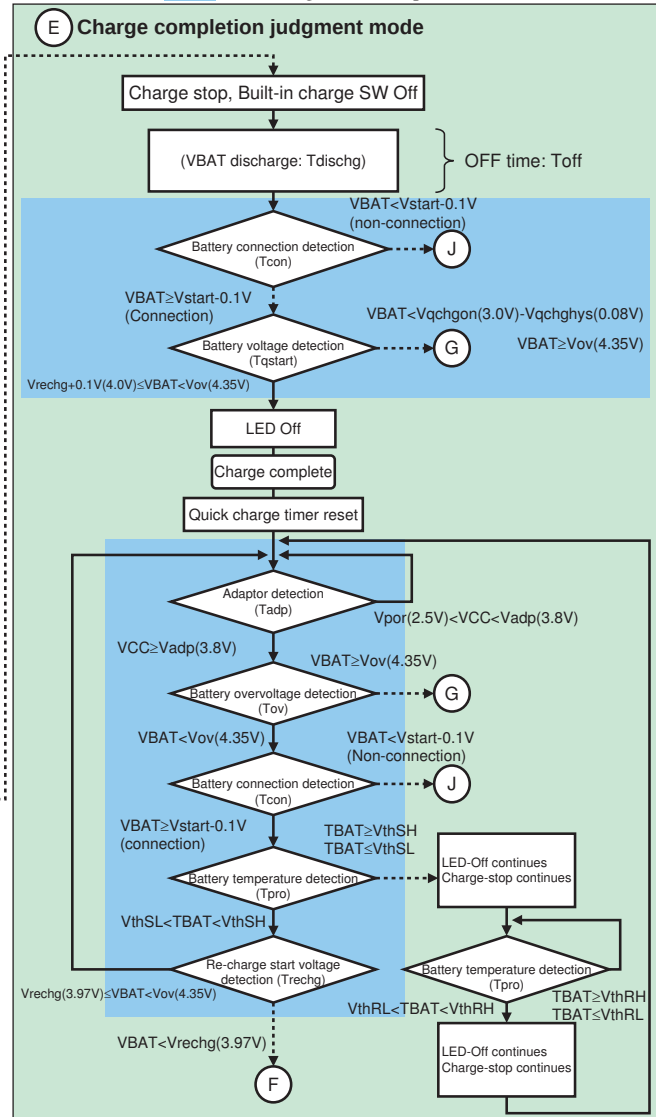
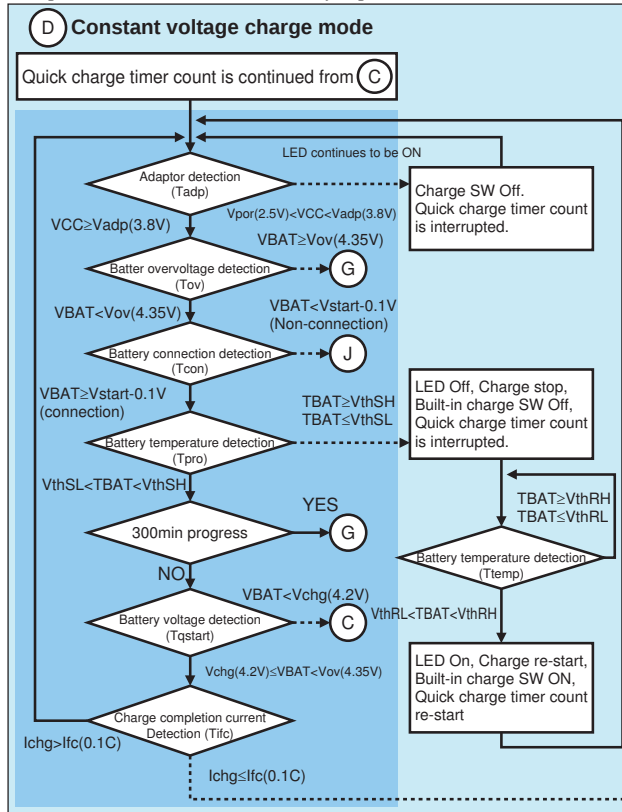
Note: Each timer expiration time and detection time will also change with the set-up frequency.
 (Each time changes proportionally to oscillation frequency.)
 Each accuracy is $\pm 15\%$ (reference value) except when Rosc = 100 kΩ.
 When Rosc terminal is short-circuited, it operates with fosc ≈ 650 kHz.

Battery overvoltage detection and thermal shutdown are always operative after power-on reset.

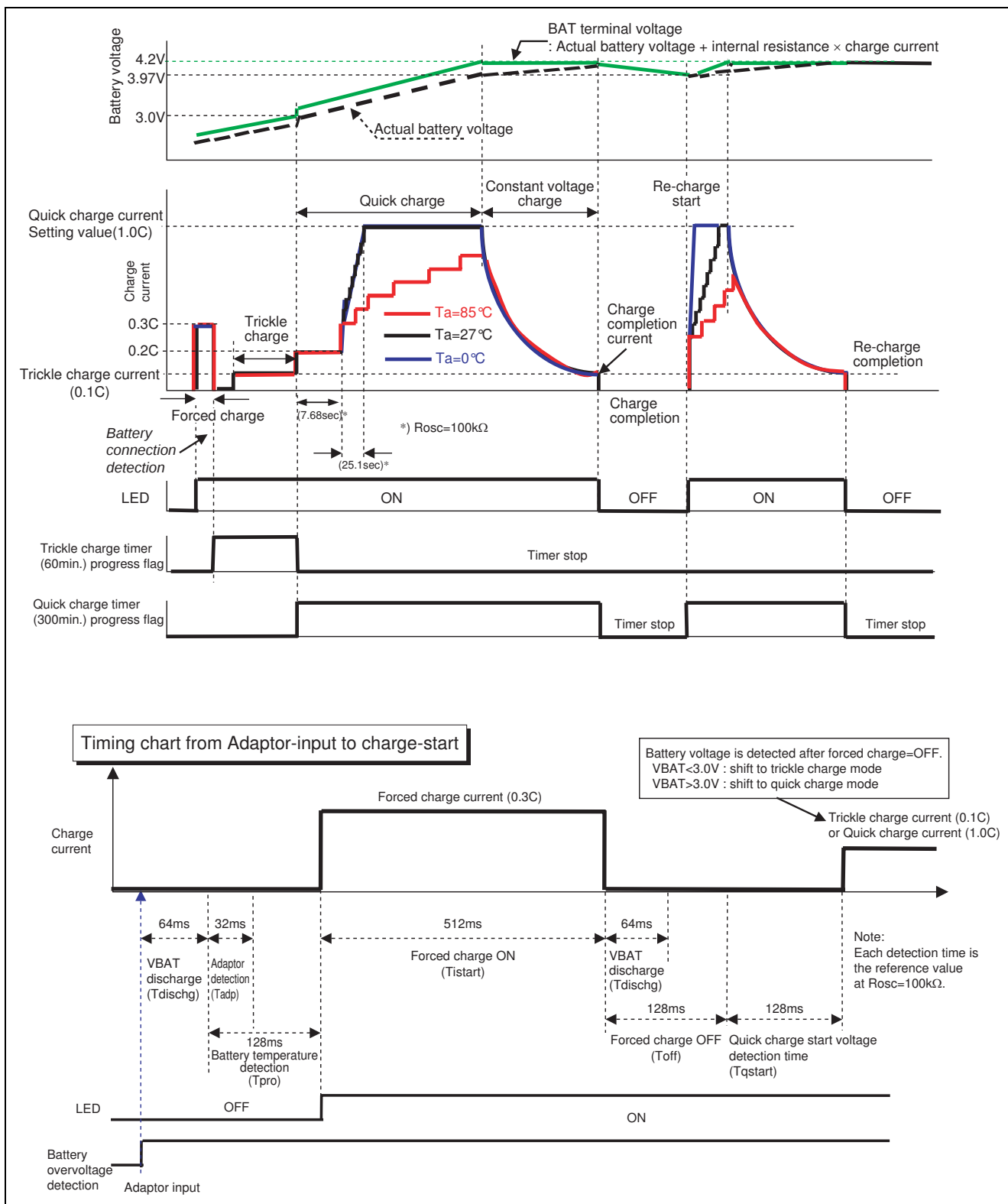
Adapter detection is always operative after BAT terminal discharge after power-on reset.

Comparison of VCC and BAT is always operative in all state.

: Detecting functions operates at once in each mode.



Timing Chart



Operational Description

Adaptor Detection

Adaptor connection is detected.

- $VCC \geq 3.8\text{ V}$: Adapter connection is detected to be connected. (Charge permission)
- $VCC \leq 2.5\text{ V}$: Adapter connection is detected to be disconnected. (Mode is reset and returns to start.)

Each detecting operation during adaptor detection is as follows.

- Battery connection/disconnection: count valid (continues to detect without suspending.)
- Quick charge start voltage detection: count suspended
- Charge completion current/voltage detection: count suspended
- Recharge start voltage detection: count suspended
- Chip temperature detection: count reset

(When adaptor is detected in quick charge mode, IC starts to charge at 0.2C. ($VCC \geq V_{adp}$))

During below charge Off time, IC continues to count even if the adaptor is detected.

- Forced charge Off time after forced charge completed.
- Charge Off time from quick charge to charge completion.
- Charge Off time from charge completion to quick charge.

When adaptor is detected in forced charge mode, forced charge mode is reset and it is restarted after released adaptor detection.

Battery Connect Detection

When adapter is detected, forced charge (0.3C) is started for canceling the cutoff state of battery due to overdischarge protection.

Then, forced charge is stopped and battery connection is detected.

Battery connection detection has a hysteresis characteristic and the battery disconnection judgment voltage after battery connection detection is battery connection detection voltage -100 mV .

1) Battery connect detection

- Battery voltage (BAT) $\geq 1.1\text{ V}$: Battery connection is detected to be connected, and charge is started.
- Battery voltage (BAT) $\leq 1.1\text{ V}$: Battery connection is detected to be disconnected (overdischarge protection), and charge stops.

2) Battery disconnect detection (under charge)

- When the battery is disconnected under charge, the state shifts to CV control mode (charge current does not flow).

Therefore, the state shifts to charge completion judgment mode.

In this time, built-in charge transistor is to be OFF, and BAT terminal voltage falls.

When BAT terminal $< 1.0\text{ V}$, battery connection is detected to be disconnected, and charge stops.

And the state shifts to battery non-connection mode. (overdischarge protection)

3) Battery disconnect detection (when charge is stopped)

- In this condition, when the battery connection is detected to be disconnected, BAT terminal voltage falls.

Because built-in charge transistor is already OFF.

When BAT terminal $< 1.0\text{ V}$, battery connection is detected to be disconnected, and the state shifts to battery non-connection mode. (overdischarge protection)

Trickle Charge (0.1C constant current charge)

After adaptor connection judgment and battery connection judgment, trickle charge is started when battery voltage is as follows.

$$1.1 \text{ V} \leq \text{battery voltage} \leq 3.0 \text{ V}$$

During the trickle charge mode, VSENSE terminal is discharged by 300 μA current.

- When battery voltage $\geq 3.0 \text{ V}$, the state shifts to quick charge mode.
- Trickle charge timer operates simultaneously with trickle charge start.
- After trickle charge timer expires, when battery voltage $< 3.0 \text{ V}$, the state shifts to charge error mode.
- When adaptor voltage is battery voltage or less, charge stops.
- When battery voltage $\geq 4.35 \text{ V}$ (overvoltage), the state shifts to charge error mode.

Quick Charge (1.0C constant current charge)

Quick charge is started when battery voltage $\geq 3.0 \text{ V}$.

During the quick charge mode, VSENSE terminal is discharged by 300 μA current.

- When battery voltage is detected to be 4.2 V, the state shifts to CV charge mode.
- Quick charge timer operates simultaneously with quick charge start.
- After quick charge timer expires, when the state is not in charge completion mode, it shifts to a charge error mode.
- When battery voltage is detected to be less than 2.92 V, the state shifts to trickle charge mode.
- When adaptor voltage is detected to be battery voltage or less, charge stops.
- When battery voltage is detected to be 4.35 V or more, the state shifts to charge error mode.

During quick charge, junction temperature is monitored to limit charge current value between 0.2C-1.0C so that the junction temperature may not exceed T_{reg} .

$$\text{Charge current } 1.0\text{C} = [\text{Reference voltage (1.495 V)} / \text{RICHG}] \times (820 \text{ to } 920) * (\text{mA})$$

Note: * The coefficient changes by the charging current.

- Reference: Charge Current Setting (subject to change)

RICHG Resistor (k Ω)	Quick Charge Current Setting (1.0C) (mA)
1.30	1000
1.40	900
1.59	800
1.85	700
2.15	600
2.63	500
3.30	400
4.40	300
6.70	200

Constant Voltage Charge

When battery voltage is detected to be 4.2 V under quick charge, constant voltage charge of 4.2 V is started.

- If the charge current falls to charge completion current or less under constant voltage charge, the state shifts to charge completion judgment mode.
- When $3.0\text{ V} \leq \text{battery voltage} < 4.2\text{ V}$ is detected, the state shifts to quick charge mode.
- When adapter voltage is detected to be battery voltage or less, charge stops.
- When battery voltage is detected to be 4.35 V or more (overvoltage), the state shifts to charge error mode.

Charge Completion Judgment Mode

When battery voltage $\geq 3.0\text{ V}$ and charge current $<$ charge completion current, the state shifts to charge completion judgment mode.

During charge completion judgment mode, charge transistor is turned off and battery voltage is monitored.

Battery voltage after charge transistor is OFF:

- When battery voltage $\geq 2.92\text{ V}$, charge completes.
- When $1.1\text{ V} \leq \text{battery voltage} < 2.92\text{ V}$, the state shifts to charge error mode.
- When battery voltage $< 1.1\text{ V}$, charge stops by battery disconnect detection.

And when the state shifts to charge completion mode, quick charge timer is reset.

Recharge Function

After charge completion, battery voltage continues to be monitored.

When battery voltage $< 3.97\text{V}$, recharge is started.

Quick charge timer also operates simultaneously with recharge start.

Battery Temperature Detection Function

Battery temperature is detected by TH terminal voltage.

- Low temperature side:
 - Charge stops when battery temperature (falling) is worth 2°C .
 - Charge restarts when battery temperature (rising) is worth 4°C .
- High temperature side:
 - Charge stops when battery temperature (rising) is worth 43°C .
 - Charge restarts when battery temperature (falling) is worth 38°C .

During charge stops, LED becomes OFF. (Timer is suspended during charge)

After charge restarts, the state return to the state before it stopped.

Timer ON/OFF Control Function

When "H" signal is input to TMRcnt terminal, timer can be suspended forcefully.

Even under charge, when "H" signal is input to TMRcnt terminal, timer is stopped and reset.

When TMRcnt terminal is "L" or "open", timer is enabled and timer operation is started.

Charge Error Mode

The state shifts to charge error mode and charge stops, when conditions are as follows;

- Overvoltage detection
- Thermal shutdown detection
- After trickle charge timer or quick charge timer expires.
- When $1.1\text{ V} \leq \text{battery voltage} < 2.92\text{ V}$ is detected.
(while battery voltage is monitored in charge completion judgment mode.)

When the following conditions are detected, charge error mode is canceled.

- When battery voltage is detected to be less than 1.0 V (battery is judged to be disconnected.)
- When battery voltage is detected to be 2.5 V or less.(power-on reset)

While charge error mode, the battery discharge current is $10\text{ }\mu\text{A}$.

Thermal Shutdown

When junction temperature becomes 140°C under charge, the state shifts to charge error mode.

Built-in Erroneous Detection Protection

The judgment result is decided delayed by the built-in erroneous detection protection circuit.

Each detection delay time is set by the internal clock.

Refer to Page-5 "Detection time" about each detection time.

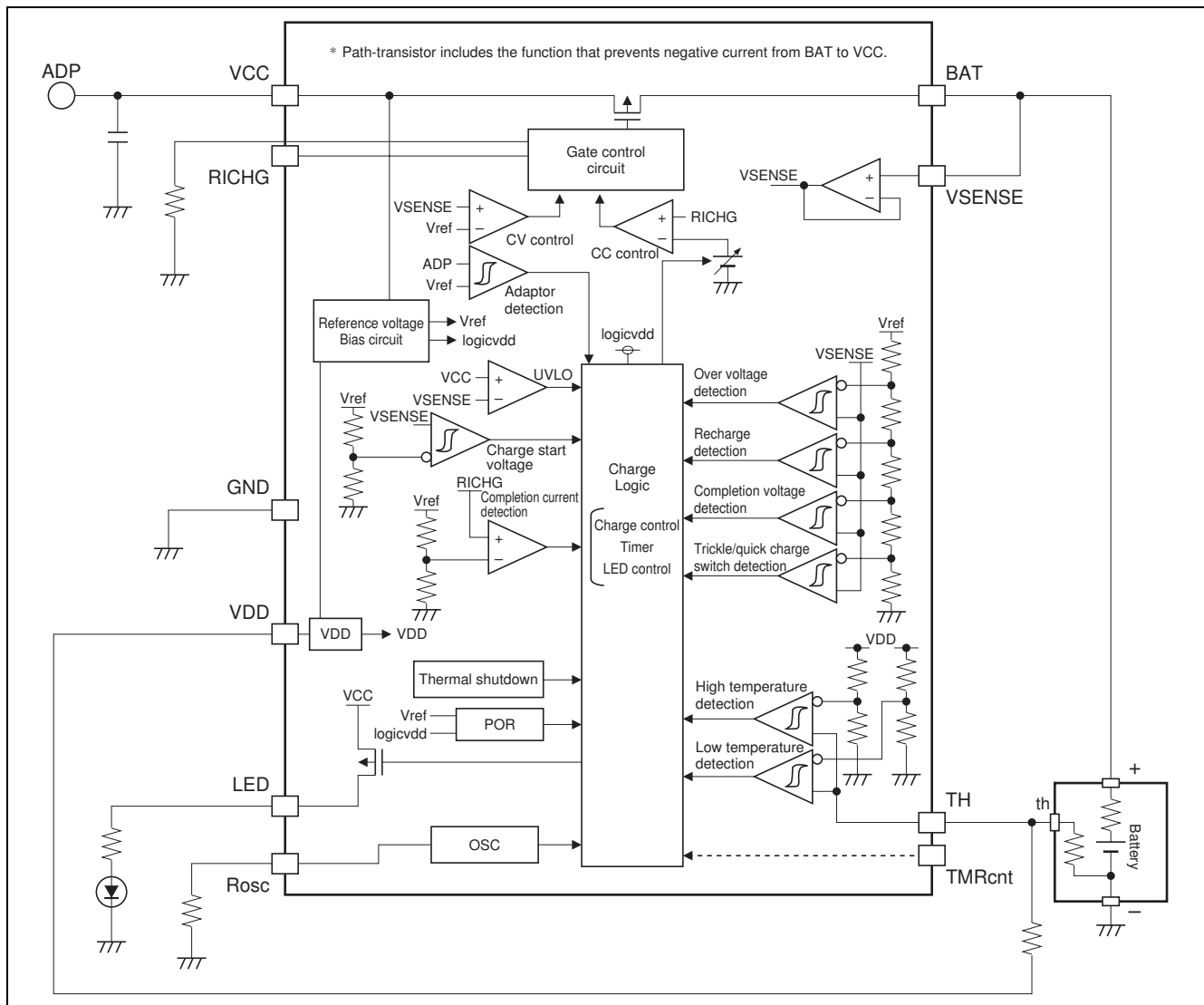
LED Control Block

LED lighting specification is as below.

- LED Lighting Table

Mode	LED
Standby	OFF
Under charge	ON
Charge completion	OFF
Recharge	ON
Charge error	Blink
Battery temperature protection	OFF

Application Circuit



Package Dimensions

10 pin DFN package.

Note: Please contact Renesas Technology Corp. for details.

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