

HSM123

Silicon Epitaxial Planar Diode for High Speed Switching

REJ03G0550-0800

Rev.8.00

Dec 12, 2008

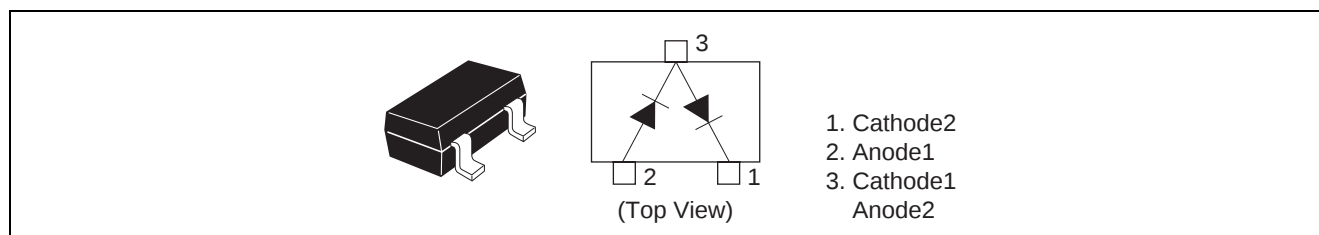
Features

- Low capacitance, proof against high voltage.
- Fast recovery time. ($t_{rr} = 3.0\text{ns max}$)
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Part No	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
HSM123TL	A9	MPAK	PLSP0003ZC-A	TL (3,000 pcs / reel)
HSM123TR	A9	MPAK	PLSP0003ZC-A	TR (3,000 pcs / reel)

Pin Arrangement



Absolute Maximum Ratings *¹

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	85	V
Reverse voltage	V_R	80	V
Peak forward current	I_{FM}	300	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*2}	4	A
Average rectified current	I_O	100	mA
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. Per one device.

2. Within 1 μ s forward surge current.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	0.70	1.0	V	$I_F = 10$ mA
	V_F	—	0.79	1.0	V	$I_F = 50$ mA
	V_F	—	0.85	1.2	V	$I_F = 100$ mA
Reverse current	I_R	—	—	0.1	μ A	$V_R = 80$ V
Capacitance	C	—	1.0	4.0	pF	$V_R = 0$ V, $f = 1$ MHz
Reverse recovery time	t_{rr}	—	—	3.0	ns	$I_F = 10$ mA, $V_R = 6$ V, $R_L = 50 \Omega$

Main Characteristics

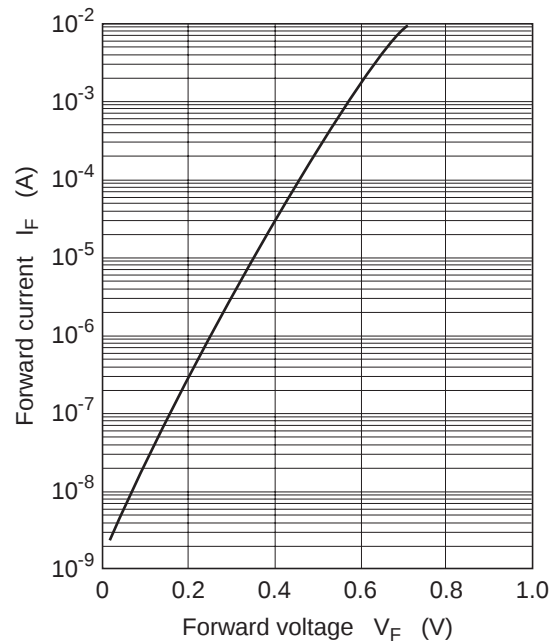


Fig.1 Forward Current vs. Forward Voltage

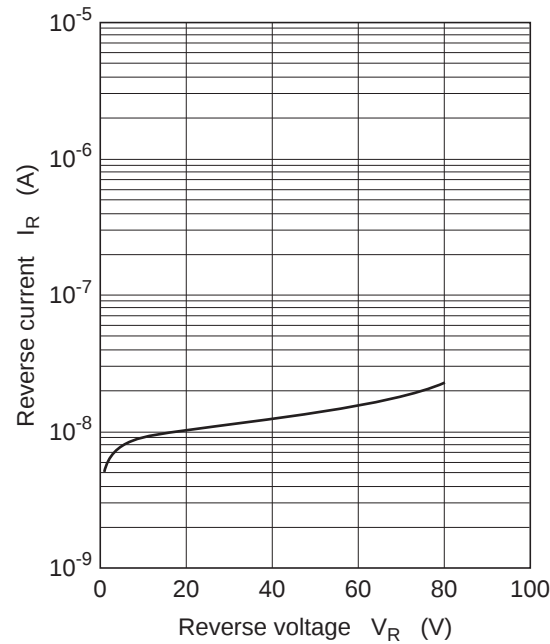


Fig.2 Reverse Current vs. Reverse Voltage

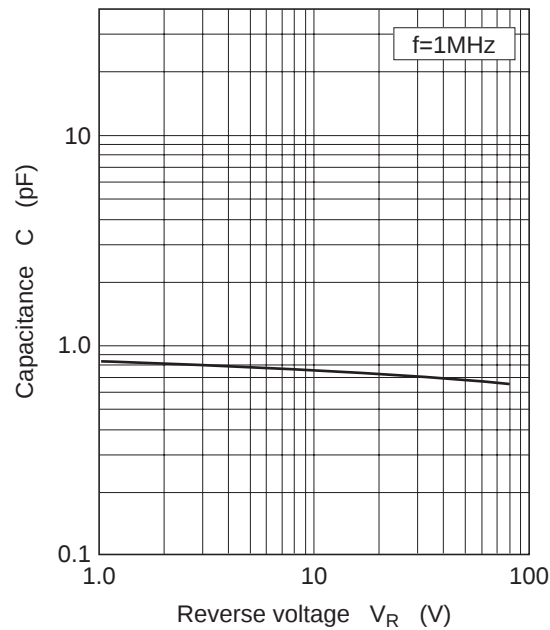
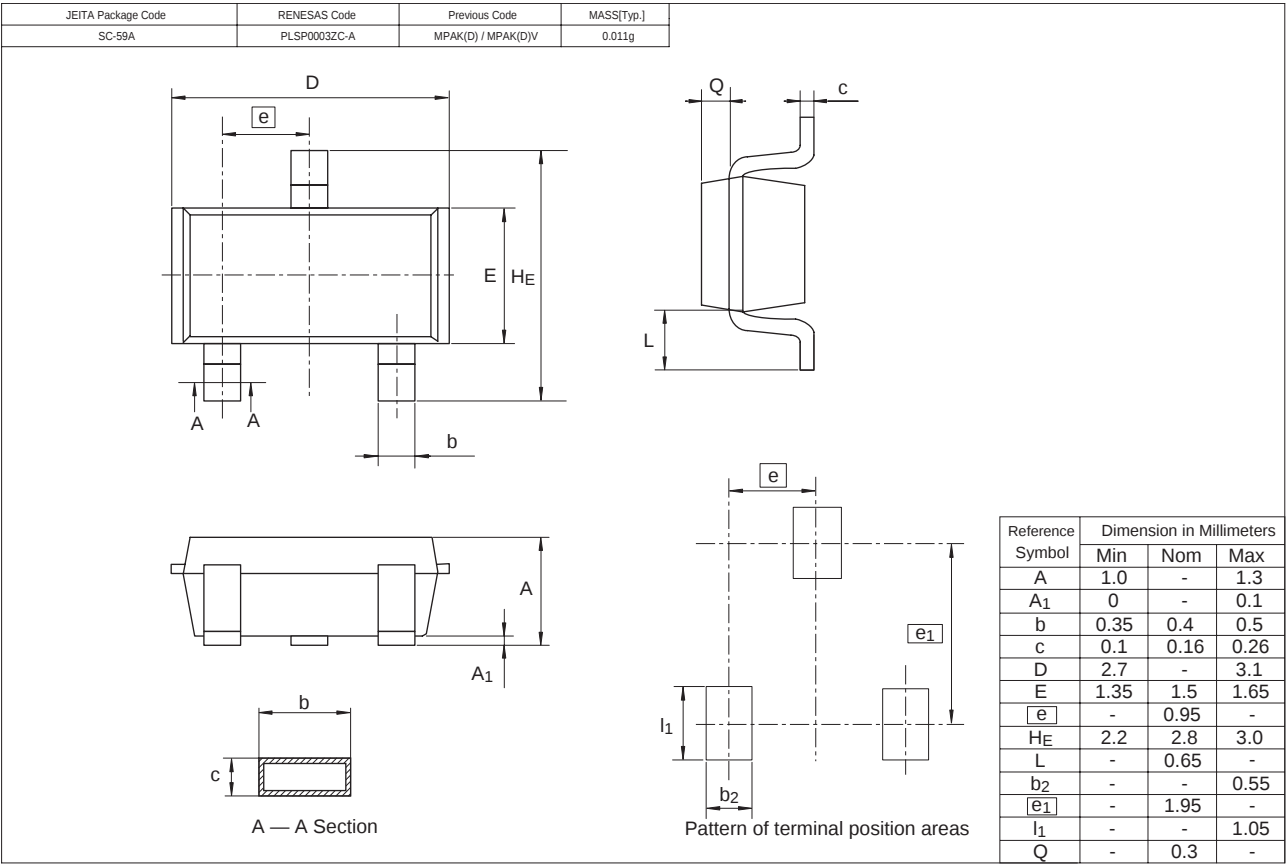


Fig.3 Capacitance vs. Reverse Voltage

Package Dimensions



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