

BCR08DS-14A

Triac Low Power Use R07DS0258EJ0100 Rev.1.00 Mar 30, 2011

Features

- $I_{T (RMS)} : 0.8 A$ V_{DRM} :700 V
- I_{FGTI}, I_{RGTI}, I_{RGTIII}: 5 mA

- Planar Passivation Type
- Surface Mounted Type
- Completed Pb Free

Outline

RENESAS Package code: PRSP0004ZA-A

(Package name: SOT-223)





- 1. T₁ Terminal 2. T₂ Terminal
- 3. Gate Terminal
- 4. T₂ Terminal

Applications

Washing machine, electric fan, air cleaner, other general purpose control applications

Maximum Ratings

Parameter	Symbol	Voltage class 14	Unit
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	700	V
Non- repetitive peak off-state voltage ^{Note1}	V_{DSM}	840	V

Notes: 1. Gate open.

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	0.8	А	Commercial frequency, sine full wave 360° conduction, Tc= 96℃ Note3
Surge on-state current	I _{TSM}	8	А	60Hz sinewave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.26	A ² s	Value corresponding to 1 cycle of half
				wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	1	W	
Average gate power dissipation	P _{G (AV)}	0.1	W	
Peak gate voltage	V_{GM}	6	V	
Peak gate current	I _{GM}	0.5	Α	
Junction temperature	Tj	-40 to +125	C	
Storage temperature	Tstg	-40 to +125	C	
Mass	_	0.12	g	Typical value

Electrical Characteristics

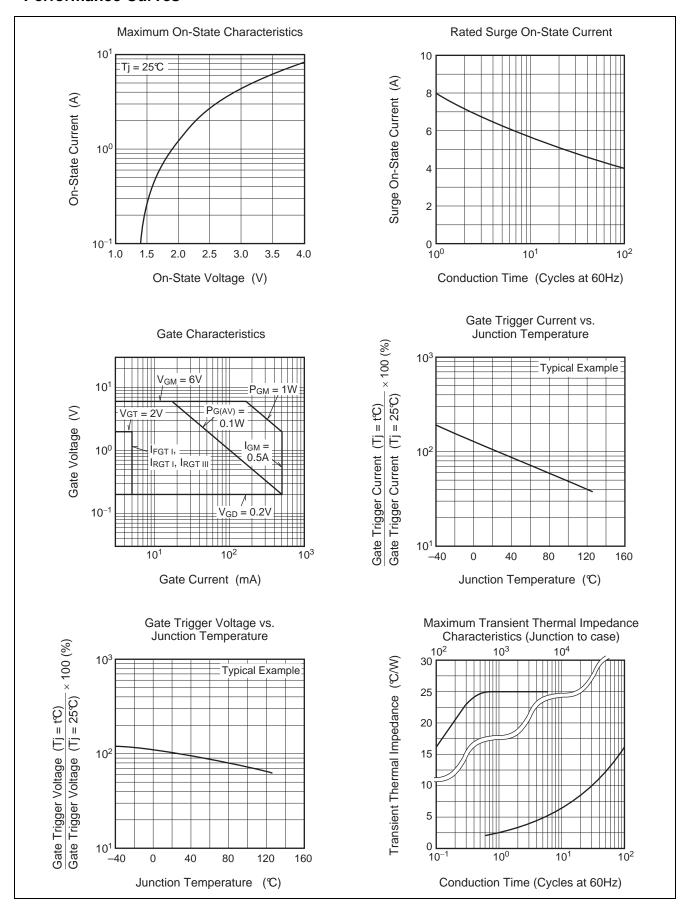
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state curr	rent	I _{DRM}	_	_	1.0	mA	Tj = 125℃, V _{DRM} applied
On-state voltage		V_{TM}	_	_	2.0	V	Tc = 25℃, I _{TM} = 1.2 A, Instantaneous measurement
Gate trigger voltage ^{Note2}	I	V_{FGTI}	_	_	2.0	V	Tj = 25°C, V _D = 6 V, R _L = 6 Ω ,
	II	V_{RGTI}		_	2.0	V	$R_G = 330 \Omega$
	III	V_{RGTIII}		_	2.0	V	
Gate trigger current ^{Note2}	I	$I_{FGT_{\mathrm{I}}}$		_	5	mA	Tj = 25°C, V _D = 6 V, R _L = 6 Ω ,
	II	I_{RGTI}		_	5	mA	$R_G = 330 \Omega$
	III	I_{RGTIII}	_	_	5	mA	
Gate non-trigger voltage		V_{GD}	0.2	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$
Thermal resistance		R _{th (j-c)}	_	_	25	C/W	Junction to case Note3
Critical-rate of rise of off-stat commutating voltage ^{Note4}	е	(dv/dt)c	0.5	_	_	V/μs	Tj = 125℃

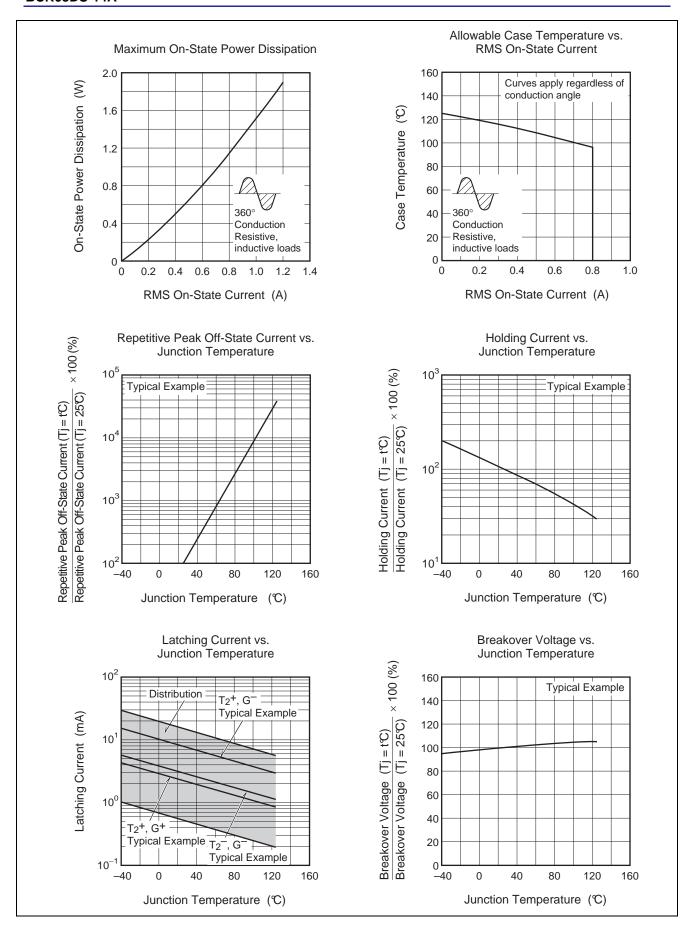
Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

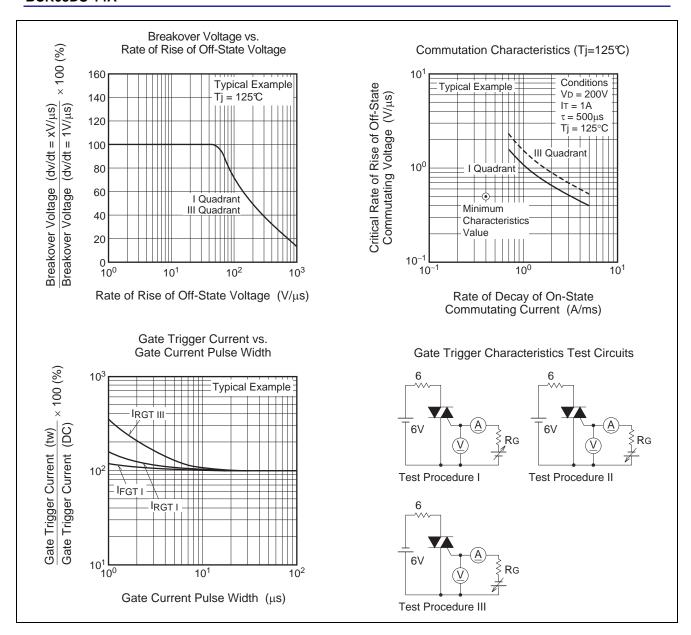
- 3. Case temperature is measured on the T2 tab..
- 4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.

Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125℃	Supply Voltage → Time
2. Rate of decay of on-state commutating current (di/dt)c = -0.4 A/ms	Main Current (di/dt)c Time
3. Peak off-state voltage $V_D = 400 \text{ V}$	Main Voltage — Time (dv/dt)c

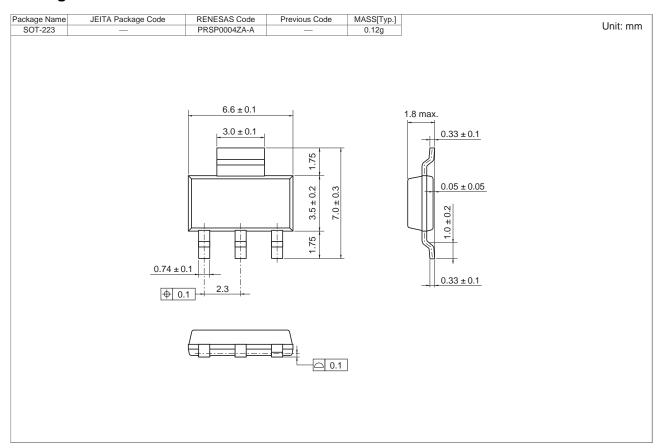
Performance Curves







Package Dimension



Ordering Information

Orderable Part Number	Packing	Quantity	Remark
BCR08DS-14A-T13#B10	Embossed Tape	3000 pcs.	Taping direction "T1"

Note: Please confirm the specification about the shipping in detail.

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