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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR 2SK3811

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK3811 is N-channel MOS Field Effect Transistor designed for high current switching applications.

ORDERING INFORMATION

PART NUMBER	PACKAGE		
2SK3811-ZP	TO-263 (MP-25ZP)		

FEATURES

• Super low on-state resistance $R_{DS(on)} = 1.8 \ m\Omega \ MAX. \ (V_{GS} = 10 \ V, \ I_{D} = 55 \ A)$

• High Current Rating: ID(DC) = ±110 A

(TO-263)



ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	40	V
Gate to Source Voltage (V _{DS} = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	I _{D(DC)}	±110	Α
Drain Current (pulse) Note1	I _{D(pulse)}	±440	Α
Total Power Dissipation (Tc = 25°C)	P _{T1}	213	W
Total Power Dissipation (T _A = 25°C)	P _{T2}	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Energy Note2	Eas	518	mJ
Repetitive Avalanche Current Note3	Iar	72	Α
Repetitive Avalanche Energy Note3	Ear	518	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Starting T_{ch} = 25°C, V_{DD} = 20 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V, L = 100 μ H

3. Rg = 25 Ω , Tch(peak) \leq 150°C

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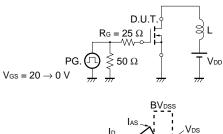


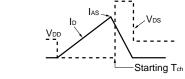
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 40 V, V _{GS} = 0 V			10	μΑ
Gate Leakage Current	Igss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 55 A	45	89		S
Drain to Source On-state Resistance Note	R _{DS(on)}	V _{GS} = 10 V, I _D = 55 A		1.4	1.8	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		17700		pF
Output Capacitance	Coss	V _{GS} = 0 V		2200		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		1300		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 20 V, I _D = 55 A		54		ns
Rise Time	t r	V _{GS} = 10 V		140		ns
Turn-off Delay Time	td(off)	R _G = 0 Ω		130		ns
Fall Time	t _f			21		ns
Total Gate Charge	Q _G	V _{DD} = 32 V		260		nC
Gate to Source Charge	Qgs	V _{GS} = 10 V		57		nC
Gate to Drain Charge	Q _{GD}	I _D = 110 A		83		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I _F = 110 A, V _{GS} = 0 V		0.87	1.5	V
Reverse Recovery Time	trr	I _F = 110 A, V _{GS} = 0 V		60		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		80		nC

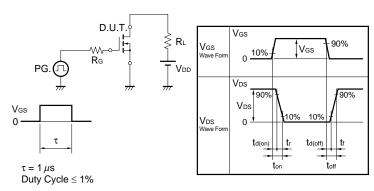
Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY





TEST CIRCUIT 2 SWITCHING TIME

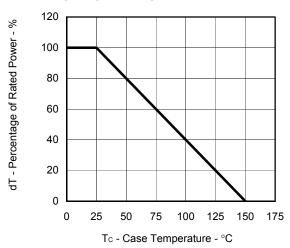


TEST CIRCUIT 3 GATE CHARGE

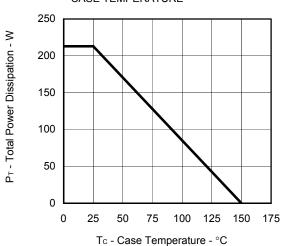
PG.
$$\square$$
 $\stackrel{\bigcirc}{>} 50 \Omega$

TYPICAL CHARACTERISTICS (TA = 25°C)

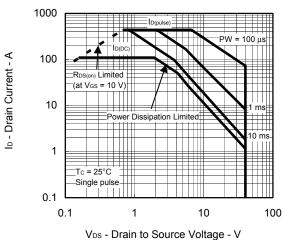
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

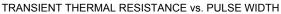


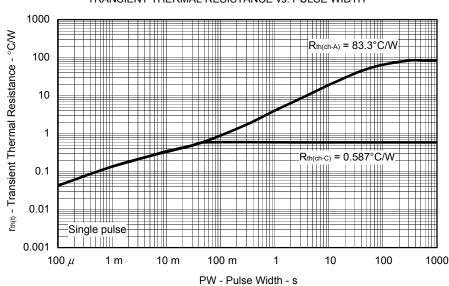
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



FORWARD BIAS SAFE OPERATING AREA

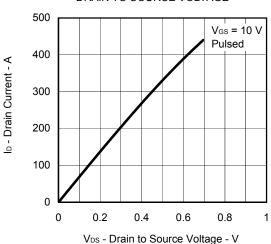




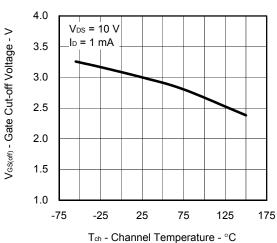


3

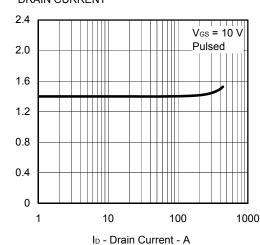
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



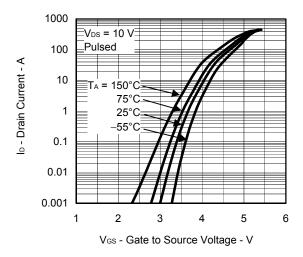
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



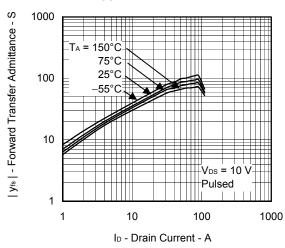
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



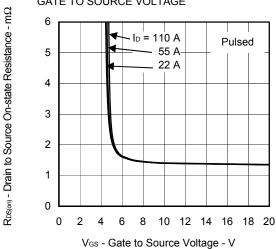
FORWARD TRANSFER CHARACTERISTICS



FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

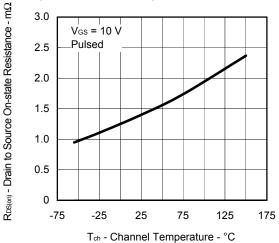


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

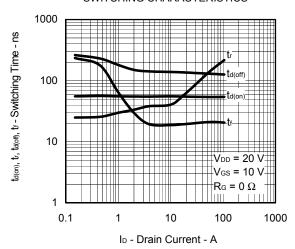


R_{DS(on)} - Drain to Source On-state Resistance - mΩ

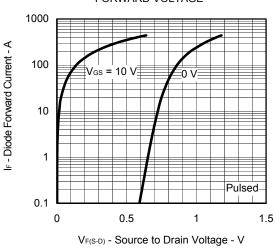
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



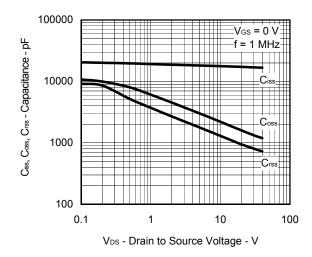
SWITCHING CHARACTERISTICS



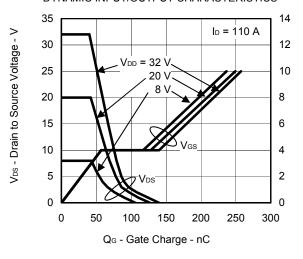
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



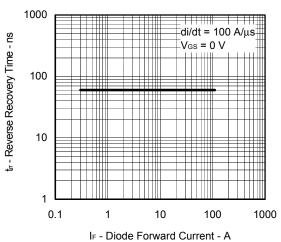
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



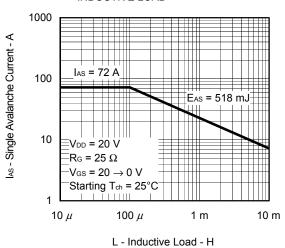
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



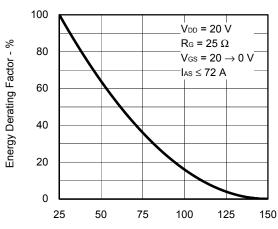
REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD



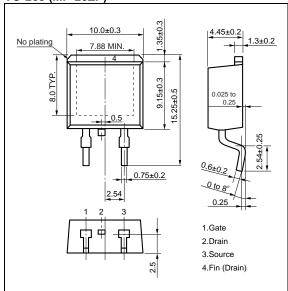
SINGLE AVALANCHE ENERGY DERATING FACTOR



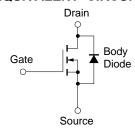
Starting Tch - Starting Channel Temperature - °C

PACKAGE DRAWING (Unit: mm)

TO-263 (MP-25ZP)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

Data Sheet D16737EJ1V0DS 7

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