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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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2SJ479(L), 2SJ479(S)

Silicon P Channel MOS FET

REJ03G0866-0300

Rev.3.00

Jun 05, 2006

Description

High speed power switching

Features

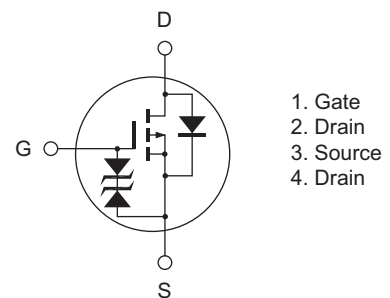
- Low on-resistance
 $R_{DS(on)} = 25 \text{ m}\Omega$ typ.
- 4 V gate drive devices.
- High speed switching

Outline

RENESAS Package code: PRSS0004AE-A
(Package name: LDPAK (L))



RENESAS Package code: PRSS0004AE-B
(Package name: LDPAK (S)-(1))



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	-30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	-30	A
Drain peak current	I _{D (pulse)} ^{Note 1}	-120	A
Body to drain diode reverse drain current	I _{DR}	-30	A
Channel dissipation	P _{ch} ^{Note 2}	50	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
 2. Value at Tc = 25°C

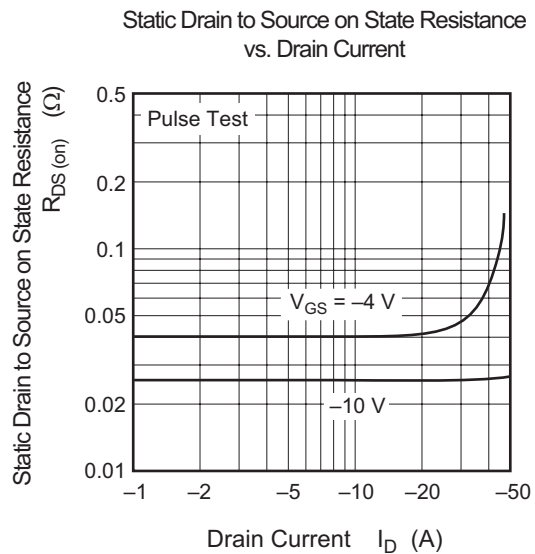
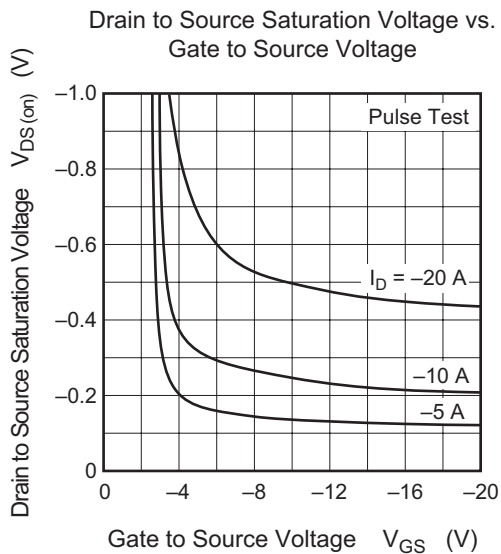
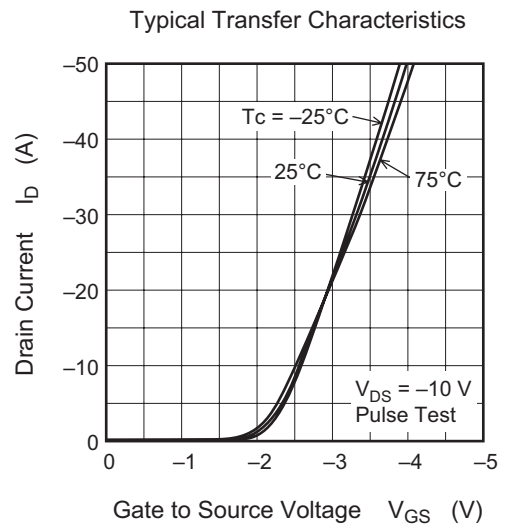
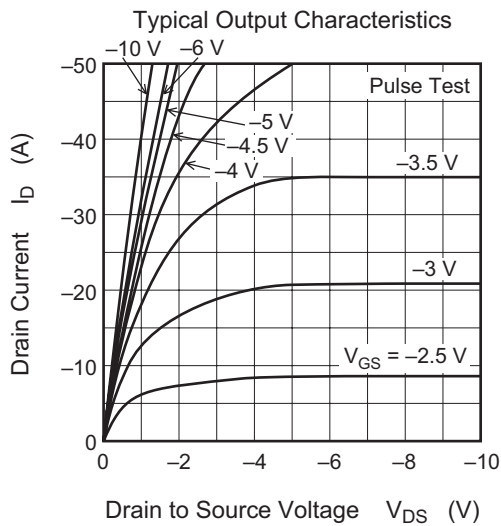
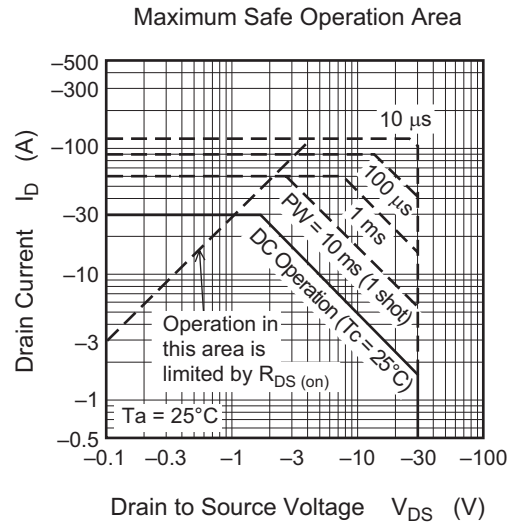
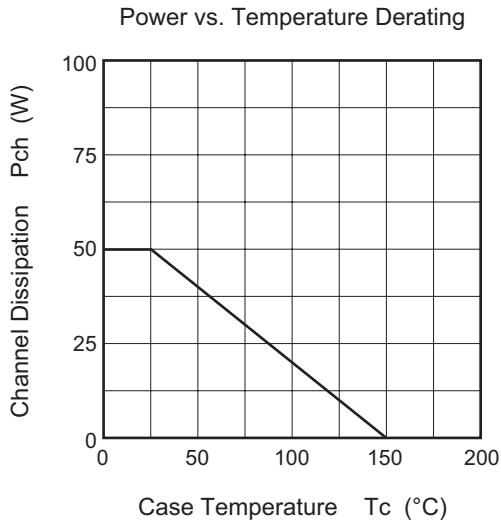
Electrical Characteristics

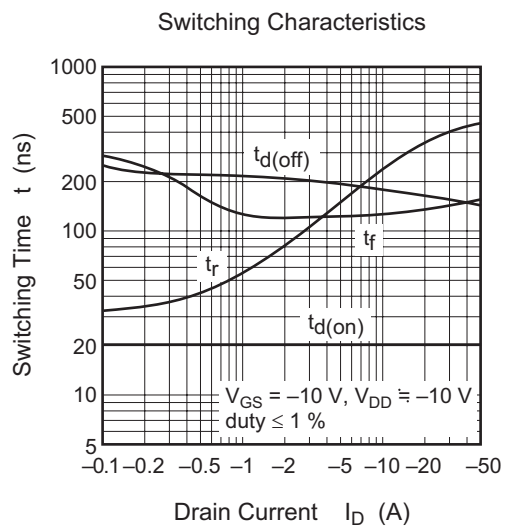
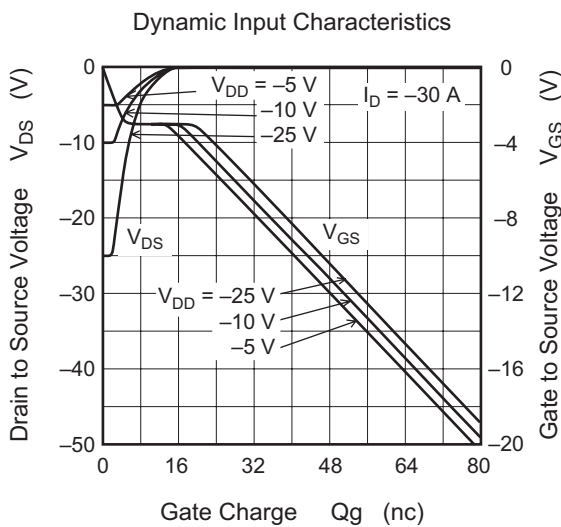
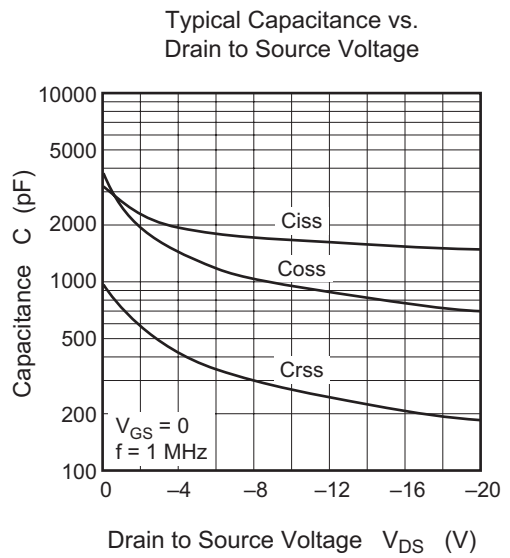
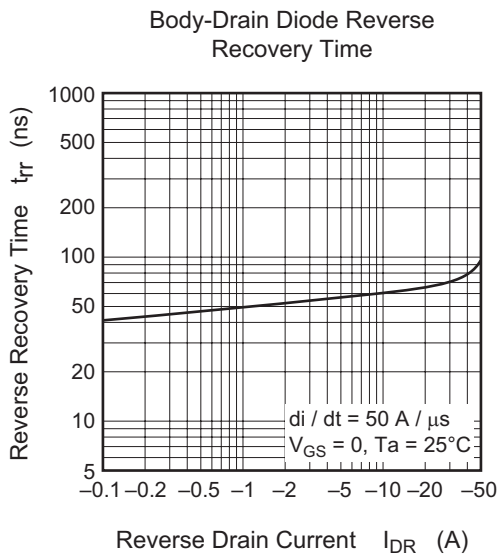
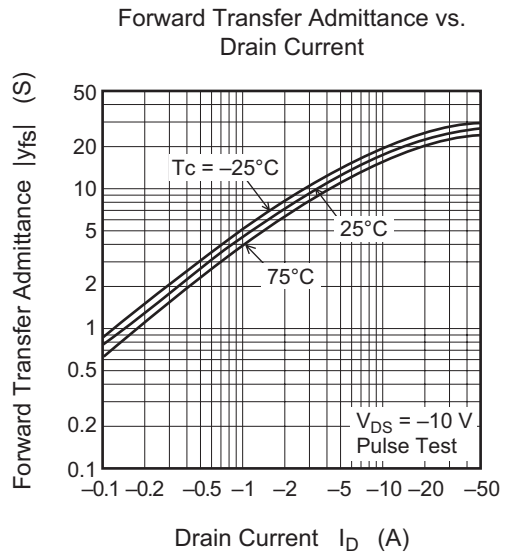
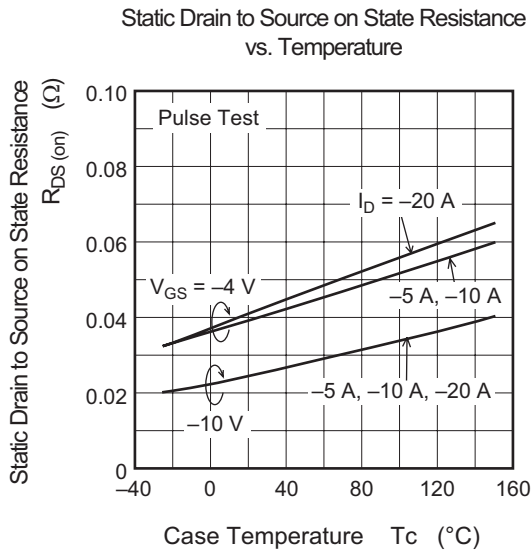
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR) DSS}	-30	—	—	V	I _D = -10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR) GSS}	±20	—	—	V	I _G = ±100 μA, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	-10	μA	V _{DS} = -30 V, V _{GS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±16 V, V _{DS} = 0
Gate to source cutoff voltage	V _{GS (off)}	-1.0	—	-2.0	V	I _D = -1 mA, V _{DS} = -10 V
Static drain to source on state resistance	R _{DS (on)}	—	25	35	mΩ	I _D = -15 A, V _{GS} = -10 V ^{Note 3}
	R _{DS (on)}	—	40	60	mΩ	I _D = -15 A, V _{GS} = -4 V ^{Note 3}
Forward transfer admittance	y _{fs}	12	20	—	S	I _D = -15 A, V _{DS} = -10 V ^{Note 3}
Input capacitance	C _{iss}	—	1700	—	pF	V _{DS} = -10 V
Output capacitance	C _{oss}	—	950	—	pF	V _{GS} = 0
Reverse transfer capacitance	C _{rss}	—	260	—	pF	f = 1 MHz
Turn-on delay time	t _{d (on)}	—	20	—	ns	V _{GS} = -10 V
Rise time	t _r	—	290	—	ns	I _D = -15 A
Turn-off delay time	t _{d (off)}	—	170	—	ns	R _L = 0.67 Ω
Fall time	t _f	—	130	—	ns	
Body to drain diode forward voltage	V _{DF}	—	-1.1	—	V	I _F = -30 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	—	70	—	ns	I _F = -30 A, V _{GS} = 0 di _F /dt = 50 A/μs

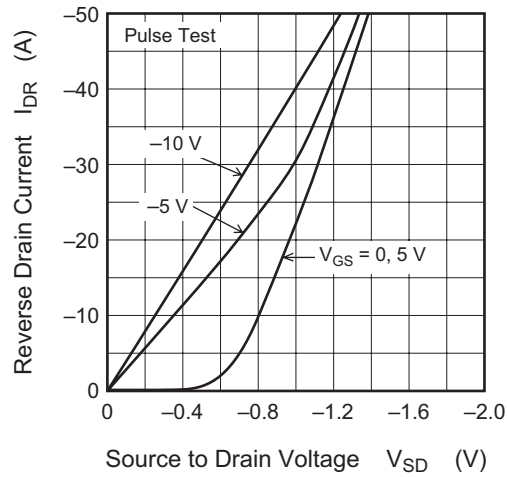
Note: 3. Pulse test

Main Characteristics

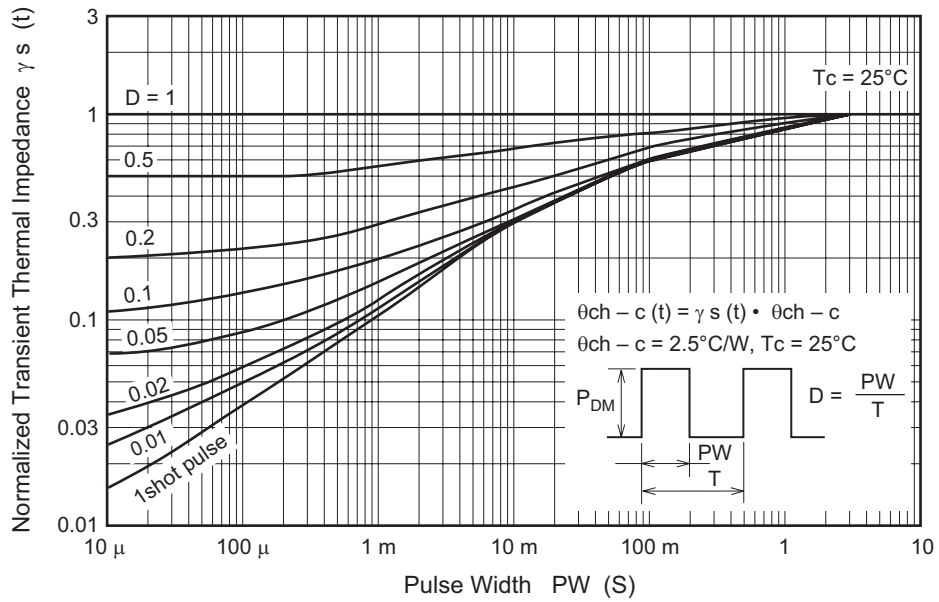




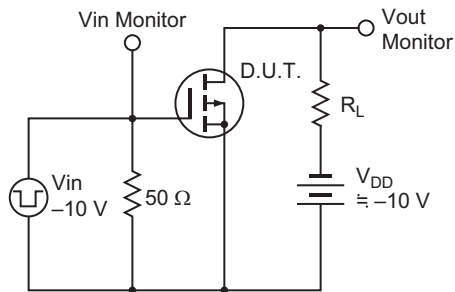
Reverse Drain Current vs. Source to Drain Voltage



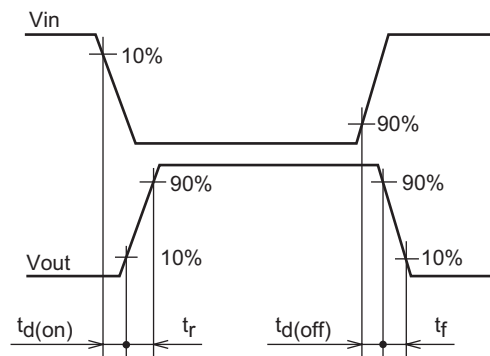
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



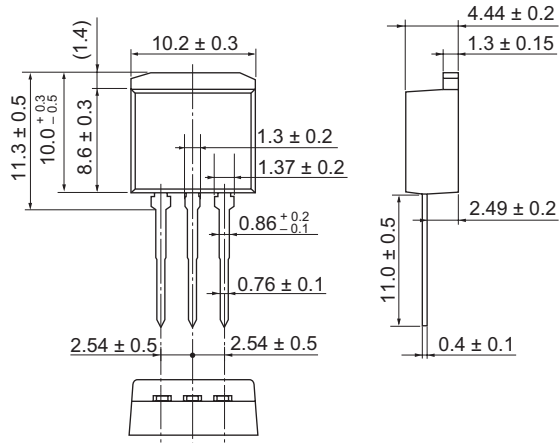
Waveform



Package Dimensions

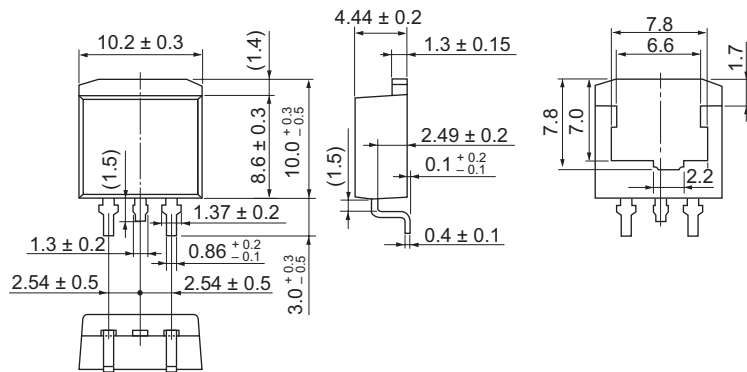
Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDBAK(L)	—	PRSS0004AE-A	LDBAK(L) / LDBAK(L)V	1.40g

Unit: mm



Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDBAK(S)-(1)	SC-83	PRSS0004AE-B	LDBAK(S)-(1) / LDBAK(S)-(1)V	1.30g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
2SJ479L-E	500 pcs	Box (Sack)
2SJ479STL-E	1000 pcs	Taping

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