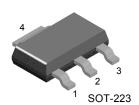


# NZT660/NZT660A PNP Low Saturation Transistor

• These devices are designed with high current gain and low saturation voltage with collector currents up to 3A continuous.



1. Base 2. Collector 3. Emitter

## Absolute Maximum Ratings\* Ta=25°C unless otherwise noted

Symbol	Parameter	NZT660	NZT660A	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	60	60	V
V <sub>CBO</sub>	Collector-Base Voltage	80	60	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	5	V
I <sub>C</sub>	Collector Current - Continuous	3	3	Α
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	- 55 ~ +150	- 55 ~ +150	°C

 $<sup>^{\</sup>star}\,\text{These ratings are limiting values above which the serviceability of any semiconductor device may be impaired}.$ 

#### NOTES

### Electrical Characteristics T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units	
Off Charac	Off Characteristics						
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10mA	60			V	
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 100μA NZT660 NZT660A	80 60			V V	
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 100μA	5			٧	
I <sub>CBO</sub>	Collector-Base Cutoff Current	V <sub>CB</sub> = 30V V <sub>CB</sub> = 30V, T <sub>A</sub> = 100°C			100 10	nA μA	
I <sub>EBO</sub>	Emitter-Base Cutoff Current	V <sub>EB</sub> = 4V			100	nA	
On Characteristics *							
h <sub>FE</sub>	DC Current Gain	$\begin{split} I_{C} &= 100 \text{mA},  V_{CE} = 2 \text{V} \\ I_{C} &= 500 \text{mA},  V_{CE} = 2 \text{V} \\ I_{C} &= 1 \text{A},  V_{CE} = 2 \text{V} \\ I_{C} &= 3 \text{A},  V_{CE} = 2 \text{V} \end{split}$	70 100 250 80 25		300 550		

<sup>1)</sup> These ratings are based on a maximum junction temperature of 150  $^{\circ}\text{C}.$ 

<sup>2)</sup> These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## 

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 1A, I <sub>B</sub> = 100mA I <sub>C</sub> = 3A, I <sub>B</sub> = 300mA NZT660 NZT660A			300 550 500	mV mV mV
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 1A, I <sub>B</sub> = 100mA			1.25	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = 1A, V <sub>CE</sub> = 2V			1	V
Small Signal Characteristics						•
C <sub>obo</sub>	Output Capacitance	V <sub>CB</sub> = 10V, I <sub>E</sub> = 0, f = 1MHz			45	pF
f <sub>T</sub>	Transition Frequency	I <sub>C</sub> = 100mA, V <sub>CE</sub> = 5V, f = 100MHz 75				MHz

<sup>\*</sup> Pulse Test: Pulse Width  $\leq 300 \mu s$ , Duty Cycle  $\leq 2.0\%$ 

## Thermal Characteristics $T_a=25$ °C unless otherwise noted

Symbol	Parameter	NZT660/NZT660A	Units
$P_D$	Total Device Dissipation	2	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62.5	°C/W

## **Typical Performance Characteristics**

Figure 1. Base-Emitter Saturation Voltage vs Collector Current

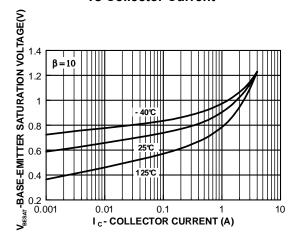


Figure 3. Collector-Emitter Saturation Voltage vs Collector Current

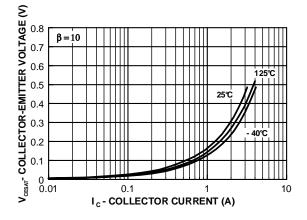


Figure 5. Current Gain vs Collector Current

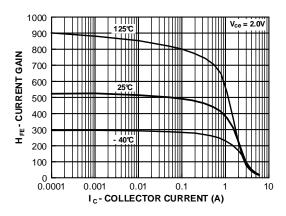


Figure 2. Base-Emitter On Voltage vs Collector Current

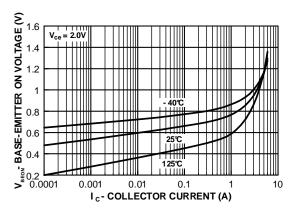
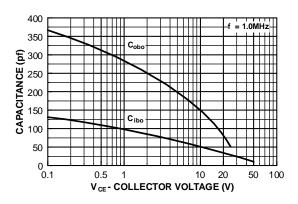
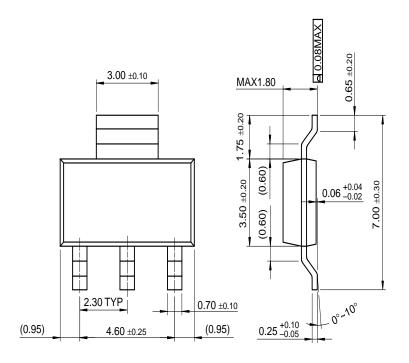


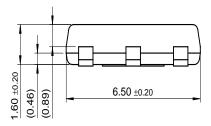
Figure 4. Input/Output Capacitance vs Reverse Bias Voltage



## **Mechanical Dimensions**

## **SOT-223**





Dimensions in Millimeters

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E <sup>2</sup> CMOS™	I <sup>2</sup> C™	MSX™	QT Optoelectronics™	TinyLogic <sup>®</sup>
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