

MC79LXXA/LM79LXXA

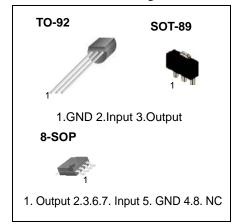
3-Terminal 0.1A Negative Voltage Regulator

Features

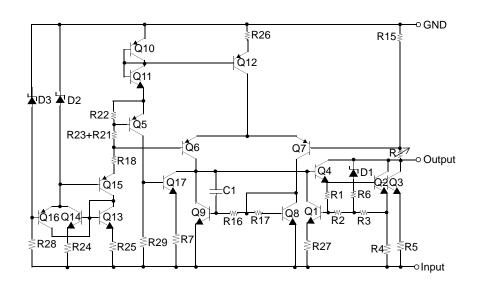
- Output Current up to 100mA
- No External Components
- Internal Thermal Over Load Protection
- Internal Short Circuit Current Limiting
- Output Voltage Offered in±5% Tolerance
- Output Voltage of -5V, -8V, -12V, -15V, -18V, -24V

Description

These regulators employ internal current limiting and thermal shutdown, making them essentially indestructible.



Internal Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Input Voltage (for $V_0 = -5V$ to $-8V$) (for $V_0 = -12V$ to $-18V$) (for $V_0 = -24V$)	VI	-30 -35 -40	V
Operating Temperature Range	Topr	0 ~ +125	°C
Storage Temperature Range	TSTG	-65 ~ +150	°C

Electrical Characteristics(MC79L05A/LM79L05A)

(VI = -10V, IO = 40mA, CI = $0.33\mu F$, CO = $0.1\mu F$, $0^{\circ}C \leq TJ \leq +125^{\circ}C$, unless otherwise specified)

Parameter		Symbol	Conditions		Min.	Тур.	Max.	Unit
Output Voltage		Vo	T _J = +25°C		-4.8	-5.0	-5.2	V
			_	-7.0V ≥ V _I ≥ -20V	-	15	150	mV
Line Regulation (No	te1)	ΔVΟ	TJ =+25°C -8V ≥ VI ≥ -20V		-	-	100	mV
Load Population (No	oto 1)	4)/0	T 25°C	$1.0\text{mA} \le I_{O} \le 100\text{mA}$	-	20	60	mV
Load Regulation (Note1)		ΔνΟ	Δ VO TJ =+25°C 1.0mA \leq IO \leq 40mA	1.0mA ≤ IO ≤ 40mA	-	10	30	mV
Output Voltage		Vo	$-7.0V \ge V_I \ge -20V$, $1.0mA \le I_O \le 40mA$		-4.75	-	-5.25	V
Output Voltage		VO	$V_I = -10V, 1.0mA \le I_O \le 70mA$		-4.75	-	-5.25	V
Quiescent Current		lo	TJ =+25°C		-	2.0	5.5	mA
Quiescent Current		lQ	T _J = +125°C		-	-	6.0	111/
Quiescent Current	With Line	ΔlQ	-8V ≥ V _I ≥ -20V		-	-	1.5	mA
Change	With Load	Load ΔIQ 1.0mA $\leq IQ \leq 40$ mA		-	-	0.1	mA	
Output Noise Voltage		VN	$T_A = +25^{\circ}C,10Hz \le f \le 100kHz$		-	30	-	μV
Ripple Rejection		RR	$f = 120Hz, -8V \ge V_I \ge -18V$ $T_J = +25^{\circ}C$		41	60	-	dB
Dropout Voltage		VD	TJ = +25°C		-	1.7	-	V

^{1.} Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (MC79L08A) (Continued)

(VI = -14V, IO = 40mA, CI = $0.33\mu F$, CO = $0.1\mu F$, $0^{\circ}C \leq TJ \leq +125^{\circ}C$, unless otherwise specified)

Parameter		Symbol	Conditions		Min.	Тур.	Max.	Unit
Output Voltage		Vo	T _J = +25°C		-7.7	-8.0	-8.3	V
			_	-10.3V ≥ V _I ≥ -23V	-	-	175	mV
Line Regulation(Note	e1)	ΔVΟ	TJ = +25°C	-12V ≥ V _I ≥ -23V	-	-	125	mV
Load Regulation (No	sto1)	ΔVο	TJ = +25°C	$1.0mA \leq I_0 \leq 100mA$	-	-	80	mV
Load Regulation (NC	ne i)	ΔνΟ	1J = +25 C	$1.0mA \le I_0 \le 40mA$	-	-	40	mV
Output Voltage		Vo	$-10.3V \ge V_I \ge -23V$, $1.0mA \le I_0 \le 40mA$		-7.6	-	-8.4	V
Output voltage		٧٥	$V_I = -14V$, $1.0mA \le I_0 \le 70mA$		-7.6	-	-8.4	\ \ \ \ \ \
Quiescent Current		I	T _j = +25°C		-	-	6.0	mA
Quiescent Current		Iq	T _j = +125°C		-	-	5.5	IIIA
Quiescent Current	With Line	Alo.	-11.7V ≥ V _I ≥ -23V		-	-	1.5	mA
Change	With Load	ΔlQ	$1.0\text{mA} \le I_0 \le 40\text{mA}$		-	-	0.1	mA
Output Noise Voltage		VN	$T_j = +25^{\circ}C,10Hz \le f \le 100kHz$		-	50	-	μV
Ripple Rejection		RR	$ f = 120Hz, -11V \ge V_I \ge -21V $ $ T_j = +25^{\circ}C $		39	55	-	dB
Dropout Voltage		VD	T _j = +25°C		-	1.7	-	V

^{1.} Load and line regulation are specified at constant junction temperature. Change in Vo due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics(MC79L12A) (Continued)

(VI = -19V, IO = 40mA, CI = $0.33\mu F$, CO = $0.1\mu F$, $0^{\circ}C \leq TJ \leq +125^{\circ}C$, unless otherwise specified)

Parameter		Symbol	Conditions		Min.	Тур.	Max.	Unit
Output Voltage		Vo	T _J = +25°C		-11.5	-12.0	-12.5	V
			TJ = +25°C	-14.5V ≥ V _I ≥ -27V	-	-	250	mV
Line Regulation (Not	te1)	ΔVΟ		-	-	200	mV	
Load Regulation (No	sto1)	ΔVο	TJ = +25°C	$1.0\text{mA} \le \text{IO} \le 100\text{mA}$	-	-	100	mV
Load Regulation (NC	ne i)	ΔνΟ	1J = +25 C	$1.0\text{mA} \le I_O \le 40\text{mA}$	-	-	50	mV
Output Voltage		Vo	$-14.5V > V_I > -27V$, $1.0mA \le I_O \le 40mA$		-11.4	-	-12.6	V
Output voltage		٧٥	$V_I = -19V, 1.0 \text{mA} \le I_O \le 70 \text{mA}$		-11.4	-	-12.6	V
Quiescent Current		Io	T _J = +25°C		-	-	6.0	mA
Quiescent Current		IQ	T _J = +125°C		-	-	6.5	
Quiescent Current	With Line	ΔlQ	-16V ≥ VI ≥ -27V		-	-	1.5	mA
Change	With Load	ΔlQ	$1.0\text{mA} \le I_O \le 40\text{mA}$		-	-	0.1	mA
Output Noise Voltage		VN	$T_A = +25^{\circ}C,10Hz \le f \le 100kHz$		-	80	-	μV
Ripple Rejection	RR		37	42	-	dB		
Dropout Voltage		VD	T _J = +25°C		-	1.7	-	V

^{1.} Load and line regulation are specified at constant junction temperature. Change in Vo due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics(MC79L15A) (Continued)

(VI = -23V, IO = 40mA, CI = $0.33\mu F$, CO = $0.1\mu F$, $0^{\circ}C \leq TJ \leq +125^{\circ}C$, unless otherwise specified)

Parameter		Symbol	Conditions		Min.	Тур.	Max.	Unit
Output Voltage		Vo	TJ = +25°C		-14.4	-15.0	-15.6	V
				-17.5V ≥ V _I ≥ -30V	-	-	300	mV
Line Regulation (No	te1)	ΔVΟ	TJ = +25°C	-20V ≥ V _I ≥ -30V		-	250	mV
Load Regulation (No	oto 1)	4)/0	T25°C	$1.0\text{mA} \le IO \le 100\text{mA}$	-	-	150	mV
Load Regulation (No	ole i)	ΔVO	$T_{J} = +25^{\circ}C$ $1.0mA \le I_{O} \le 40mA$	-	-	75	mV	
Output Voltage		Vo	$-17.5V \ge V_I \ge -30V, \ 1.0mA \le IO \le 40mA$		-14.25	-	-15.75	V
Output Voltage		٧٥	V _I = -23V, 1.0mA ≤ I _O ≤ 70mA		-14.25	-	-15.75	V
Quiescent Current		lo.	T _J = +25°C		-	-	6.0	mA
Quiescent Current		lQ	T _J = +125°C		-	-	6.5	
Quiescent Current	With Line	ΔlQ	-20V ≥ VI ≥ -30°	V	-	-	1.5	mA
Change	With Load	ΔlQ	1.0mA ≤ I _O ≤ 40mA		-	-	0.1	mA
Output Noise Voltage V_N $T_A = +25^{\circ}C,10Hz \le f \le 100kHz$		-	90	-	μV			
Ripple Rejection		RR	$f = 120Hz, -18.5V \ge V_I \ge -28.5V$ $T_J = +25^{\circ}C$		34	39		dB
Dropout Voltage	_	VD	T _J = +25°C		-	1.7	-	V

^{1.} Load and line regulation are specified at constant junction temperature. Change in Vo due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics(MC79L18A) (Continued)

(VI = -27V, IO = 40mA, CI = $0.33\mu F$, CO = $0.1\mu F$, $0^{\circ}C \leq TJ \leq +125^{\circ}C$, unless otherwise specified)

Parameter		Symbol	Conditions		Min.	Тур.	Max.	Unit
Output Voltage		Vo	T _J = +25°C		-17.3	-18.0	-18.7	V
			_	-20.7V ≥ VI ≥ -33V	-	-	325	mV
Line Regulation (No	te1)	ΔVΟ	TJ = +25°C	-21V ≥ V _I ≥ -33V	-	-	275	mV
Load Population (No	oto1)	ΔVο	T 125°C	1.0mA ≤ I _O ≤ 100mA	-	-	170	mV
Load Regulation (No	ne i)	ΔνΟ	T _J = +25°C	$1.0\text{mA} \le I_O \le 40\text{mA}$	-	-	85	mV
Output Voltage		Vo	$-20.7V > V_I > -33V$, $1.0mA \le I_O \le 40mA$		-17.1	-	-18.9	V
Output voltage		٧٥	V _I = -27V, 1.0mA ≤ I _O ≤ 70mA		-17.1	-	-18.9	V
Ouissant Current		I.o.	T _J = +25°C		-	-	6.5	A
Quiescent Current		lQ	T _J = +125°C		-	-	6.0	mA
Quiescent Current	With Line	ΔlQ	-21V ≥ V _I ≥ -33	V	-	-	1.5	mA
Change	With Load	ΔlQ	1.0mA ≤ I _O ≤ 40mA		-	-	0.1	mA
Output Noise Voltage		VN	T _A =+25°C,10Hz ≤ f ≤ 100kHz		-	150	-	μV
Ripple Rejection RR		$f = 120Hz, -23V \ge VI \ge -33V$ $TJ = +25^{\circ}C$		33	48	-	dB	
Dropout Voltage		VD	T _J = +25°C		-	1.7	-	V

^{1.} Load and line regulation are specified at constant junction temperature. Change in Vo due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics(MC79L24A) (Continued)

(VI = -33V, IO = 40mA, CI = $0.33\mu F$, CO = $0.1\mu F$, $0^{\circ}C \leq TJ \leq +125^{\circ}C$, unless otherwise specified)

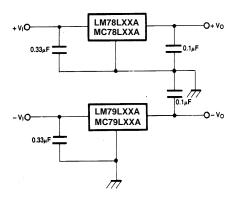
Parameter		Symbol	Conditions		Min.	Тур.	Max.	Unit
Output Voltage		Vo	T _J = +25°C		-23	-24	-25	V
				-27V ≥ V _I ≥ -38V	-	-	350	mV
Line Regulation (Not	te1)	ΔVΟ	$TJ = +25^{\circ}C$ $-28V \ge V_{\parallel} \ge -38V$		-	-	300	mV
Load Regulation (No	sto1)	ΔVο	TJ = +25°C	$1.0\text{mA} \le \text{IO} \le 100\text{mA}$	-	-	200	mV
Load Regulation (NC	ne i)	ΔνΟ	1J = +25 C	$1.0\text{mA} \le \text{Io} \le 40\text{mA}$	-	-	100	mV
Output Voltage		Vo	$-27V \ge V_I \ge -38V$, $1.0mA \le I_O \le 40mA$		-22.8	-	-25.2	V
Output voltage		٧٥	V _I = -33V, 1.0mA ≤ I _O ≤ 70mA		-22.8	-	-25.2	V
Quiescent Current		Io	T _J = +25°C		-	-	6.5	mΛ
Quiescent Current		lQ	T _J = +125°C		-	-	6.0	mA
Quiescent Current	With Line	ΔlQ	-28V ≥ VI ≥ -38	3V	-	-	1.5	mA
Change	With Load	ΔlQ	1.0mA ≤ I _O ≤ 40mA		-	-	0.1	mA
Output Noise Voltage V		VN	$T_A = +25^{\circ}C,10Hz \le f \le 100kHz$		-	200	-	μV
Ripple Rejection		RR	$f = 120Hz, -29V \ge VI \ge -35V$ $TJ = +25^{\circ}C$		31	47	-	dB
Dropout Voltage		VD	TJ = +25°C		-	1.7	-	V

^{1.} Load and line regulation are specified at constant junction temperature. Change in Vo due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Typical Application

Design Considerations

The MC79LXXA/LM79LXXA Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition. Internal Short Circuit Protection that limits the maximum current the circuit will pass. In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. An input bypass capacitor should be selected to provide good high frequency characteristics to insure stable operation under all load conditions. A $0.33\mu F$ or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulator's input terminals. Normally good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead. Bypassing the output is also recommended.



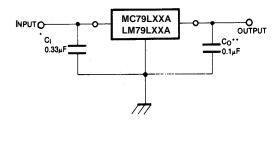


Figure 1. Positive And Negative Regulator

Figure 2. Typical Application

A common ground is required between the Input and the output voltages. The input voltage must remain typically 2.0V above the output voltage even during the low point on the input ripple voltage.

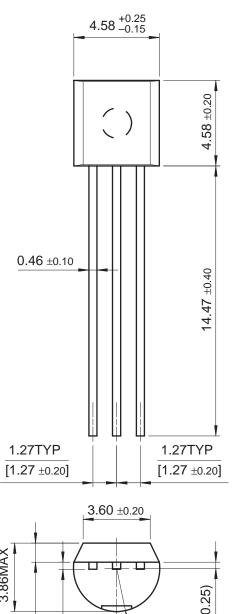
- * C₁ is required if regulator is located an appreciable distance from power supply filter.
- * Co improves stability and transient response.

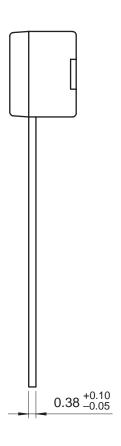
Mechanical Dimensions

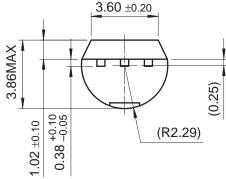
Package

Dimensions in millimeters

TO-92





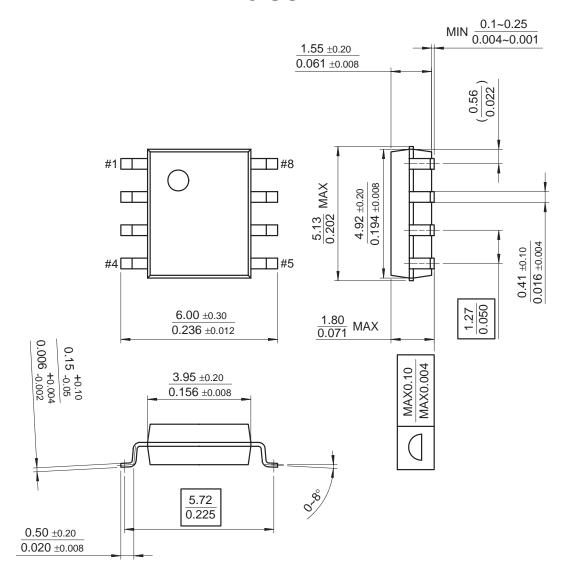


Mechanical Dimensions (Continued)

Package

Dimensions in millimeters

8-SOP

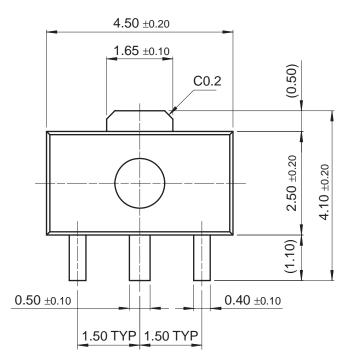


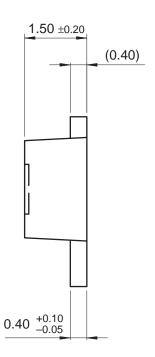
Mechanical Dimensions (Continued)

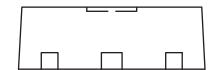
Package

Dimensions in millimeters

SOT-89







Ordering Information

Product Number	Package	Operating Temperature
LM79L05ACZ	TO-92	0 ~ +125°C
Product Number	Package	Operating Temperature
MC79L05ACP		
MC79L08ACP		
MC79L12ACP	TO-92	
MC79L15ACP	10-92	0 ~ +125°C
MC79L18ACP		0~+125 C
MC79L24ACP		
MC79L15ACD	8-SOP	
MC79L05ACH	SOT-89	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.fairchildsemi.com