

LIMITED DATASHEET

Email <u>Analog.Switch@fairchildsemi.com</u> to request the full datasheet.



FSA9285 — MCPC-Compliant, USB-Port, Multimedia Switch with Auto-Detection

Features

Switch Type	Audio, FS/HS-USB, Charging		
Switch Mechanism	Programmable Switching with Available Interrupt		
Accessory Detection	Headsets with MIC and Send/End USB Data Cable USB Chargers (Car, CDP, DCP) USB On-The-Go (OTG) MCPC Specification Compliant Programmable Modes		
USB	FS and HS 2.0 Compliant		
USB Charging	Battery Charging 1.2 Compliant Integrated FET, Charger Detect, OCP (1.45A), OVP (6.5V - 28.0V)		
Audio	Left, Right, MIC (Negative Swing) Built-in Termination Resistors for Audio Pop Reduction		
V_{BAT}	2.7 to 4.4V		
Programmability	I ² C		
ESD	15kV IEC 61000-4-2 Air Gap		
Package	20-Lead, WLCSP (2.010 x 1.672 x 0.625mm, 0.4mm Pitch)		
Ordering Information	FSA9285UCX		

Description

The FSA9285 is a high-performance multimedia switch featuring automatic switching and accessory detection for a USB port. The FSA9285 allows sharing of a common USB port to pass audio and USB data while simultaneously charging.

In addition, the FSA9285 integrates detection of accessories such as headphones, headsets Mobile Computing Promotion Consortium (MCPC) with MIC and Send/End, car chargers, USB chargers, USB On-The-Go (OTG), and Accessory Charging Adapters (ACA) to use a common USB connector. The FSA9285 can be programmed for manual or automatic switching of USB data paths based on the accessory detected. With an integrated 28V over-voltage and 1.45A over-current protected FET, the FSA9285 integrates common USB protection functions for V_{BUS} .

Applications

Mobile Phones, Portable Media Players

For additional performance information, please contact <u>analogswitch@fairchildsemi.com.</u>

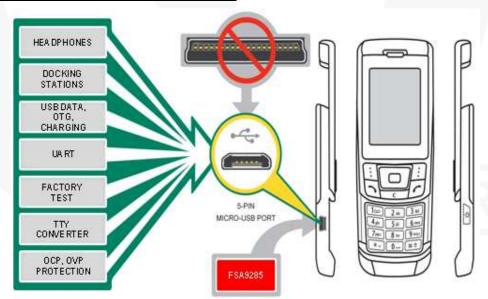
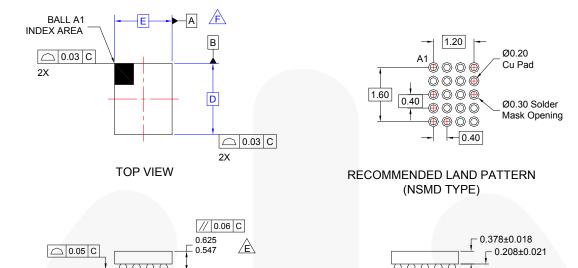
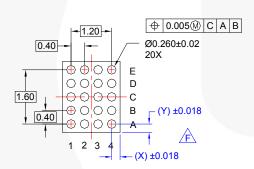


Figure 1. Typical Application

Physical Dimensions





SEATING PLANE

BOTTOM VIEW

NOTES:

SIDE VIEWS

- A. NO JEDEC REGISTRATION APPLIES.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCE PER ASMEY14.5M, 1994.
- D. DATUM C IS DEFINED BY THE SPHERICAL CROWNS OF THE BALLS.
- E PACKAGE NOMINAL HEIGHT IS 586 MICRONS ±39 MICRONS (547-625 MICRONS).
- FOR DIMENSIONS D, E, X, AND Y SEE PRODUCT DATASHEET.
- G. DRAWING FILNAME: MKT-UC020AArev2.

Figure 14. 20-Lead, Wafer-Level Chip-Scale Package (WLCSP)

Product-Specific Dimensions

С

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Product	D	E	X	Y
FSA9285UCX	2.010mm	1.672mm	0.236mm	0.205mm

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

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Part Number	Operating Temperature Range	Top Mark	Package
FSA9285UCX	-40 to +85°C	TBD	20-Lead, WLCSP (2.010 x 1.672 x 0.625mm, 0.4mm Pitch)





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Definition of Terms

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Datasheet Identification	Product Status	Definition
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