



FGD3N60LSD IGBT

Features

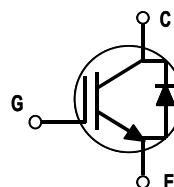
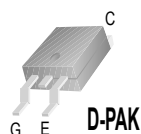
- High Current Capability
- Very Low Saturation Voltage : $V_{CE(sat)} = 1.2 \text{ V @ } I_C = 3 \text{ A}$
- High Input Impedance

Applications

- HID Lamp Applications
- Piezo Fuel Injection Applications

Description

Fairchild's Insulated Gate Bipolar Transistors (IGBTs) provide very low conduction losses. The device is designed for applications where very low On-Voltage Drop is a required feature.



Absolute Maximum Ratings

Symbol	Description	FGD3N60LSD	Units
V_{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	± 25	V
I_C	Collector Current @ $T_C = 25^\circ\text{C}$	6	A
	Collector Current @ $T_C = 100^\circ\text{C}$	3	A
$I_{CM(1)}$	Pulsed Collector Current (1)	25	A
I_F	Diode Continuous Forward Current @ $T_C = 100^\circ\text{C}$	3	A
I_{FM}	Diode Maximum Forward Current	25	A
P_D	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	40	W
	Derating Factor	0.32	W/ $^\circ\text{C}$
T_J	Operating Junction Temperature	-55 to +150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 Seconds	250	$^\circ\text{C}$

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}$ (IGBT)	Thermal Resistance, Junction-to-Case	--	3.1	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (PCB Mount) (2)	--	100	$^\circ\text{C/W}$

Notes :

(2) Mounted on 1" square PCB (FR4 or G-10 Material)

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGD3N60LSD	FGD3N60LSDTM	D-PAK	380mm	16mm	2500

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250uA	600	--	--	V
ΔBV _{CES} /ΔT _J	Temperature Coefficient of Breakdown Voltage	V _{GE} = 0V, I _C = 1mA	--	0.6	--	V/°C
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0V	--	--	250	uA
I _{GES}	G-E Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0V	--	--	± 100	nA
On Characteristics						
V _{GE(th)}	G-E Threshold Voltage	I _C = 3mA, V _{CE} = V _{GE}	2.5	3.2	5.0	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 3A, V _{GE} = 10V	--	1.2	1.5	V
		I _C = 6A, V _{GE} = 10V	--	1.8	--	V
Dynamic Characteristics						
C _{ies}	Input Capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz	--	185	--	pF
C _{oes}	Output Capacitance		--	20	--	pF
C _{res}	Reverse Transfer Capacitance		--	5.5	--	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{CC} = 480 V, I _C = 3A, R _G = 470Ω, V _{GE} = 10V, Inductive Load, T _C = 25°C	--	40	--	ns
t _r	Rise Time		--	40	--	ns
t _{d(off)}	Turn-Off Delay Time		--	600	--	ns
t _f	Fall Time		--	600	--	ns
E _{on}	Turn-On Switching Loss		--	250	--	uJ
E _{off}	Turn-Off Switching Loss		--	1.00	--	mJ
E _{ts}	Total Switching Loss		--	1.25	--	mJ
t _{d(on)}	Turn-On Delay Time	V _{CC} = 480 V, I _C = 3A, R _G = 470Ω, V _{GE} = 10V, Inductive Load, T _C = 125°C	--	40	--	ns
t _r	Rise Time		--	45	--	ns
t _{d(off)}	Turn-Off Delay Time		--	620	--	ns
t _f	Fall Time		--	800	--	ns
E _{on}	Turn-On Switching Loss		--	300	--	uJ
E _{off}	Turn-Off Switching Loss		--	1.9	--	mJ
E _{ts}	Total Switching Loss		--	2.2	--	mJ
Q _g	Total Gate Charge	V _{CE} = 480 V, I _C = 3A, V _{GE} = 10V	--	12.5	--	nC
Q _{ge}	Gate-Emitter Charge		--	2.8	--	nC
Q _{gc}	Gate-Collector Charge		--	4.9	--	nC
L _e	Internal Emitter Inductance	Measured 5mm from PKG	--	7.5	--	nH

Electrical Characteristics of DIODE $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Units
V _{FM}	Diode Forward Voltage	I _F = 3A	T _C = 25°C	--	1.5	1.9	V
			T _C = 100°C	--	1.55	--	
t _{rr}	Diode Reverse Recovery Time	I _F = 3A, di/dt = 100A/us V _R = 200V	T _C = 25°C	--	234	--	ns
			T _C = 100°C	--	--	--	
I _{rr}	Diode Peak Reverse Recovery Current		T _C = 25°C	--	2.64	--	A
			T _C = 100°C	--	--	--	
Q _{rr}	Diode Reverse Recovery Charge		T _C = 25°C	--	309	--	nC
			T _C = 100°C	--	--	--	

Typical Performance Characteristics

Figure 1. Typical Output Characteristics

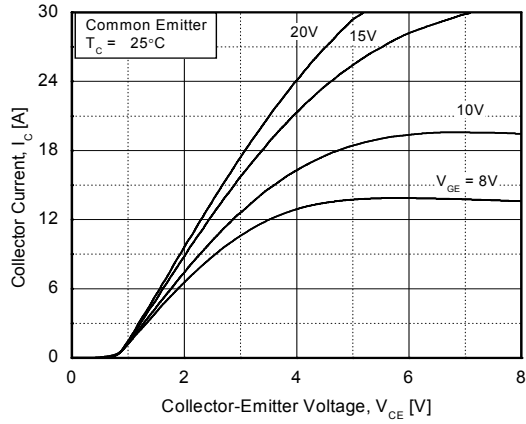


Figure 2. Typical Output Characteristics

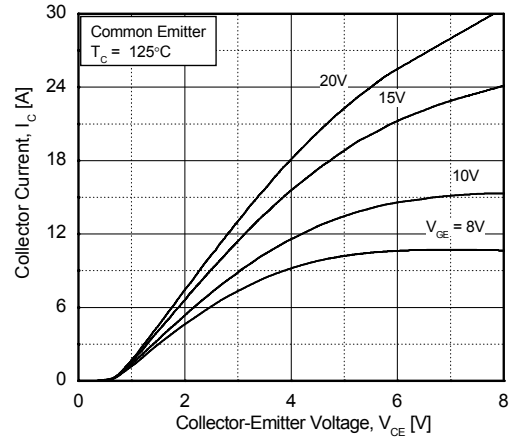


Figure 3. Typical Output Characteristics

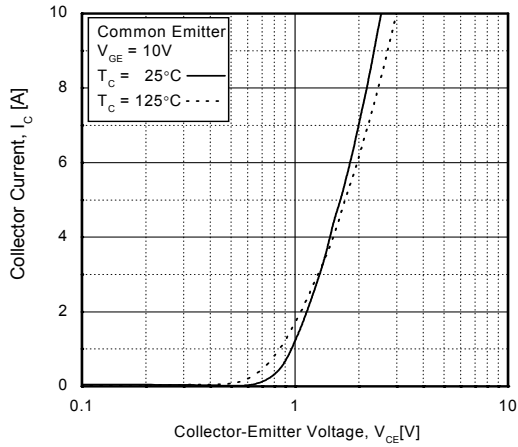


Figure 4. Transfer Characteristics

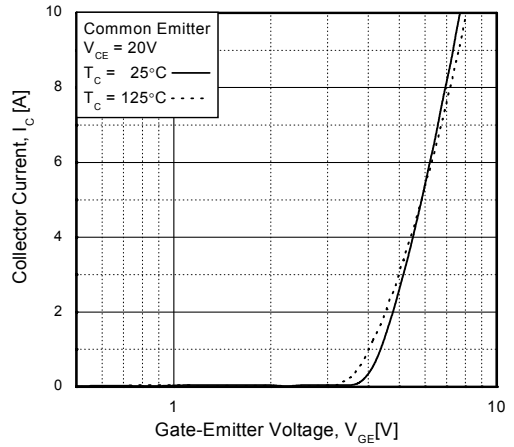


Figure 5. Saturation Voltage vs. Case

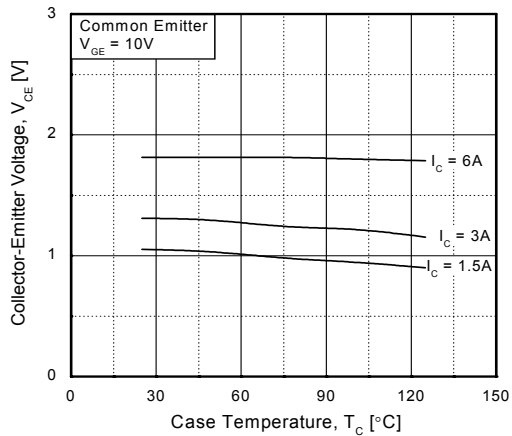
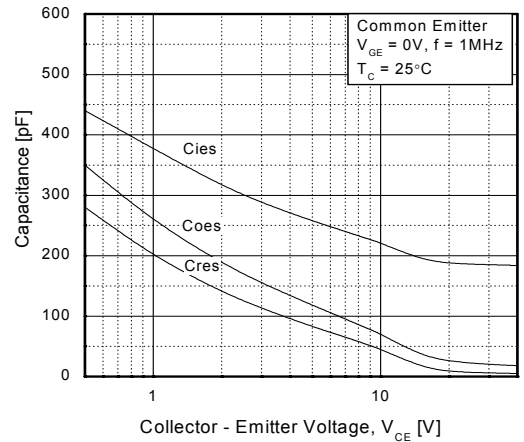


Figure 6. Capacitance Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Gate Charge

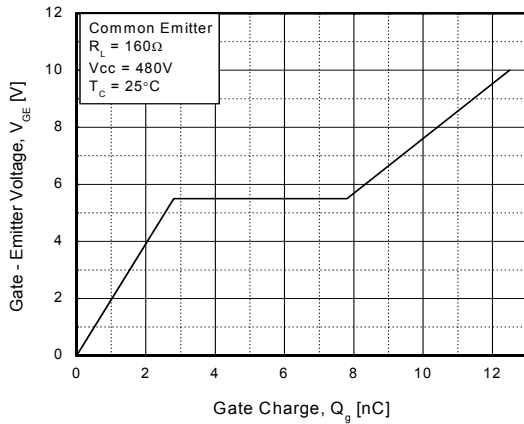


Figure 8. Turn-On Characteristics vs. Gate Resistance

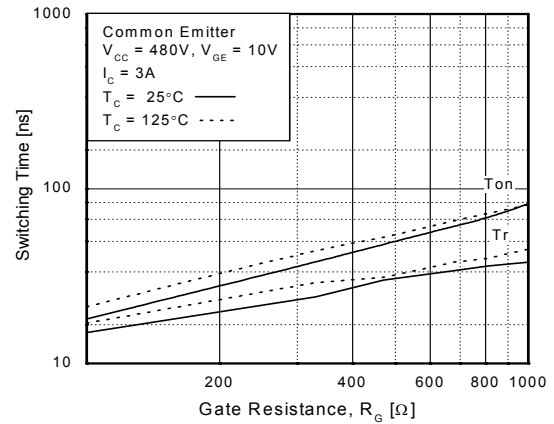


Figure 9. Turn-Off Characteristics vs. Gate Resistance

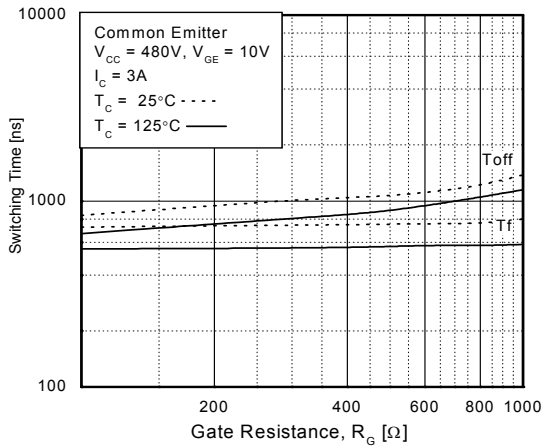


Figure 10. Switching Loss vs. Gate Resistance

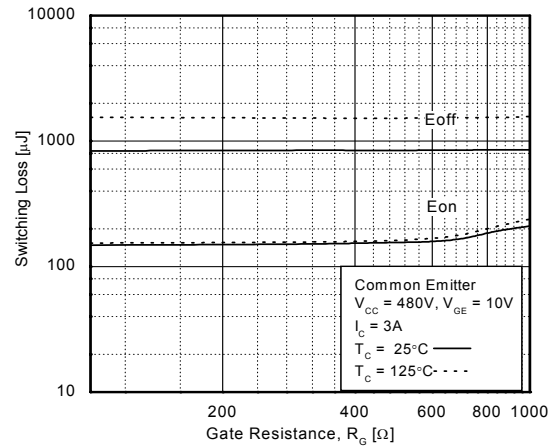


Figure 11. Turn-On Characteristics vs. Collector Current

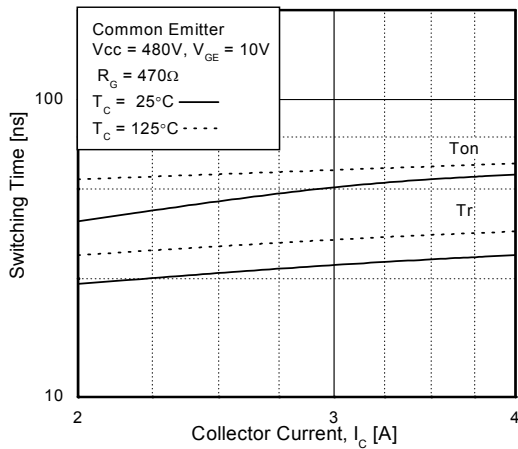
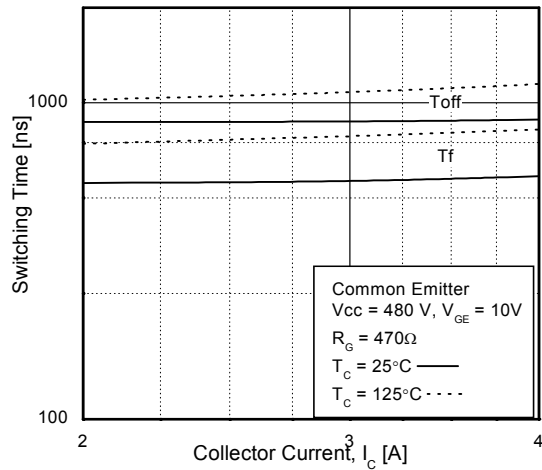


Figure 12. Turn-Off Characteristics vs. Collector Current



Typical Performance Characteristics (Continued)

Figure 13. Switching Loss vs. Collector Current

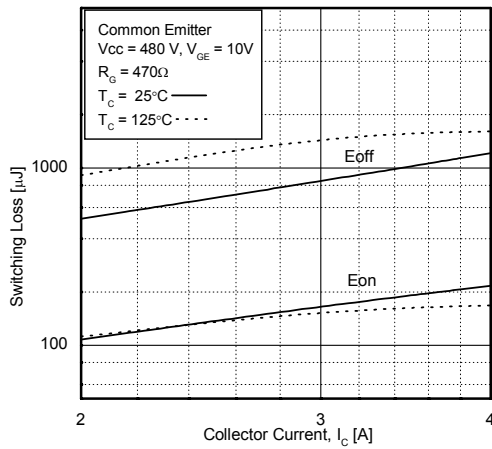


Figure 14. Forward Characteristics

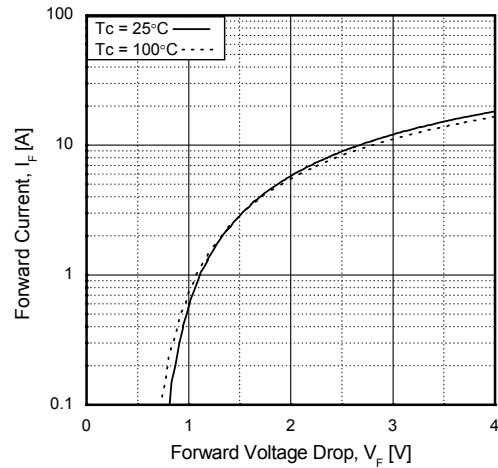


Figure 15. Forward Voltage Drop Vs Tj

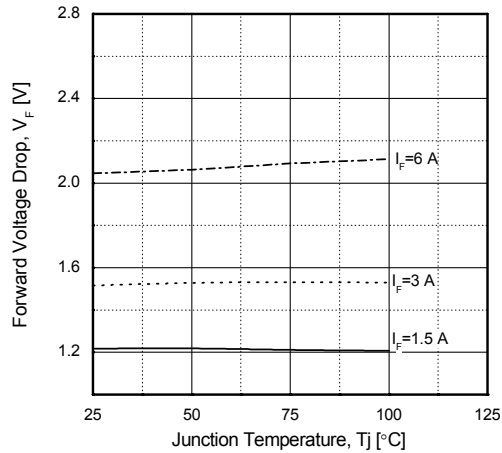


Figure 16. SOA Characteristics

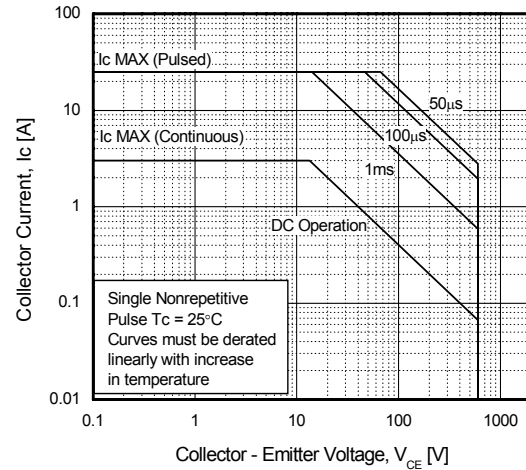
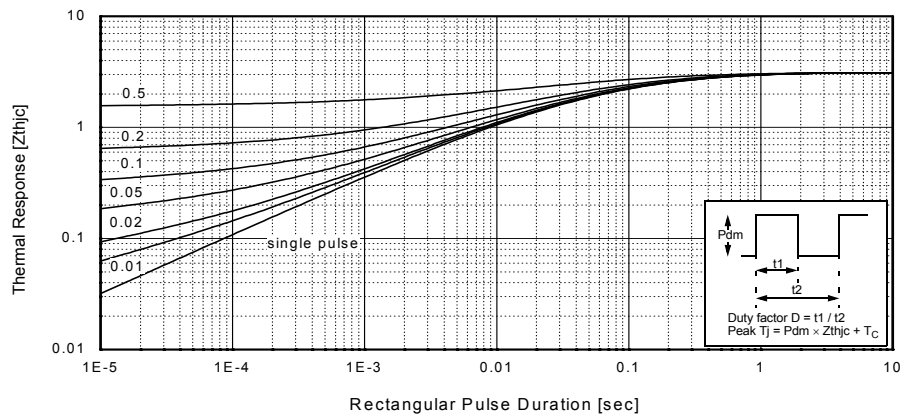
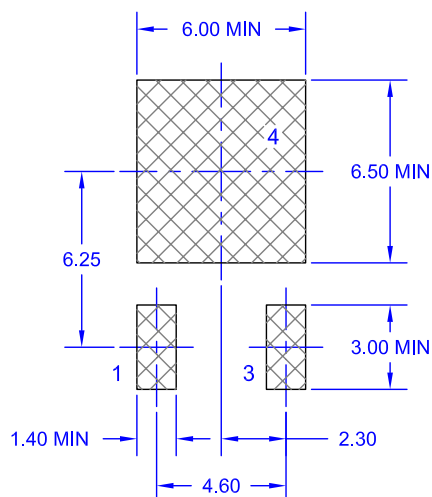
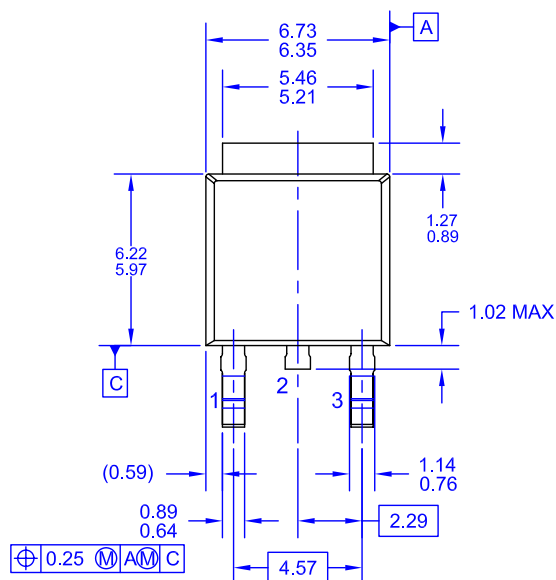


Figure 17. Transient Thermal Impedance of IGBT

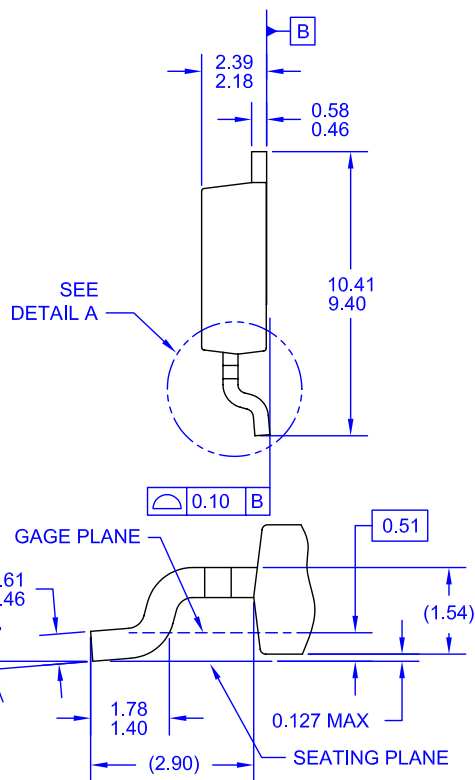
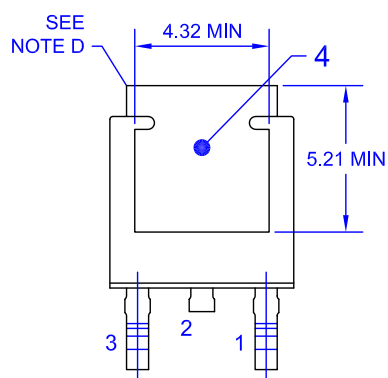


Mechanical Dimensions

D-PAK



LAND PATTERN RECOMMENDATION



NOTES: UNLESS OTHERWISE SPECIFIED

A) THIS PACKAGE CONFORMS TO JEDEC, TO-252, ISSUE C, VARIATION AA.

B) ALL DIMENSIONS ARE IN MILLIMETERS.

C) DIMENSIONING AND TOLERANCING PER
ASME Y14.5M-1994.

D) HEAT SINK TOP EDGE COULD BE IN CHAMFERED CORNERS OR EDGE PROTRUSION.

E) PRESENCE OF TRIMMED CENTER LEAD IS OPTIONAL.

F) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS.

G) LAND PATTERN RECOMENDATION IS BASED ON IPC7351A STD TO220P1003X238-3N.

H) DRAWING NUMBER AND REVISION: MKT-TO252A03REV8

Dimensions in Millimeters

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