

October 2008
UniFET<sup>TM</sup>

# FDP15N40 / FDPF15N40 N-Channel MOSFET 400V, 15A, $0.3\Omega$

#### **Features**

- $R_{DS(on)} = 0.24\Omega$  (Typ.)@  $V_{GS} = 10V$ ,  $I_D = 7.5A$
- Low Gate Charge (Typ. 28nC)
- Low C<sub>ss</sub> (Typ. 17pF)
- Fast Switching
- 100% Avalanche Tested
- · Improved dv/dt Capability
- RoHS Compliant



## **Description**

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pluse in the avalanche and commutation mode. These devices are well suited for high efficient switched mode power supplies and active power factor correction.



# **MOSFET Maximum Ratings** $T_C = 25^{\circ}C$ unless otherwise noted\*

Symbol		Parameter		FDP15N40	FDPF15N40	Units
$V_{DSS}$	Drain to Source Voltage			4	V	
$V_{GSS}$	Gate to Source Voltage			±30		V
	Drain Current	-Continuous (T <sub>C</sub> = 25°C)		15	15*	۸
ID	Dialii Current	-Continuous (T <sub>C</sub> = 100°C)		9	9*	Α
I <sub>DM</sub>	Drain Current	1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3		60 60*		Α
E <sub>AS</sub>	Single Pulsed Avalanche Energy (I		(Note 2)	731		mJ
I <sub>AR</sub>	Avalanche Current		(Note 1)	15		Α
E <sub>AR</sub>	Repetitive Avalanche Energy		(Note 1)	17		mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	15		V/ns
Б	Dower Dissipation	(T <sub>C</sub> = 25°C)		170	40	W
$P_{D}$	Power Dissipation  - Derate above 25°C			1.45	0.3	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to	+150	°C	
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			3	00	°C

\*Drain current limited by maximum junction temperature

#### **Thermal Characteristics**

Symbol	Parameter	FDP15N40	FDPF15N40	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.7	3.0	
$R_{\theta CS}$	Thermal Resistance, Case to Sink Typ. 0.5 -			
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62.5	62.5	

# Package Marking and Ordering Information $T_C = 25^{\circ}C$ unless otherwise noted

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP15N40	FDP15N40	TO-220	-	-	50
FDPF15N40	FDPF15N40	TO-220F	-	-	50

#### **Electrical Characteristics**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	eteristics					
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$I_D = 250\mu A, V_{GS} = 0V, T_J = 25^{\circ}C$	400	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C	-	0.5	-	V/°C
	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 400V, V <sub>GS</sub> = 0V	-	-	1	
IDSS	Zero Gate voltage Drain Current	$V_{DS} = 320V, T_C = 125^{\circ}C$	-	-	10	μΑ
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	±100	nA

#### **On Characteristics**

V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	3.0	=	5.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 7.5A$	-	0.24	0.3	Ω
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 20V, I_D = 7.5A$ (Note 4)	-	15.3	-	S

### **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V 05V V 0V	-	1310	1750	pF
C <sub>oss</sub>	Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V$ f = 1MHz		210	280	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 - 1101112	-	17	25	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10V		-	28	36	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	$V_{DS} = 320V, I_{D} = 15A$	-	8	-	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge	V <sub>GS</sub> = 10V (Note 4, 5)	-	12	-	nC

#### **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time			-	26	62	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD} = 200V, I_{D} = 15A$		-	55	120	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$R_G = 25\Omega$		-	72	154	ns
t <sub>f</sub>	Turn-Off Fall Time		(Note 4, 5)	-	40	90	ns

#### **Drain-Source Diode Characteristics**

I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current			-	-	15	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current			-	-	60	Α
$V_{SD}$	Drain to Source Diode Forward Voltage V <sub>GS</sub> = 0V, I <sub>SD</sub> = 15A		-	-	1.4	V	
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>SD</sub> = 15A		-	333	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$	(Note 4)	-	3.24	-	μС

- Notes: 1: Repetitive Rating: Pulse width limited by maximum junction temperature 2: L = 6.5mH,  $I_{AS}$  = 15A,  $V_{DD}$  = 50V,  $R_G$  = 25 $\Omega$ , Starting  $T_J$  = 25 $^{\circ}$ C 3:  $I_{SD}$  ≤ 15A, di/dt ≤ 200A/ $\mu$ s,  $V_{DD}$  ≤ BV $_{DSS}$ , Starting  $T_J$  = 25 $^{\circ}$ C 4: Pulse Test: Pulse width ≤ 300 $\mu$ s, Duty Cycle ≤ 2% 5: Essentially Independent of Operating Temperature Typical Characteristics

# **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

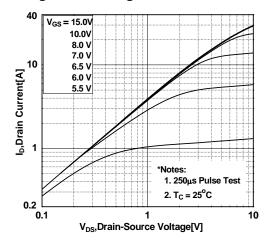


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

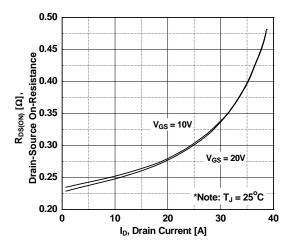


Figure 5. Capacitance Characteristics

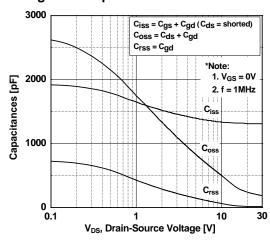


Figure 2. Transfer Characteristics

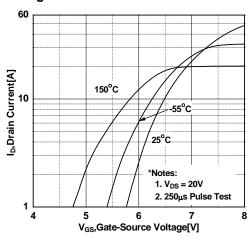


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

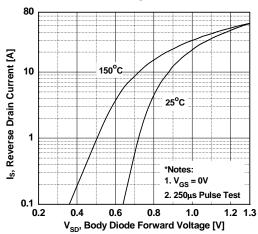
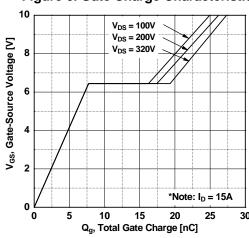


Figure 6. Gate Charge Characteristics



# **Typical Performance Characteristics (Continued)**

Figure 7. Breakdown Voltage Variation vs. Temperature

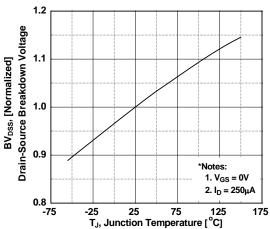


Figure 8. On-Resistance Variation vs. Temperature

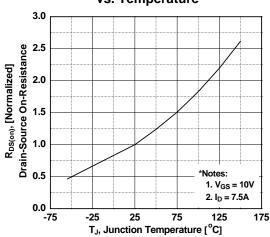
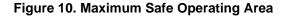
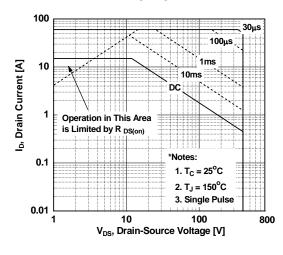


Figure 9. Maximum Safe Operating Area - FDP15N40





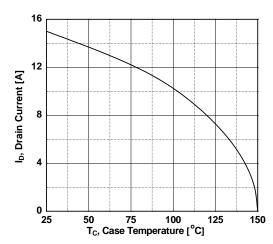
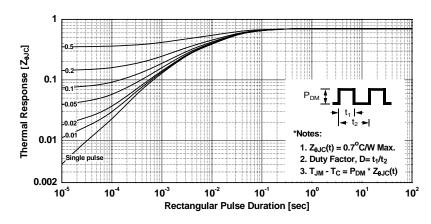
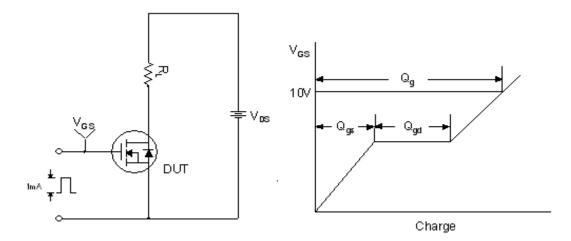


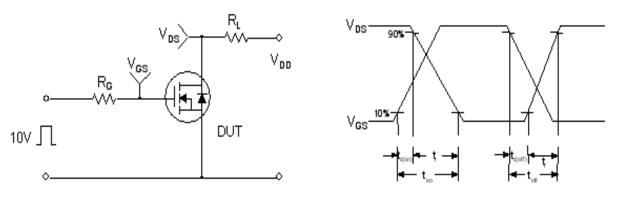
Figure 11. Transient Thermal Response Curve - FDP15N40



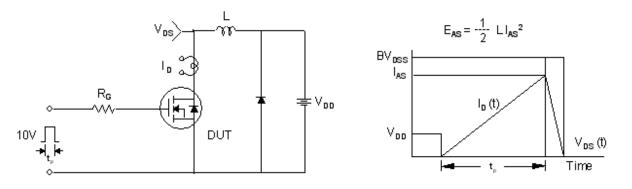
#### **Gate Charge Test Circuit & Waveform**



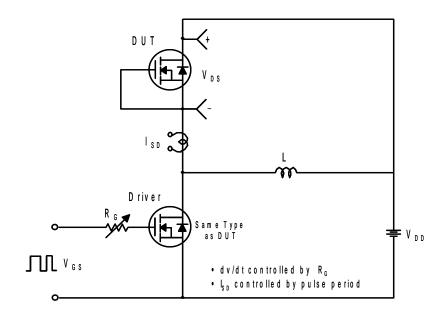
#### **Resistive Switching Test Circuit & Waveforms**

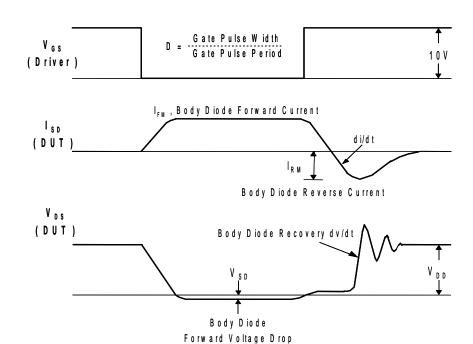


#### **Unclamped Inductive Switching Test Circuit & Waveforms**



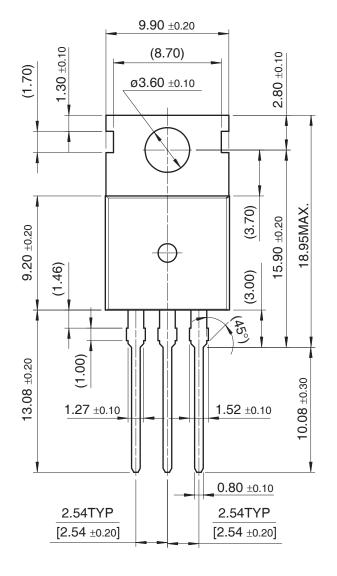
#### Peak Diode Recovery dv/dt Test Circuit & Waveforms

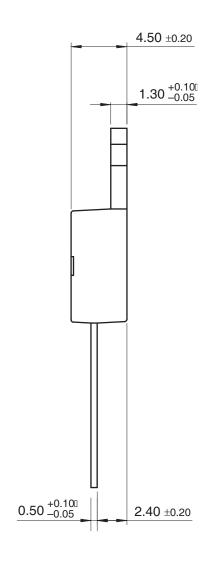




# **Mechanical Dimensions**

# TO-220

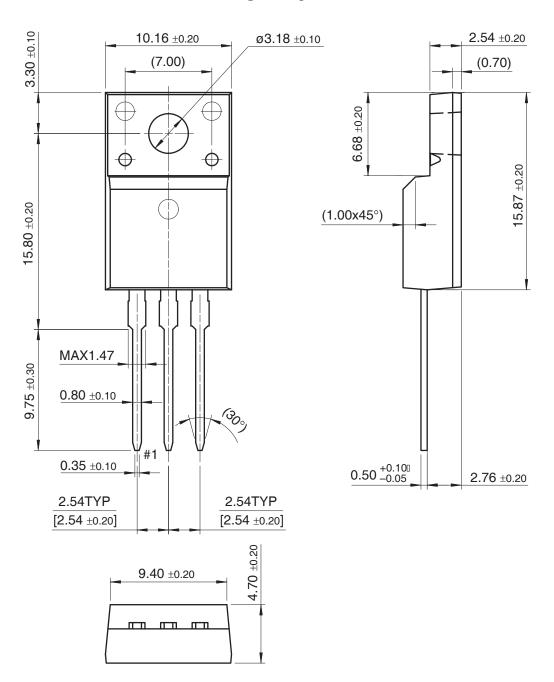






# **Mechanical Dimensions**

# TO-220F



Dimensions in Millimeters





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