

April 2011

FDH055N15A

N-Channel PowerTrench[®] MOSFET 150V, 167A, 5.9m Ω

Features

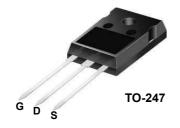
- $R_{DS(on)}$ = 4.8m Ω (Typ.)@ V_{GS} = 10V, I_D = 120A
- · Fast Switching Speed
- · Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(\text{on})}$
- · High Power and Current Handling Capability
- · RoHS Compliant

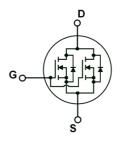
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Application

- DC to DC Converters
- · Synchronous Rectification for Server/Telecom PSU
- · Battery Charger
- · AC motor drives and Uninterruptible Power Supplies
- · Off-line UPS





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol		Parameter	Ratings	Units
V _{DSS}	Drain to Source Voltage		150	V
V _{GSS}	Gate to Source Voltage		±20	V
		- Continuous (T _C = 25°C, Silicon Limite	ed) 167*	
I _D	Drain Current	- Continuous (T _C = 100°C, Silicon Limit	ted) 118	Α
	- Continuous (Tc = 25°C, Package Lim	ited) 156		
I _{DM}	Drain Current	- Pulsed (No	te 1) 668	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2,6)		2,6) 835	mJ
dv/dt	Peak Diode Recovery dv/d	Peak Diode Recovery dv/dt (Note 3)		V/ns
П	Dawer Dissination	$(T_C = 25^{\circ}C)$	429	W
P _D Power Dis	Power Dissipation	- Derate above 25°C	2.86	W/°C
T _J , T _{STG}	Operating and Storage Ter	-55 to +175	°C	
T _L	Maximum Lead Temperatu 1/8" from Case for 5 Second	300	°C	

^{*}Calculated continuous current based on maximum allowable junction temperature, Package limitation current is 156A.

Thermal Characteristics

Symbol	Parameter	Ratings	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.35	
$R_{\theta CS}$	Thermal Resistance, Case to Heat Sink (Typical) 0.24		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	40	

Units

Max.

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDH055N15A	FDH055N15A	TO-247	-	-	30

Test Conditions

Min.

Тур.

Electrical Characteristics T_C = 25°C unless otherwise noted **Parameter**

Off Characteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	150	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.1	-	V/°C
ı	Zero Gate Voltage Drain Current	V _{DS} = 120V, V _{GS} = 0V	-	-	1	μА
I _{DSS} Zero Gate voltage Drain	Zero Gate Voltage Drain Current	$V_{DS} = 120V, T_{C} = 150^{\circ}C$	-	-	500	μΑ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

Symbol

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2.0	-	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 120A$	-	4.8	5.9	mΩ
9 _{FS}	Forward Transconductance	$V_{DS} = 10V, I_D = 120A$ (Note 4)	-	219	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 75V, V _{GS} = 0V f = 1MHz		-	7100	9445	pF
Coss	Output Capacitance			-	664	885	pF
C _{rss}	Reverse Transfer Capacitance			-	23	-	pF
C _{oss(er)}	Energy Related Output Capacitance	V _{DS} = 75V, V _{GS} = 0V		-	1159	-	pF
Q _{g(tot)}	Total Gate Charge at 10V			-	92	120	nC
Q_{gs}	Gate to Source Gate Charge $V_{DS} = 75V$, $I_D = 120A$		-	31	-	nC	
Q _{gs2}	Gate Charge Threshold to Plateau	V _{GS} = 10V		-	15	-	nC
Q_{gd}	Gate to Drain "Miller" Charge	(Note	e 4,5)	-	16	-	nC
ESR	Equivalent Series Resistance(G-S)	Drain Open		-	1.2	-	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	35	80	ns
t _r	Turn-On Rise Time	V _{DD} = 75V, I _D = 120A	-	67	144	ns
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10V, R_{GEN} = 4.7 Ω	-	71	152	ns
t _f	Turn-Off Fall Time	(Note 4,5)	-	21	52	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current			-	167*	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current			-	668	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 120A	-	-	1.25	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 120A, V _{DS} = 75V	-	105	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$ (Note 4)	-	342	-	nC

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Starting T_J = 25°C, L = 3 mH, I_{AS} = 23.6 A.
- 3. $I_{SD} \leq$ 120A, di/dt \leq 200A/ μ s, $V_{DD} \leq$ BV $_{DSS}$, Starting T_J = 25°C.
- 4. Pulse Test: Pulse width $\leq 300 \mu s$, Duty Cycle $\leq 2\%$.
- 5. Essentially Independent of Operating Temperature Typical Characteristics.
- 6. Single Pulsed Avalanche Energy per Die.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

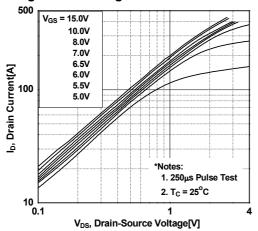


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

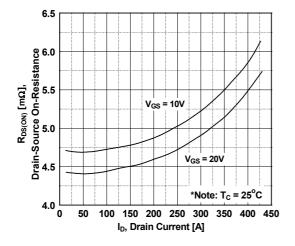


Figure 5. Capacitance Characteristics

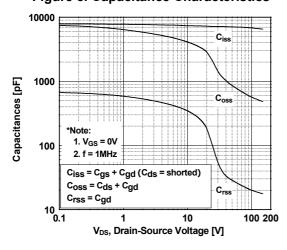


Figure 2. Transfer Characteristics

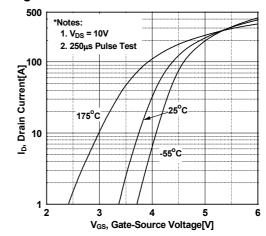


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

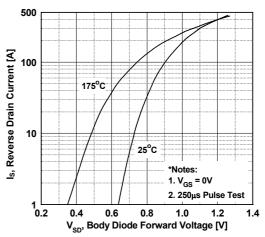
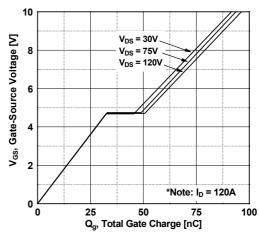


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

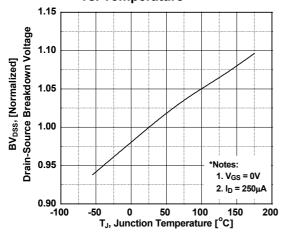


Figure 9. Maximum Safe Operating Area

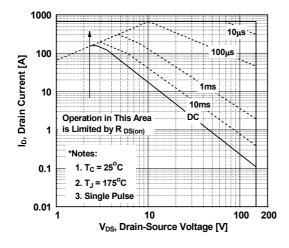


Figure 11. Eoss vs. Drain to Source Voltage

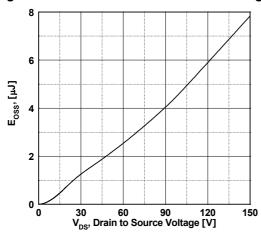


Figure 8. On-Resistance Variation vs. Temperature

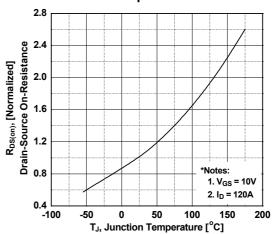
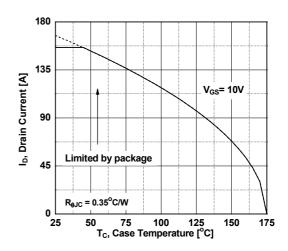


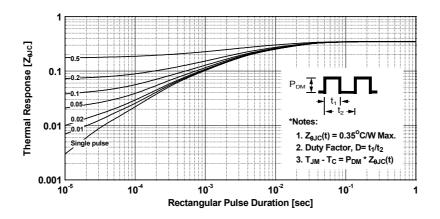
Figure 10. Maximum Drain Current vs. Case Temperature



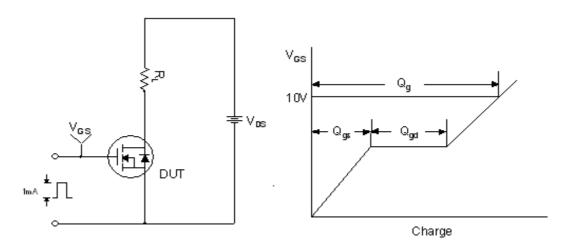
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Typical Performance Characteristics (Continued)

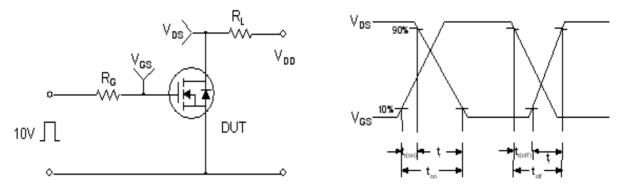
Figure 12. Transient Thermal Response Curve



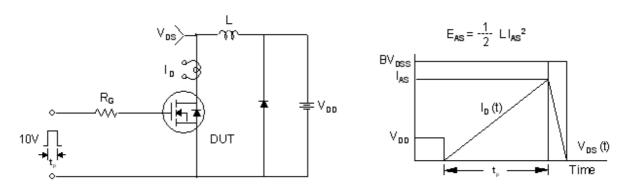
Gate Charge Test Circuit & Waveform



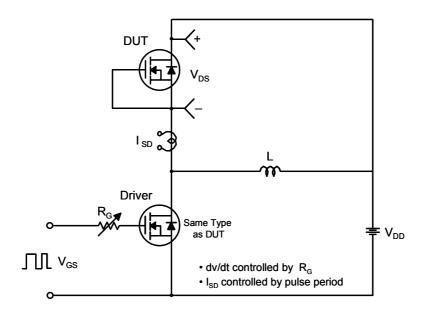
Resistive Switching Test Circuit & Waveforms

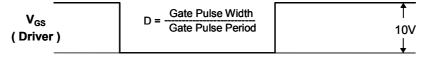


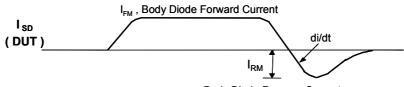
Unclamped Inductive Switching Test Circuit & Waveforms



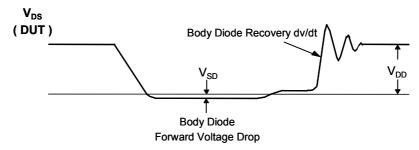
Peak Diode Recovery dv/dt Test Circuit & Waveforms





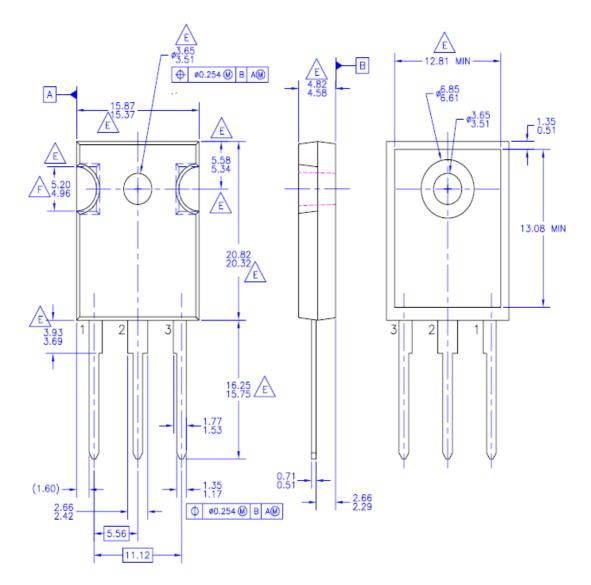


Body Diode Reverse Current



Mechanical Dimensions

TO-247-3L



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Dimensions in Millimeters





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