IAR SYSTEMS

IAR J-Link Ultra for ARM

IAR J-link Ultra debug probes enables power debugging

J-Link Ultra for ARM is a JTAG/SWD debug probe designed for ARM/Cortex and other supported CPUs. It is fully compatible to the standard J-Link and works with the same PC software.

Based on the highly optimized and proven J-Link, it offers even higher speed as well as target power measurement capabilities due to the faster CPU, built-in FPGA and High Speed USB interface. It connects via USB to a PC running Windows XP, Windows 2003, Windows Vista or Windows 7. J-Link Ultra has a built-in 20-pin JTAG/SWD connector.

J-Link Ultra debug probe. Designed for use with ARM Cortex devices, this JTAG/SWD probe supports IAR Systems' pioneering power debugging technology available within IAR Embedded Workbench. Power debugging couples power consumption to the application code and can help users extend battery life time in embedded systems. This new dimension of software debugging is innovated by IAR Systems.

Key Features

- Fully compatible to the standard J-Link
- Very high performance for all supported CPU cores
- Hi-speed USB 2.0 interface
- Serial Wire Debug (SWD) supported
- Serial Wire Viewer (SWV) supported
- SWV: UART and Manchester encoding supported
- SWO frequency up to 25 MHz
- Target power can be supplied
- Target power consumption can be measured with high accuracy (up to approximately 50 kHz)
- Direct download into flash memory of most popular microcontrollers supported
- Any ARM7/ARM9/ARM11, Cortex-M0/M1/M3/M4/R4 core support, including thumb mode
- Automatic core recognition
- JTAG speed up to 25 MHz
- Seamless integration into the IAR Embedded Workbench IDE
- No power supply required, powered through USB
- Support for adaptive clocking
- All JTAG signals can be monitored, target voltage can be measured
- Support for multiple devices
- Fully plug and play compatible
- Standard 20-pin JTAG connector
- Wide target voltage range: 1.8V – 5.0V
- USB and 20-pin ribbon cable included
- TCP/IP server included, which allows using J-Link via TCP/IP networks
- Embedded Trace Buffer (ETB) support
- Target power supply: J-Link can supply up to 300 mA to target with overload protection

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