

Description

The LM4041 is a bandgap circuit designed to achieve a precision micro-power voltage reference of 1.225 V. The device is available in the small outline SOT23 and SC70-5 surface mount packages which are ideal for applications where space saving is important.

Both packages are available to 0.5% C grade and 1% D grade for precision applications. Excellent performance is maintained over the 60µA to 12mA operating current range with a typical temperature coefficient of only 20ppm/℃. The device has been designed to be highly tolerant of capacitive loads so maintaining excellent stability.

This device offers a pin for pin compatible alternative to the LM4041 voltage reference in both adjustable and 1.225V output variants.

Features

Small packages: SOT23, SC70-5

No output capacitor required

Output voltage tolerance

LM4041C: ±0.5% at 25℃
 LM4041D: ±1% at 25℃
 Low output noise: 20µVrms

(10Hz to 10kHz)

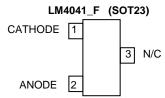
Wide operating current range: 60µA to 12mA
 Extended temperature range: -40℃ to +125℃
 Low temperature coefficient: 100ppm/℃ (max)

All parts AEC-Q100 Grade1 qualified

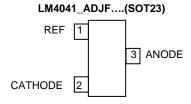
Applications

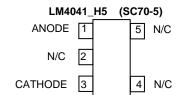
- Battery powered equipment
- Precision power supplies
- Portable instrumentation
- Portable communications devices
- · Notebook and palmtop computers
- · Data acquisition systems

Pin Assignments

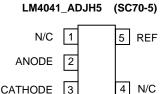


Pin 3 must left floating or connected to pin 2.





Pin 2 must be left floating or connected to pin 1





Absolute Maximum Ratings

Description	Rating	Unit
Continuous Reverse Current (I _R)	20	mA
Continuous Forward Current (I _F)	10	mA
Maximum Output Voltage (LM4041_ADJ)	15	V
Junction Temperature	-40 to 155	C
Storage Temperature	-55 to 150	C
ESD Ratings		
Human Body Model	4000	V
Machine Model	200	V

These are stress ratings only. Operation outside the absolute maximum ratings may cause device failure. Operation at the absolute maximum rating, for extended periods may reduce device reliability.

Package Thermal Data

Package	θ _{JA}	P _{DIS} T _A = 25℃, T _J = 150℃		
SOT23	380℃/W	330mW		
SC70-5	420℃/W	300mW		

Recommended Operating Conditions

Parameter	Min	Max	Units
Reverse Current	0.06	12	mA
Output Voltage Range	1.24	10	V
Operating Ambient Temperature Range	-40	125	°C

Semiconductor devices are ESD sensitive and may be damaged by exposure to ESD events. Suitable ESD precautions should be taken when handling and transporting these devices.



Electrical Characteristics

LM4041-1.2

Electrical characteristics over recommended operating conditions, $T_A = 25^{\circ}C$, unless otherwise stated, $I_{RMIN} \le I_R \le 12mA$, $V_{REF} \le V_{OUT} \le 10V$. LM4041C and LM4041D have initial tolerances of 0.5% and 1% respectively.

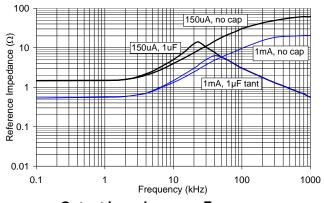
Cumbal	Dorometer	Conditions		T	LM4041C	LM4041D	l luita	
Symbol	Parameter	T _A		Тур	Limits	Limits	Units	
	Reverse Breakdown Voltage		25℃	1.225			V	
V_{REF}	Reverse Breakdown Voltage	I _R = 100 μA	25℃		±6	±12		
V KEF	Tolerance	ΙΚ = 100 μ/τ	-40 to 85℃		±14	±24	mV	
	Tolerance		-40 to 125℃		±18.4	±31		
			25℃		60	65	μA	
I _{RMIN}	Minimum Operating Current		-40 to 85℃	45	65	70		
			-40 to 125℃		68	73		
	Average Reverse Breakdown Voltage Temperature Coefficient	$I_R = 10 \text{ mA}$		±20			ppm/℃	
$\Delta V_R/\Delta T$		$I_R = 1 \text{ mA},$	-40 to 125℃	±15	±100	±150		
		$I_R = 100 \mu A$		±15				
	Reverse Breakdown Change With	$I_{RMIN} < I_{R} < 1mA$	25℃		1.5	2.0	mV	
			-40 to 85℃	0.7	2.0	2.5		
۸۱/۵/۸۱۵			-40 to 125℃		2.0	2.5		
$\Delta V_R/\Delta I_R$	Current		25℃		6.0	8.0	111 V	
		1mA < I _R < 12 mA	-40 to 85℃	2.5	8.0	10.0		
			-40 to 125℃		8.0	10.0		
Z_R	Dynamic Output Impedance	$I_R = 1 \text{mA}$, $f = 120 \text{Hz}$, $I_{AC} = 0.1 I_R$		0.5	1.5	2.0	Ω	
en	Noise Voltage	$I_R = 100 \mu A$ $10 Hz < f < 10 kHz$		20			μV_{RMS}	
ΔV_R	Long Term Stability (Non cumulative)	$t = 1000 Hrs$ $I_R = 100 \mu A$					ppm	

LM4041-Adj

Electrical characteristics over recommended operating conditions, $T_A = 25^{\circ}\text{C}$, $I_{\text{RMIN}} \leq I_{\text{R}} \leq 12$ mA, $V_{\text{REF}} \leq V_{\text{OUT}} \leq 10\text{V}$ unless otherwise stated. The grade D designates initial reference voltage tolerance of $\pm 1\%$ and is measured at an output/cathode voltage of 5V.

		Condi		LM4041D	l luite	
Symbol	Parameter		T _A	Тур	Limits	Units
•	Reverse Breakdown Voltage		25℃	1.233		V
V_{REF}	Deverse Breekdeurs Veltaria	$I_R = 100 \mu A, V_{KA} = 5V$	25℃		±12	
V REF	Reverse Breakdown Voltage Tolerance	$_{\rm IR} = 100 \mu \text{A}, v_{\rm KA} = 3 \text{V}$	-40 to 85℃		±24	mV
	Tolerance		-40 to 125℃		±30	
			25℃		65	
I _{RMIN}	Minimum Operating Current		-40 to 85℃	45	70	μΑ
			-40 to 125℃		73	
	Average Reverse Breakdown	$I_R = 10 \text{ mA}$		±20		ppm/℃
$\Delta V_R/\Delta T$	Voltage Temperature	$I_R = 1 \text{ mA},$	-40 to 125℃	±15	±150	
	Coefficient	$I_R = 100 \mu A$		±15		
	Reference voltage change with cathode voltage change		25℃		-2.5	mV/V
$\Delta V_R / \Delta V_K$		$I_R = 1 \text{mA}$	-40 to 85℃	-1.55	-3.0	
			-40 to 125℃		-4.0	
	Reference input current		25℃		150	nA
I _{REF}			-40 to 85℃	60	200	
			-40 to 125℃		200	
		-1 -1mΛ	25℃		2.0	
		$I_{RMIN} < I_R < 1mA$ $V_{OUT} > 1.6V$	-40 to 85℃	0.7	2.5	
$\Delta V_R/\Delta I_R$	Reverse Breakdown Change	VOUT > 1.0V	-40 to 125℃		2.5	
ΔVR/ΔIR	With Current	1mA < I _R < 12 mA	25℃		6.0	IIIV
		V _{OUT} > 1.6V	-40 to 85℃	2	8.0	
		V ₀₀₁ > 1.0 V	-40 to 125℃		10.0	
	Dynamic Output Impedance	$I_R = 1 \text{mA},$	$V_{KA} = V_{REF}$	0.5		Ω
Z_R		$f = 120Hz$ $I_{AC} = 0.1I_{R}$	V _{KA} = 10V	2		
en	Noise Voltage	I _R = 100μA 10Hz < f < 10kHz		20		μV_{RMS}
ΔV_R	Long Term Stability (Non cumulative)	$t = 1000Hrs$ $I_R =$	120		ppm	

Typical Characteristics LM4041 1.225



Output Impedance vs. Frequency



Application Information

In a conventional shunt regulator application (Figure 1), an external series resistor (R_s) is connected between the supply voltage, V_s , and the LM4041.

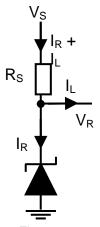


Figure 1

 R_S determines the current that flows through the load (I_L) and the LM4041 (I_R). Since load current and supply voltage may vary, R_S should be small enough to supply at least the minimum acceptable I_R to the LM4040 even when the supply voltage is at its minimum and the load current is at its maximum value. When the supply voltage is at its maximum and I_L is at its minimum, R_S should be large enough so that the current flowing through the LM4040 is less than 12 mA.

 R_S is determined by the supply voltage, (V_S), the load and operating current, (I_L and I_Q), and the LM4040's reverse breakdown voltage, V_R.

$$R_S = \frac{V_S - V_R}{I_L + I_R}$$

The LM4041 comes in two varients:

- LM4041 with fixed 1.225V output
- LM4041_ADJ with variable output voltage.

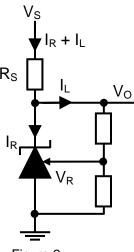


Figure 2

The LM4041-ADJ's output voltage can be adjusted to any value in the range of 1.24V through 10V. The output voltage is set by the ratio of two external feedback resistors as shown in Figure 2 and the internal reference voltage (V_R).

The output voltage is found using the equation:

$$V_{O} = V_{R} \times \left(1 + \frac{R_{2}}{R_{1}}\right)$$

Printed circuit board layout considerations

LM4041 with fixed output voltage in the SOT23 package has the die attached to pin 1, which results in an electrical contact between pin 2 and pin 1. Therefore, pin 1 of the SOT23 package must be left floating or connected to pin 2.

LM4041 with fixed output voltage in the SC70-5 package have the die attached to pin 2, which results in an electrical contact between pin 2 and pin 1. Therefore, pin 2 must be left floating or connected to pin1.



Ordering Information

25℃ Tol	Voltage (V)	ORDER CODE	QUALIFICATION†	PACK	PART MARK	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
		LM4041CFTA	Commercial	SOT23	R1C	7", 180mm	8mm	3000
0.5%	1.225	LM4041CQFTA	Automotive	SOT23	R1C	7", 180mm	8mm	3000
		LM4041CH5TA	Commercial	SC70-5	R1C	7", 180mm	8mm	3000
1%		LM4041DFTA	Commercial	SOT23	R1D	7", 180mm	8mm	3000
	1.225	LM4041DQFTA	Automotive	SOT23	R1D	7", 180mm	8mm	3000
		LM4041DH5TA	Commercial	SC70-5	R1D	7", 180mm	8mm	3000
	Adj	LM4041DADJFTA	Commercial	SOT23	RAD	7", 180mm	8mm	3000
	Auj	LM4041DADJH5TA	Commercial	SC70-5	RAD	7", 180mm	8mm	3000

[†] All parts AEC-Q100 grade 1 qualified

Package Outline Information



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