

## Features

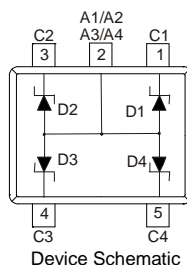
- Quad TVS in Common Anode Configuration
- Ultra-Small Surface Mount Package
- Ideal For Transient Suppression and ESD Protection
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **"Green Device" (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## ESD Capability

- IEC 61000-4-2 Contact Method  $\pm 8\text{kV}$
- IEC 61000-4-2 Air Discharge Method  $\pm 15\text{kV}$

## Mechanical Data

- Case: SOT-553
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Finish: Matte Tin, Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Polarity: Pin 1 Indicator
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.002g (approx.)



## Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Forward Voltage @ $I_F = 10\text{mA}$	$V_F$	0.9	V

## Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 4, 7)	$P_D$	380	mW
Peak Power Dissipation, 8x20 $\mu\text{s}$ Waveform (Note 5)	$P_{pk}$	20	W
Thermal Resistance, Junction-to-Ambient (Note 4)	$R_{\theta JA}$	327	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

## Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Type Number	Marking Code	Breakdown Voltage (Note 3)			Leakage Current (Note 3)		Clamping Voltage (Note 5)		Typ Capacitance @0V Bias(pF) (Note 6)		Typ Capacitance @3V Bias(pF) (Note 6)	
		$V_{BR} @ I_T = 1\text{mA}$			$I_{RM} @ V_{RM}$		$V_C @ I_{PP}$		$C_T$		$C_T$	
		Min (V)	Nom (V)	Max (V)	Max( $\mu\text{A}$ )	(V)	$V_C(V)$	$I_{PP}(A)$	Typ	Max	Typ	Max
DZQA5V6AXV5	T56	5.3	5.6	5.9	1	3.0	13	1.6	18.7	20	11.4	12.3

Notes:

1. No purposefully added lead.
2. Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).
3. Short duration pulse test used to minimize self-heating effect.
4. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. Suggested Pad Layout Document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
5. Non-repetitive current pulse per Figure 3 and derate above  $T_A = 25^\circ\text{C}$  per Figure 1.
6. Per element,  $f = 1\text{MHz}$ ,  $T_A = 25^\circ\text{C}$
7. Only 1 diode under power. For all 4 diodes under power,  $P_D$  will be 25% of the listed value.

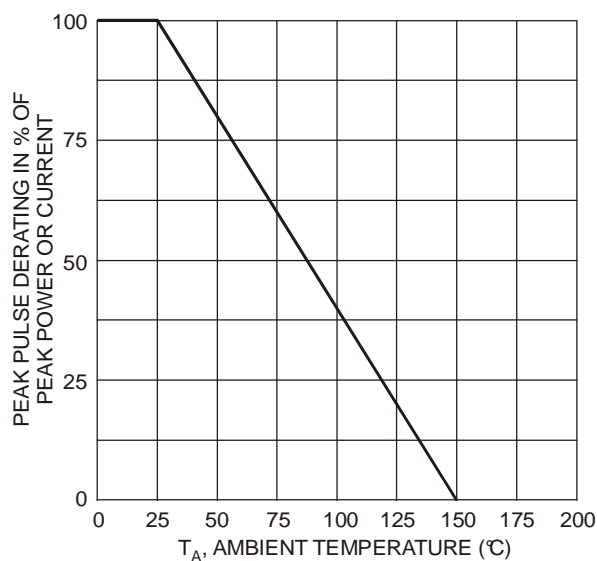


Fig. 1 Pulse Derating Curve

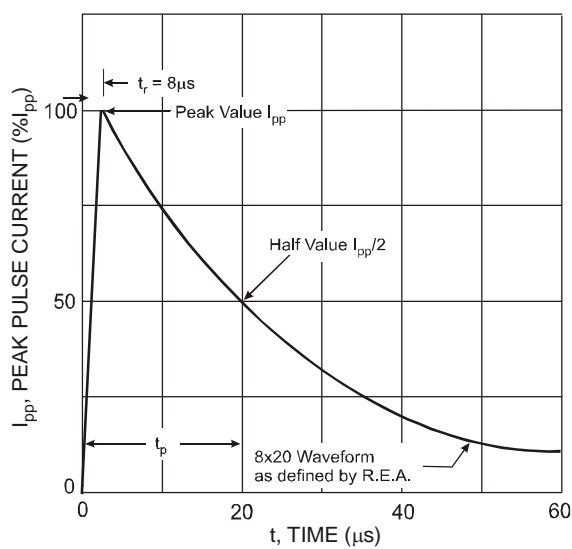


Fig. 2 Pulse Waveform

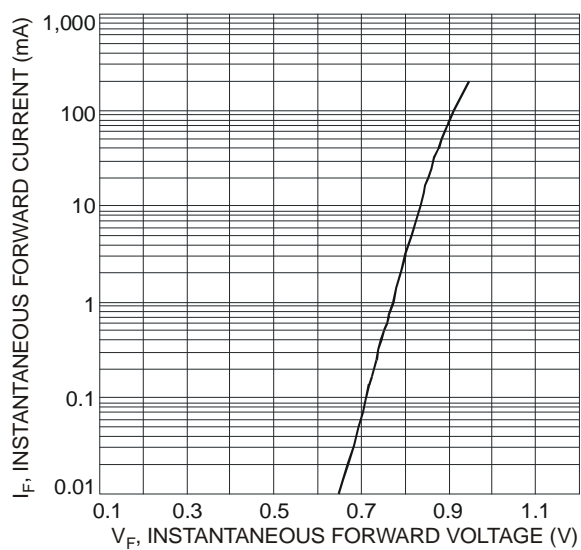


Fig. 3 Typical Forward Characteristics

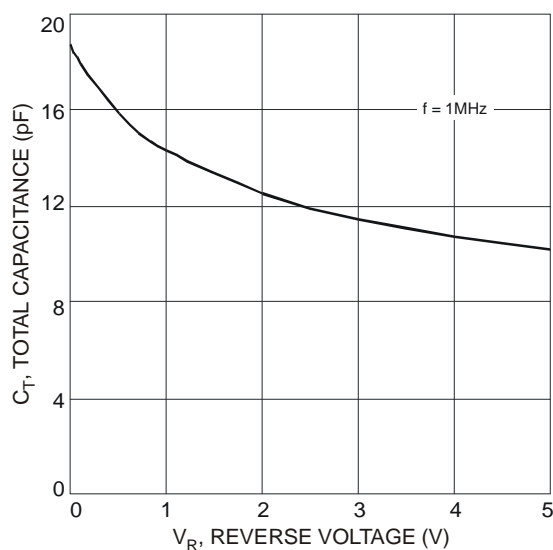


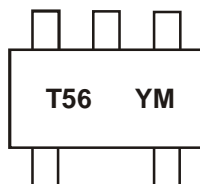
Fig. 4 Typical Total Capacitance vs. Reverse Voltage (Per Element)

## Ordering Information (Note 8)

Part Number	Case	Packaging
DZQA5V6AXV5-7	SOT-553	3000/Tape & Reel

Notes: 8. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

## Marking Information



T56 = Product type marking code  
YM = Date Code Marking  
Y = Year (ex: W = 2009)  
M = Month (ex: 9 = September)

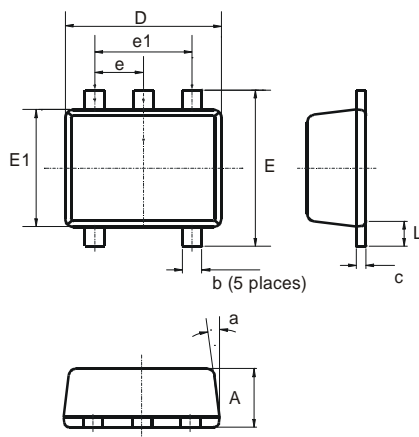
### Date Code Key

Date Code Key

Year	2009	2010	2011	2012	2013	2014	2015
Code	W	X	Y	Z	A	B	C

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

## Package Outline Dimensions



SOT-553			
Dim	Min	Max	Typ
A	0.55	0.60	0.60
c	0.10	0.18	0.15
D	1.50	1.70	1.60
E	1.55	1.70	1.60
E1	1.10	1.25	1.20
L	0.10	0.30	0.20
b	0.15	0.30	0.20
e	0.50 Typ		
e1	1.00 Typ		
a	6°	8°	7°
All Dimensions in mm			

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