

Green

#### **60V N-CHANNEL ENHANCEMENT MODE MOSFET**

### **Product Summary**

BV <sub>DSS</sub>	Max R <sub>DS(on)</sub>	Max I <sub>D</sub> T <sub>A</sub> = 25°C (Note 3)
60V	$40\text{m}\Omega$ @ V <sub>GS</sub> = $10\text{V}$	7.7A
	$60m\Omega @ V_{GS} = 4.5V$	6.3A

### **Description and Applications**

This MOSFET has been designed to minimize the on-state resistance  $(R_{DS(on)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

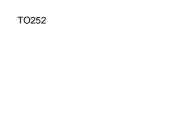
- DC-DC converters
- Power management functions
- · Disconnect switches
- Motor control

#### **Features and Benefits**

- Low on-resistance
- Fast switching speed
- Low gate drive
- Lead-Free Finish; RoHS compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: TO252
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish. Solderable per MIL-STD-202, Method 208
- Weight: 0.33 grams (approximate)



Top View

Pin Out -Top View

D

D

**Equivalent Circuit** 

#### Ordering Information (Note 1 & 2)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN6A09KTC	ZXMN6A09	13	16	2,500

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

### **Marking Information**

ZXMN6A09 = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 10 = 2010) WW = Week (01 - 53)





#### Maximum Ratings @T<sub>A</sub> = 25℃ unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source voltage			$V_{DSS}$	60	V
Gate-Source voltage			$V_{GS}$	±20	V
		(Note 4)		11.8	
Continuous Drain current	$V_{GS} = 10V$	$T_A = 70^{\circ}C \text{ (Note 4)}$	I <sub>D</sub>	9.6	Α
		(Note 3)		7.7	
Pulsed Drain current (Note 5)		I <sub>DM</sub>	43	Α	
Continuous Source current (Body diode) (Note 4)		Is	10.8	Α	
Pulsed Source current (Body diode) (Note 5)		I <sub>SM</sub>	43	A	

#### Thermal Characteristics @TA = 25℃ unless otherwise specified

Characteristic	Symbol	Value	Unit		
	(Note 3)		4.3 34.4		
Power dissipation Linear derating factor	(Note 4)	$P_{D}$	10.1 80.8	W mW/°C	
	(Note 6)		2.15 17.2		
	(Note 3)		29		
Thermal Resistance, Junction to Ambient	(Note 4)	$R_{ heta JA}$	12.3	2011	
	(Note 6)	·	58.1	°C/W	
Thermal Resistance, Junction to Lead	(Note 7)	$R_{ heta JL}$	1.04		
Operating and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C	

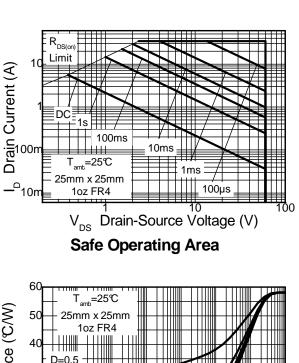
Notes:

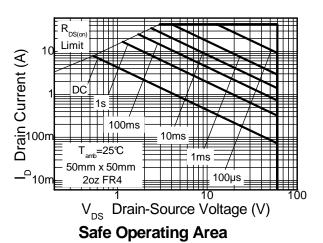
- 3. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 4. For a device surface mounted on FR4 PCB measured at  $t \le 10$  sec.
- 5. Repetitive rating 50mm x 50mm x 1.6mm FR4 PCB, D = 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.
- 6. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 7. Thermal resistance from junction to solder-point (at the end of the drain lead).

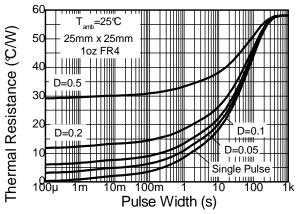


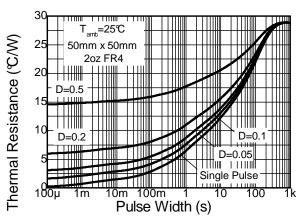


#### **Thermal Characteristics**



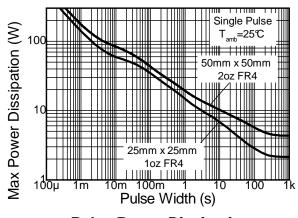


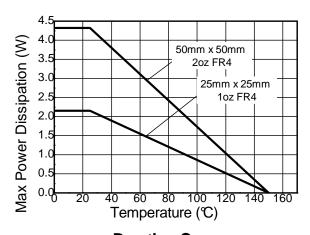




**Transient Thermal Impedance** 

**Transient Thermal Impedance** 





**Pulse Power Dissipation** 

**Derating Curve** 





# Electrical Characteristics @T<sub>A</sub> = 25℃ unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$I_D = 250 \mu A, V_{GS} = 0V$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	_	3.0	V	$I_D = 250 \mu A$ , $V_{DS} = V_{GS}$	
Static Drain Source On Decistones (Note 9)	5			40	mΩ	$V_{GS} = 10V, I_D = 7.3A$	
Static Drain-Source On-Resistance (Note 8)	R <sub>DS (ON)</sub>	_	_	60	11177	$V_{GS} = 4.5V, I_D = 5.6A$	
Forward Transconductance (Notes 8 & 9)	9fs		15	_	S	$V_{DS} = 15V, I_D = 7.3A$	
Diode Forward Voltage (Note 8)	$V_{SD}$		0.85	0.95	V	$I_S = 6.6A, V_{GS} = 0V, T_J = 25^{\circ}$	
Reverse recovery time (Note 9)	t <sub>rr</sub>		25.6	_	ns	$I_S = 3A$ , $di/dt = 100A/\mu s$	
Reverse recovery charge (Note 9)	Q <sub>rr</sub>		26.0	_	nC	T <sub>J</sub> = 25℃	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C <sub>iss</sub>		1426	_	pF	), oo, , , , o, ,	
Output Capacitance	Coss		134	_	pF	$V_{DS} = 30V, V_{GS} = 0V$ -f = 1MHz	
Reverse Transfer Capacitance	Crss		64	_	pF	T = TIVIMZ	
Total Gate Charge (Note 10)	$Q_g$	_	15	_	nC	$V_{GS} = 4.5V, V_{DS} = 30V, I_{D} = 5.6A$	
Total Gate Charge (Note 10)	Qg	_	29	_	nC	101/11/	
Gate-Source Charge (Note 10)	Q <sub>gs</sub>	_	7.0	_	nC	$V_{GS} = 10V, V_{DS} = 30V$ $I_{D} = 7.3A$	
Gate-Drain Charge (Note 10)	$Q_{gd}$	_	4.7	_	nC		
Turn-On Delay Time (Note 10)	t <sub>D(on)</sub>	_	4.8	_	ns		
Turn-On Rise Time (Note 10)	t <sub>r</sub>	_	4.6	_	ns	$V_{DD} = 30V$ , $V_{GS} = 10V$ $I_D = 1A$ , $R_G \approx 6.0\Omega$	
Turn-Off Delay Time (Note 10)	t <sub>D(off)</sub>	_	32.5	_	ns		
Turn-Off Fall Time (Note 10)	t <sub>f</sub>		14.5	_	ns		

Notes:

<sup>8.</sup> Measured under pulsed conditions. Pulse width  $\leq 300 \mu s;$  duty cycle  $\leq 2\%$ 

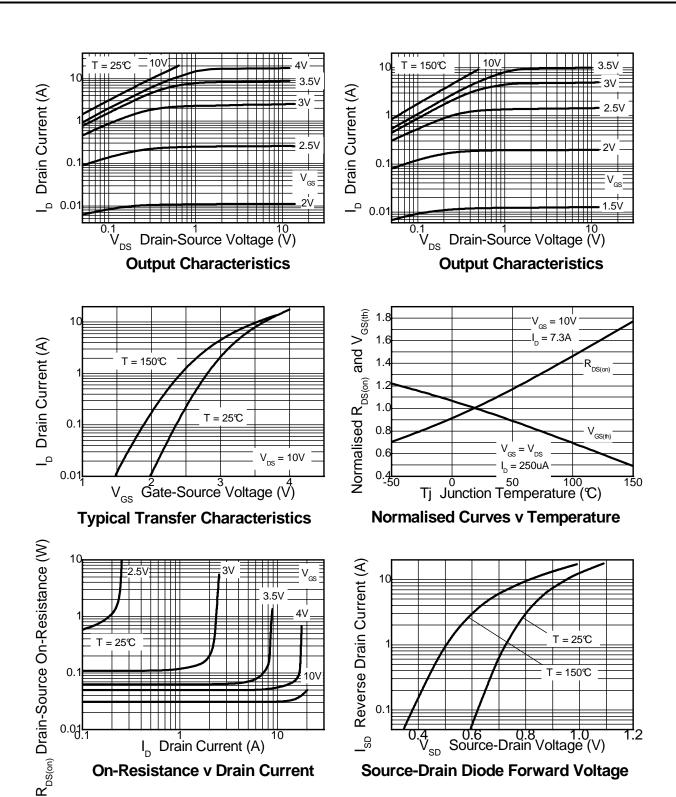
<sup>9.</sup> For design aid only, not subject to production testing.

10. Switching characteristics are independent of operating junction temperatures.





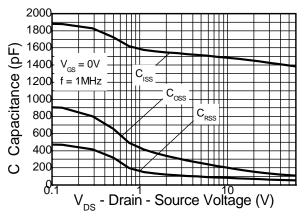
# Typical Characteristics



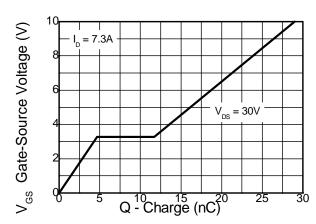




## **Typical Characteristics - continued**

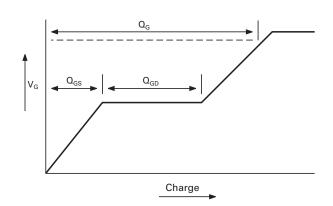


Capacitance v Drain-Source Voltage

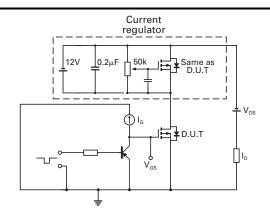


**Gate-Source Voltage v Gate Charge** 

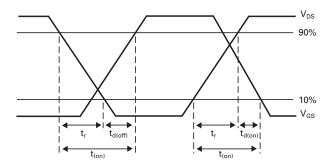
### **Test Circuits**



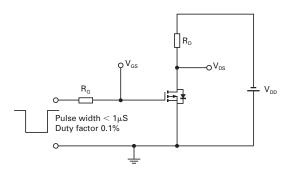
Basic gate charge waveform



**Gate charge test circuit** 



Switching time waveforms

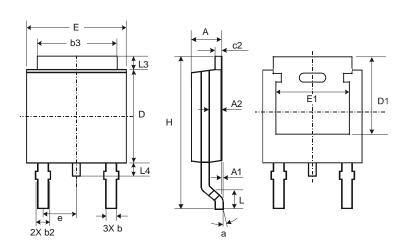


Switching time test circuit



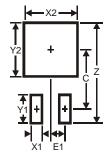


# **Package Outline Dimensions**



TO252					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
<b>A</b> 1	0.00	0.13	0.08		
<b>A2</b>	0.97	1.17	1.07		
q	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c2	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	_	_		
е	Î	_	2.286		
П	6.45	6.70	6.58		
E1	4.32	_	_		
H	9.40	10.41	9.91		
٦	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	_		
All Dimensions in mm					

# **Suggested Pad Layout**



Dimensions	Value (in mm)		
Z	11.6		
X1	1.5		
X2	7.0		
Y1	2.5		
Y2	7.0		
С	6.9		
E1	2.3		





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