

N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance: R_{DS(ON)}
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead, Halogen and Antimony Free, RoHS Compliant (Note 1)
- "Green" Device (Notes 3 & 4)
- ESD Protected Up To 2kV

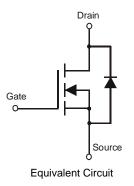
Mechanical Data

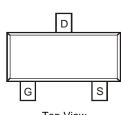
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)





Top View





Top View Pin Out Configuration

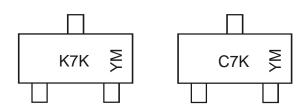
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN601K-7	SOT23	3000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead. Halogen and Antimony free.
- 2. Diodes Inc.'s "Green" Policy can be found on our website at http://www.diodes.com
- 3. Product manufactured with Date Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



K = SAT (Shanghai Assembly / Test site)
C = CAT (Chengdu Assembly / Test site)
7K = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: S = 2005)
M = Month (ex: 9 = September)

Date Code Key

	,													
Year	2005	2006	2007	2008	2009	2010	2011	2011	2012	2013	2014	2015	2016	2017
Code	S	T	U	V	W	Х	Υ	Υ	Z	Α	В	С	D	Е
Month	Jan	Feb	Ma	ar .	Apr	May	Jun	Jul	Aug	Sep) (Oct	Nov	Dec
Code	1	2	3		4	5	6	7	8	9		0	N	D



Maximum Ratings @T_A = 25℃ unless otherwise specified

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	60	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current (Note 5)	Continuous	1-	300	m ^
Drain Current (Note 5)	Pulsed (Note 6)	ID	800	mA

Thermal Characteristics @T_A = 25℃ unless otherwise specified

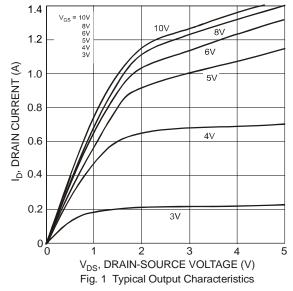
Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_{D}	350	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	357	€\M
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-65 to +150	J

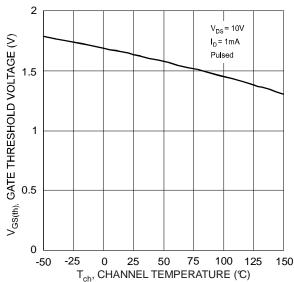
Electrical Characteristics @TA = 25°C unless otherwise specified

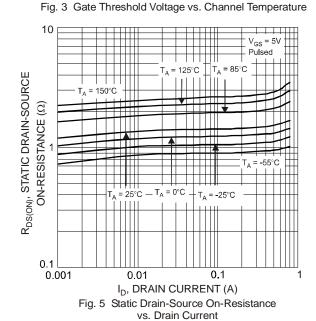
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)				a.		
Gate Threshold Voltage	$V_{GS(th)}$	1.0	1.6	2.5	V	$V_{DS} = 10V$, $I_D = 1mA$
Static Drain-Source On-Resistance	R _{DS (ON)}	_		2.0	. ()	$V_{GS} = 10V, I_D = 0.5A$
Static Dialit-Source Off-Resistance		_		3.0		$V_{GS} = 5V, I_D = 0.05A$
Forward Transfer Admittance	Y _{fs}	80	_	_	ms	$V_{DS} = 10V, I_D = 0.2A$
DYNAMIC CHARACTERISTICS	_	-	_	_	-	
Input Capacitance	C _{iss}	_	_	50	pF	
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance	C _{rss}			5.0	pF	

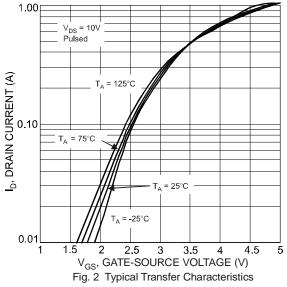
- 5. Device mounted on FR-4 PCB.
- 6. Pulse width ≤10μS, Duty Cycle ≤1%.
 7. Short duration pulse test used to minimize self-heating effect.











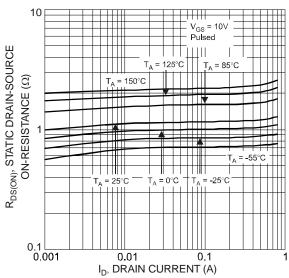
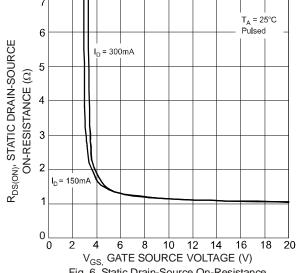
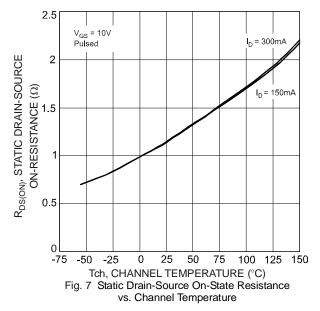
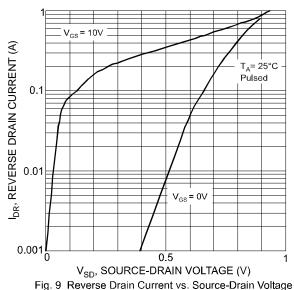


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current









W V_{GS} = 0V Pulsed

T_A = 150°C

T_A = 125°C

T_A = 85°C

T_A = 25°C

T_A = 25°C

V_{SD}, SOURCE-DRAIN VOLTAGE (V)

Fig. 8 Reverse Drain Current

vs. Source-Drain Voltage

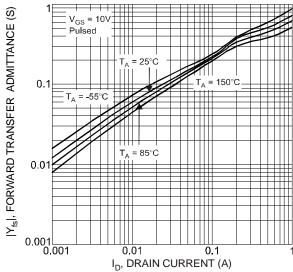
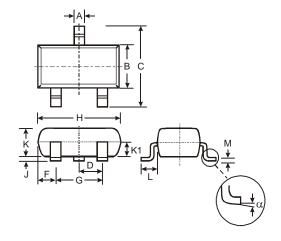


Fig.10 Forward Transfer Admittance vs. Drain Current

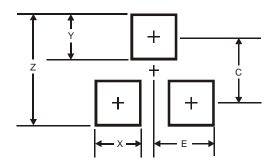
Package Outline Dimensions



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.903	1.10	1.00				
K1	-	-	0.400				
L	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All	All Dimensions in mm						



Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
E	1.35

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2012, Diodes Incorporated

www.diodes.com