

#### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = 25℃	
20V	0.99Ω @ V <sub>GS</sub> = 4.5V	450mA	
	1.2Ω @ V <sub>GS</sub> = 2.5V	400mA	
	1.8Ω @ V <sub>GS</sub> = 1.8V	330mA	
	2.4Ω @ V <sub>GS</sub> = 1.5V	300mA	

### **Description and Applications**

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(on)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- DC-DC Converters
- Analog Switch

#### **Features and Benefits**

- Dual N-Channel MOSFET
- Low On-Resistance
- Very low Gate Threshold Voltage, 1.0V max
- Low Input Capacitance
- Fast Switching Speed
- Ultra-Small Surface Mount Package 1mm x 1mm
- Low Package Profile, 0.45mm Maximum Package height
- ESD Protected Gate
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device, Halogen and Antimony Free (Note 2)
- Qualified to AEC-Q101 standards for High Reliability

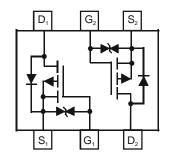
#### **Mechanical Data**

- Case: SOT963
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe.
  Solderable per MIL-STD-202, Method 208
- Weight: 0.027 grams (approximate)



Top View

**SOT963** 



Top View Schematic and Transistor Diagram

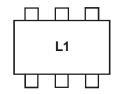
#### **Ordering Information** (Note 3)

Part Number	Case	Packaging
DMN2990UDJ-7	SOT963	10K/Tape & Reel

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

## Marking Information



L1 = Product Type Marking Code



### Maximum Ratings @T<sub>A</sub> = 25℃ unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±8	V
Continuous Drain Current (Note 4) V <sub>GS</sub> = 4.5V	Steady State	$T_A = 25$ °C $T_A = 70$ °C	I <sub>D</sub>	450 350	mA
Continuous Drain Current (Note 4) V <sub>GS</sub> = 1.8V	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	330 220	mA
Pulsed Drain Current (Note 5)			I <sub>DM</sub>	800	mA

### Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 4)	$P_{D}$	350	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	360	℃/W
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +150	${\mathfrak C}$

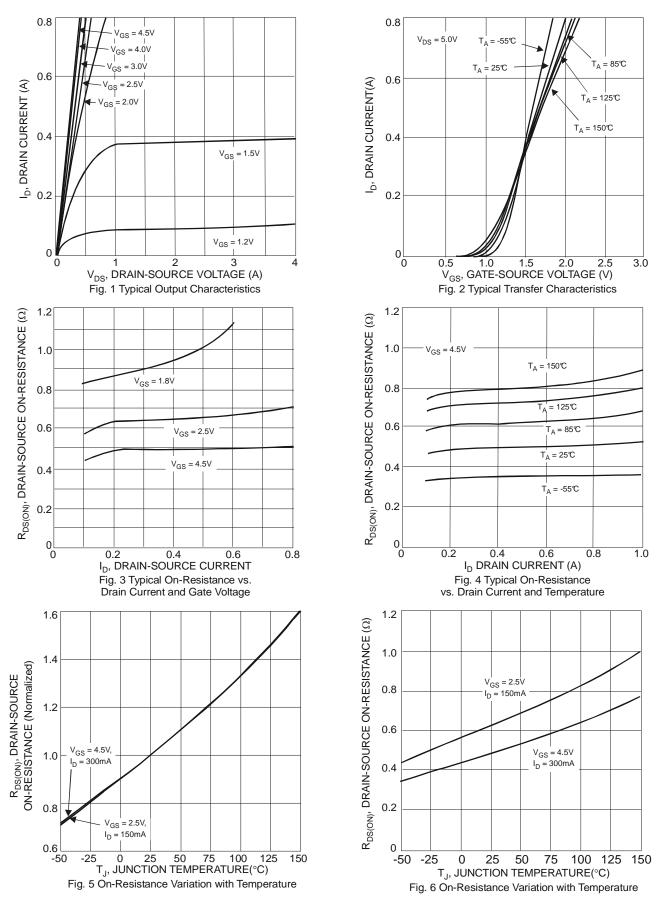
# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)								
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	20	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
7 0			-	-	50	nA	$V_{DS} = 5V$ , $V_{GS} = 0V$	
Zero Gate Voltage Drain Current	$@T_c = 25$ °C	IDSS	-	-	100	ΠA	$V_{DS} = 16V, V_{GS} = 0V$	
Gate-Source Leakage		I <sub>GSS</sub>	-	-	±100	nA	$V_{GS} = \pm 5V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)								
Gate Threshold Voltage		V <sub>GS(th)</sub>	0.4	-	1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
			-	0.60	0.99		$V_{GS} = 4.5V, I_D = 100mA$	
			-	0.75	1.2		$V_{GS} = 2.5V, I_D = 50mA$	
Static Drain-Source On-Resistance		R <sub>DS</sub> (ON)	-	0.90	1.8	Ω	$V_{GS} = 1.8V, I_D = 20mA$	
		_ ( ( ( ) )	-	1.2	2.4		$V_{GS} = 1.5V, I_D = 10mA$	
			-	2.0	-		$V_{GS} = 1.2V, I_{D} = 1mA$	
Forward Transfer Admittance		Y <sub>fs</sub>	180	-	-	mS	$V_{DS} = 10V, I_D = 400mA$	
Diode Forward Voltage (Note 6)		$V_{SD}$	-	0.6	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 150mA	
DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance		C <sub>iss</sub>	ı	27.6	-	рF	101/11/101/	
Output Capacitance		Coss	-	4.0	-	pF	$V_{DS} = 16V, V_{GS} = 0V,$ -f = 1.0MHz	
Reverse Transfer Capacitance		C <sub>rss</sub>	-	2.8	-	pF	1 = 1.0WH12	
Total Gate Charge		Qq	-	0.5	-	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Source Charge		Q <sub>gs</sub>	-	0.07	-	nC	I <sub>D</sub> = 250mA	
Gate-Drain Charge		$Q_{gd}$	-	0.07	-	nC	1	
Turn-On Delay Time	rn-On Delay Time		-	4.0	-	ns	10// 15//	
Turn-On Rise Time		t <sub>D(on)</sub>	-	3.3	-	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time		t <sub>D(off)</sub>	-	19.0	-	ns	$R_L = 47\Omega$ , $R_G = 10\Omega$ ,	
Turn-Off Fall Time		t <sub>f</sub>	-	6.4	-	ns	$I_D = 200 \text{mA}$	

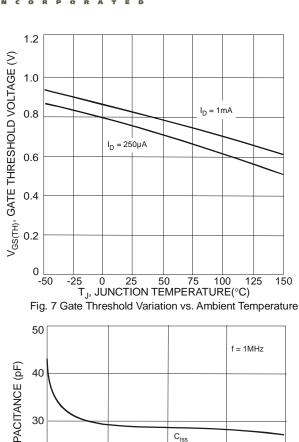
Notes:

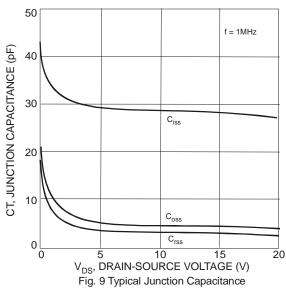
- Device mounted on FR-4 PCB, with minimum recommended pad layout.
  Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
  Short duration pulse test used to minimize self-heating effect.
  Guaranteed by design. Not subject to product testing.

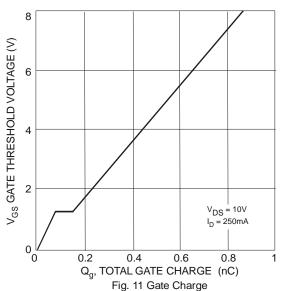


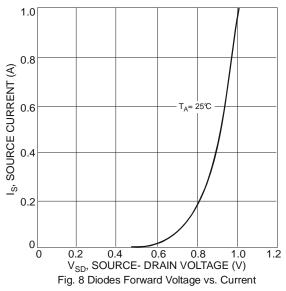












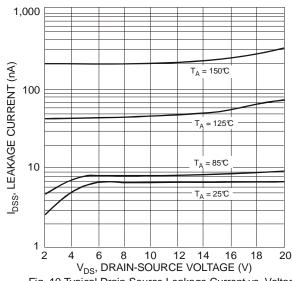
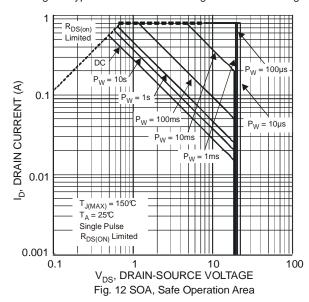
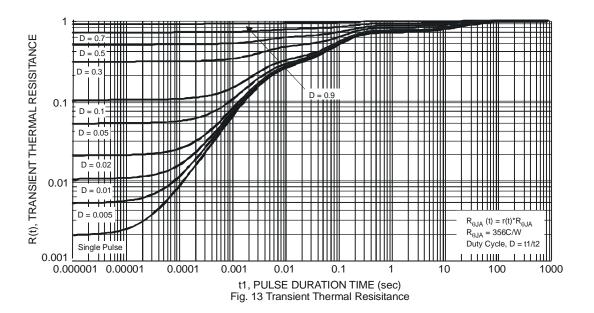


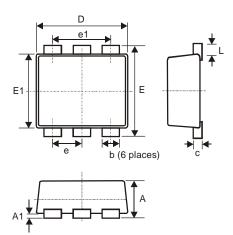
Fig. 10 Typical Drain-Source Leakage Current vs. Voltage





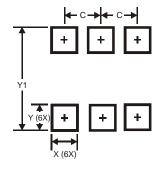


# **Package Outline Dimensions**



SOT963						
Dim	Min	Max	Тур			
Α	0.40	0.50	0.45			
A1	0	0.05	-			
C	0.120	0.180	0.150			
ם	0.95	1.00				
ш	0.95	1.05	1.00			
E1	0.75 0.85 0.80					
L	0.05 0.15 0.10					
b	<b>b</b> 0.10 0.20 0.15					
е	0.35 Typ					
e1	0.70 Typ					
All Dimensions in mm						

# **Suggested Pad Layout**



Dimensions	Value (in mm)
С	0.350
Х	0.200
Y	0.200
Y1	1.100



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