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SINGLE N-CHANNEL ENHANCEMENT MODE MOSFET

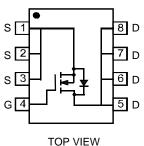
Features

- Low On-Resistance
 - $8m\Omega @ V_{GS} = 10V$
 - $9m\Omega$ @ $V_{GS} = 4.5V$
 - $12m\Omega$ @ $V_{GS} = 2.5V$
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 4)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.072 grams (approximate)





Internal Schematic

Maximum Ratings @T_A = 25℃ unless otherwise specified

Char	acteristic		Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±12	V
Drain Current (Note 1)	Steady State	T _A = 25℃ T _A = 70℃	I _D	12 9.6	А
Pulsed Drain Current (Note 3)			I _{DM}	42	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P _D	2	W
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	62.5	€\M
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	Ç

Notes:

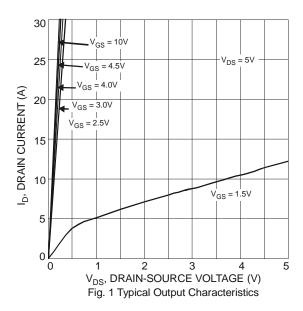
- 1. Device mounted on 2 oz, FR-4 PCB, with $R_{\theta JA}$ = 62.5°C/W
- 2. No purposefully added lead.
- 3. Pulse width $\leq 10 \mu S$, Duty Cycle $\leq 1\%$.
- 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

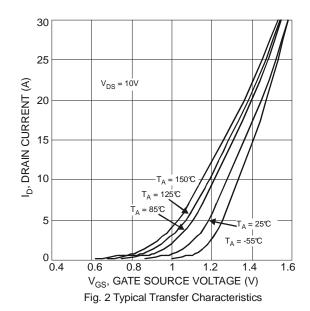


Electrical Characteristics $@T_A = 25$ % unless otherwise specified

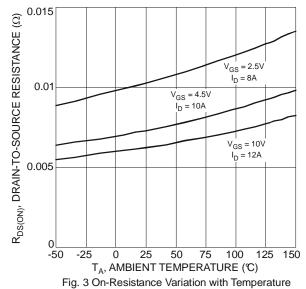
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μА	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	I_{GSS}	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)	ON CHARACTERISTICS (Note 5)					
Gate Threshold Voltage	$V_{GS(th)}$	0.5	_	1.2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
			_	8		$V_{GS} = 10V, I_D = 12A$
Static Drain-Source On-Resistance	R _{DS} (ON)	_	_	9	$m\Omega$	$V_{GS} = 4.5V, I_D = 10A$
			_	12		$V_{GS} = 2.5V, I_D = 8A$
Forward Transconductance	g fs		27	_	S	$V_{DS} = 5V, I_{D} = 6.5A$
Diode Forward Voltage (Note 5)	V_{SD}	0.5	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 3A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	_	2555	_	pF	
Output Capacitance	Coss	_	523	_	pF	$V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance	Crss	_	496	_	pF	
Gate Resistance	R_{G}		1.1	_	Ω	$V_{GS} = 0V V_{DS} = 0V, f = 1MHz$
SWITCHING CHARACTERISTICS						
Total Gate Charge			28.9			$V_{DS} = 10V, V_{GS} = 4.5V, I_{D} = 12A$
Total Gate Charge	Q_g	_	58.3		nC	$V_{DS} = 10V, V_{GS} = 10V, I_D = 12A$
Gate-Source Charge	Qgs	_	3.7	_	IIC	$V_{DS} = 10V, V_{GS} = 10V, I_D = 12A$
Gate-Drain Charge	Q_{gd}	_	11.4	_		$V_{DS} = 10V, V_{GS} = 10V, I_D = 12A$

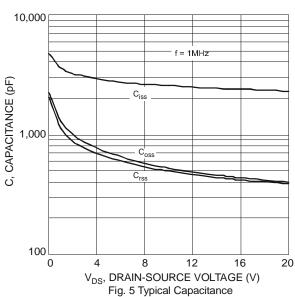
Notes: 5. Short duration pulse test used to minimize self-heating effect.

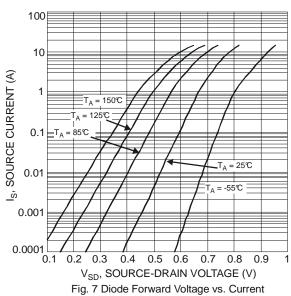


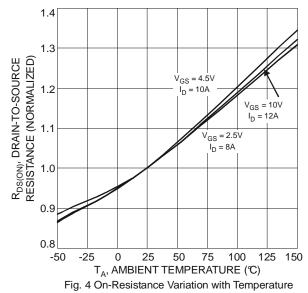












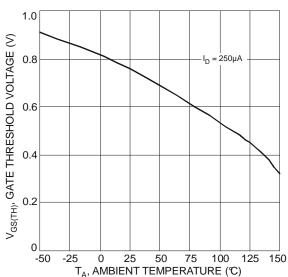


Fig. 6 Gate Threshold Variation vs. Ambient Temperature



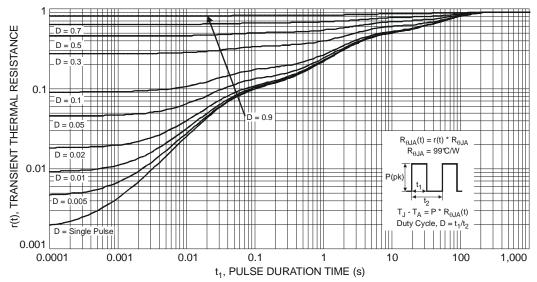


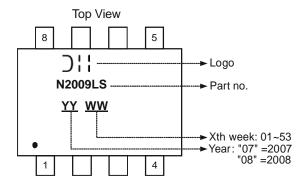
Fig. 8 Transient Thermal Response

Ordering Information (Note 6)

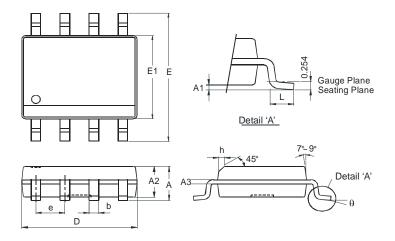
Part Number	Case	Packaging
DMN2009LSS-13	SO-8	2500/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



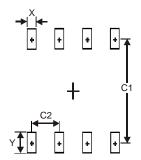
Package Outline Dimensions



SO-8				
Dim	Min	Max		
Α	-	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
А3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	1.27 Typ			
h	-	0.35		
L	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				



Suggested Pad Layout



Dimensions	Value (in mm)
X	0.60
Υ	1.55
C1	5.4
C2	1.27

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