





### 100V NPN MEDIUM POWER LOW SATURATION TRANSISTOR PowerDI<sup>®</sup>5

#### **Features**

- 43% smaller than SOT223; 60% smaller than TO252
- Maximum height just 1.1mm
- Rated up to 3.2W
- $V_{CEO} = 100V$
- $I_C = 2A$ ;  $I_{CM} = 6A$
- Low Saturation, high gain transistor,
- Lead, Halogen and Antimony Free, RoHS Compliant (Note 1)
- "Green" Device (Note 2)

### **Features**

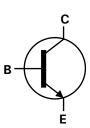
Regular circuits

### **Mechanical Data**

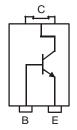
- Case: PowerDI®5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.093 grams (approximate)



Top View **Bottom View** 



**Device Schematic** 



Pin-out diagram

## **Ordering Information** (Note 3)

Part Number	Case	Packaging
DXTN07100BP5-13	PowerDI <sup>®</sup> 5	5000/Tape & Reel

Notes:

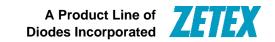
- 1. No purposefully added lead. Halogen and Antimony Free.
- Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com
  For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

### **Marking Information**



DTN7100B = Product Type Marking Code Oll = Manufacturers' Code Marking K = Factory Designator YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 09 for 2009) WW = Week code (01 to 53)





# Maximum Ratings @T<sub>A</sub> = 25℃ unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	120	V
Collector-Emitter Voltage	V <sub>CEO</sub>	100	V
Emitter-Base Voltage	V <sub>EBO</sub>	5	V
Continuous Collector Current	Ic	2	A
Peak Pulse Current	Ісм	6	A

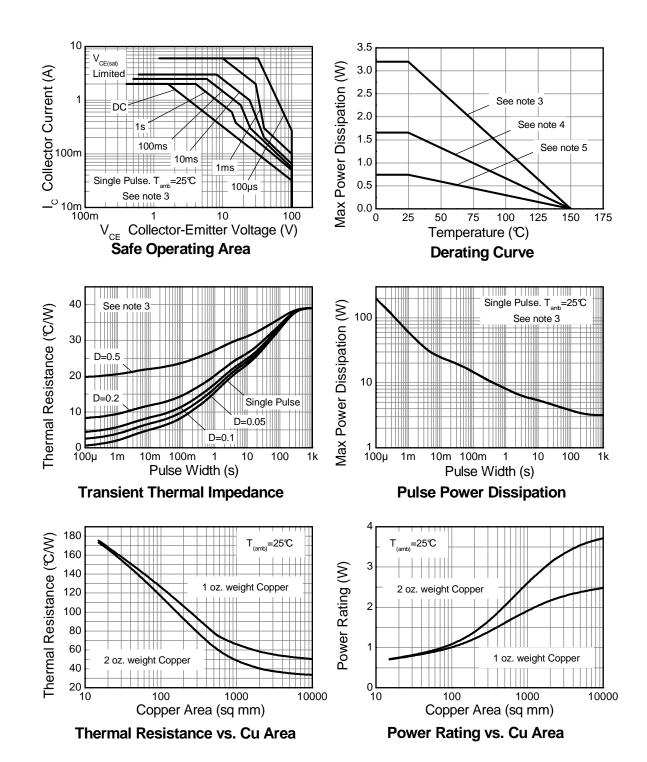
## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation @ T <sub>A</sub> = 25℃ (Note 4)	$P_{D}$	3.2	W
Thermal Resistance, Junction to Ambient Air (Note 4) @T <sub>A</sub> = 25℃	$R_{\theta JA}$	39	℃/W
Power Dissipation @ T <sub>A</sub> = 25℃ (Note 5)	P <sub>D</sub>	1.7	W
Thermal Resistance, Junction to Ambient Air (Note 5) @T <sub>A</sub> = 25℃	$R_{\theta JA}$	75	℃/W
Power Dissipation @ T <sub>A</sub> = 25℃ (Note 6)	$P_{D}$	0.74	W
Thermal Resistance, Junction to Ambient Air (Note 6) @T <sub>A</sub> = 25℃	$R_{ heta JA}$	169	℃/W
Thermal Resistance, Junction to Collector Terminal	$R_{ heta JT}$	8.9	C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	C

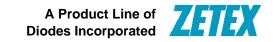
Notes:

- 4. Device mounted on FR-4 PCB, single sided 2 oz. copper, collector pad dimensions  $50 \text{mm} \times 50 \text{mm}$ .
- Device mounted on FR-4 PCB, single sided 1 oz. copper, collector pad dimensions 50mm x 50mm.
  Device mounted on FR-4 PCB, single sided 1 oz. copper, minimum recommended pad layout.









# **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

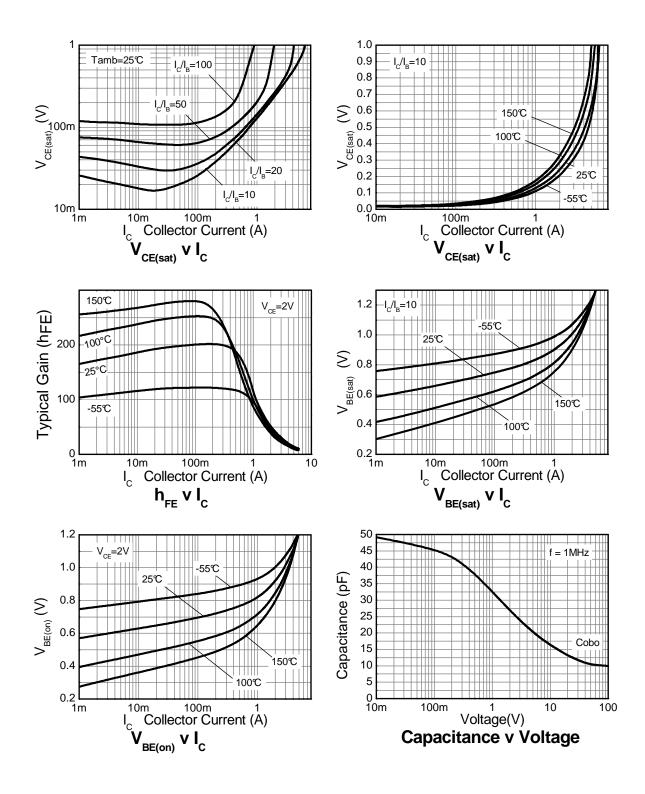
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	120	_		V	$I_{C} = 100 \mu A$
Collector-Emitter Breakdown Voltage (Note 7)	V <sub>(BR)CEO</sub>	100	_		٧	I <sub>C</sub> = 10mA
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	5			<b>V</b>	$I_E = 100 \mu A$
Collector Cutoff Current	I <sub>CBO</sub>		_	0.1 10	μΑ	V <sub>CB</sub> = 100V V <sub>CB</sub> = 100V, T <sub>amb</sub> = 100 ℃
Emitter Cutoff Current	I <sub>EBO</sub>			0.1	μΑ	$V_{EB} = 4V$
Collector-Emitter Saturation Voltage (Note 7)	V <sub>CE(sat)</sub>		0.13 0.23	0.3 0.5	٧	$I_C = 1A$ , $I_B = 100mA$ $I_C = 2A$ , $I_B = 200mA$
Base-Emitter Saturation Voltage (Note 7)	V <sub>BE(sat)</sub>		0.9	1.25	V	I <sub>C</sub> = 1A, I <sub>B</sub> = 100mA
Base-Emitter Turn-On Voltage (Note 7)	V <sub>BE(on)</sub>		0.8	1.00	V	$I_C = 1A$ , $V_{CE} = 2V$
DC Current Gain (Note 7)	h <sub>FE</sub>	70 100 55 25	200 200 110 55	300 — —		$\begin{split} I_{C} &= 50\text{mA}, \ V_{CE} = 2\text{V} \\ I_{C} &= 500\text{mA}, \ V_{CE} = 2\text{V} \\ I_{C} &= 1\text{A}, \ V_{CE} = 2\text{V} \\ I_{C} &= 2\text{A}, \ V_{CE} = 2\text{V} \end{split}$
Transition Frequency	f⊤	140	175		MHz	$I_{C} = 100 \text{mA}, \ V_{CE} = 5 \text{V}$ f = 100MHz
Output Capacitance	$C_{obo}$		_	30	pF	V <sub>CB</sub> = 10A, f = 1MHz
Switching Times	t <sub>on</sub> t <sub>off</sub>		80 1200		ns ns	$I_C = 500$ mA, $V_{CC} = 10$ V, $I_{B1} = I_{B2} = 50$ mA

Notes: 7. Pulse Test: Pulse width  $\leq 300 \mu s$ . Duty cycle  $\leq 2.0\%$ .





## **Typical Characteristic**

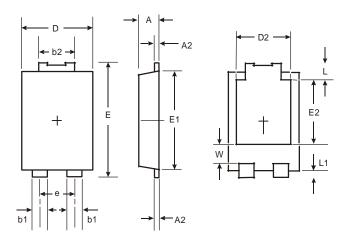


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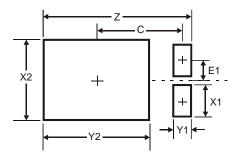


# **Package Outline Dimensions**



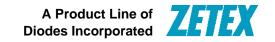
PowerDI <sup>®</sup> 5				
Dim	Min	Max		
Α	1.05	1.15		
A2	0.33	0.43		
<b>b1</b> 0.80		0.99		
<b>b2</b> 1.70		1.88		
<b>D</b> 3.90 4.0		4.05		
D2	3.054 Typ			
Е	6.40	6.60		
е	1.84	Тур		
E1	<b>E1</b> 5.30 5.4			
E2	3.549 Typ			
L	0.75	0.95		
L1	0.50	0.65		
W	<b>W</b> 1.10 1.41			
All Dimensions in mm				

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	6.6
X1	1.4
X2	3.6
Y1	0.8
Y2	4.7
С	3.87
F1	0.9





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