

ULTRA-SMALL SURFACE MOUNT LOW LEAKAGE DIODE

Features

- Ultra-Small Leadless Surface Mount Package (0.6 x 0.3mm)
- Ultra-Low Profile Package (0.3mm)
- Very Low Leakage Current
- Low Capacitance
- Ideal for Compact Battery Powered Portable Electronics
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

Mechanical Data

- Case: X3-DFN0603-2
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Bar
- Terminals: Finish Matte Tin Finish over Copper Leadframe
- (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Weight: 0.2mg (Approximate)



Top View



Bottom View

Ordering Information (Note 3)

Part Number	Case	Packaging	
BAS116LP3-7	X3-DFN0603-2	10,000/Tape & Reel	

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



BA = Product Type Marking Code Bar Denotes Cathode Side



Maximum Ratings @T_A = 25℃ unless otherwise specified

Characteristic		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _R WM V _R	85	V
RMS Reverse Voltage		V _{R(RMS)}	60	V
Forward Continuous Current (Note 4)		I _{FM}	215	mA
Repetitive Peak Forward Current		I _{FRM}	500	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0μs @ t = 1.0ms @ t = 1.0s	I _{FSM}	4.0 1.0 0.5	А

Thermal Characteristics

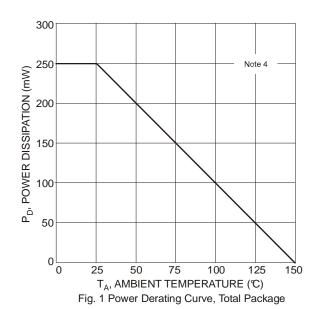
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P_{D}	250	mW
Thermal Resistance Junction to Ambient Air (Note 4)	$R_{ hetaJA}$	500	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	${\mathfrak C}$

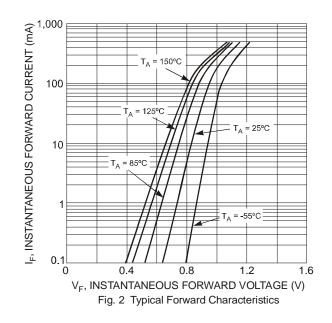
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	$V_{(BR)R}$	85	_	_	V	$I_R = 100 \mu A$
Forward Voltage	V _F	_	0.75 0.9 1.0 1.15	0.95 1.10 1.20 1.35	V	I _F = 1.0mA I _F = 10mA I _F = 50mA I _F = 150mA
Leakage Current (Note 5)	I _R		_	10.0 100 500	nA	$V_R = 75V$ $V_R = 1V$, $T_J = 150$ °C $V_R = 75V$, $T_J = 150$ °C
Total Capacitance	C _T		1.6	3.0	pF	$V_R = 0, f = 1.0MHz$
Reverse Recovery Time	t _{rr}	_	120	3000	ns	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \text{ x } I_R, R_L = 100 \Omega$

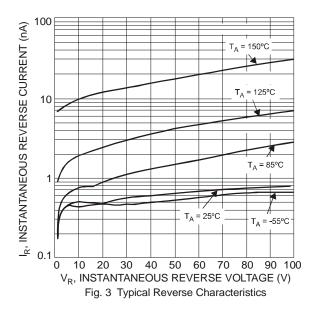
Notes:

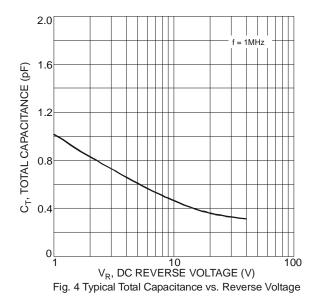
- 4. Part mounted on FR-4 PC board with recommended pad layout, which can be found on our website at http://www.diodes.com.
- 5. Short duration pulse test used to minimize self-heating effect.



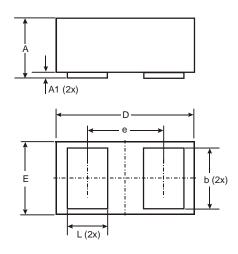








Package Outline Dimensions

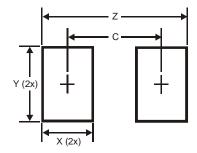


Bottom View

Dim	Min	Max	Тур		
Α	0.27	0.35	0.30		
A1	0.00	0.03	0.02		
b	0.19	0.29	0.24		
D	0.595	0.645	0.62		
Е	0.295	0.345	0.32		
е	-	-	0.355		
L	0.14	0.24	0.19		
All	All Dimensions in mm				

X3-DFN0603-2

Suggested Pad Layout



Dimensions	Value (in mm)
С	0.355
Х	0.230
Υ	0.300
Z	0.610



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