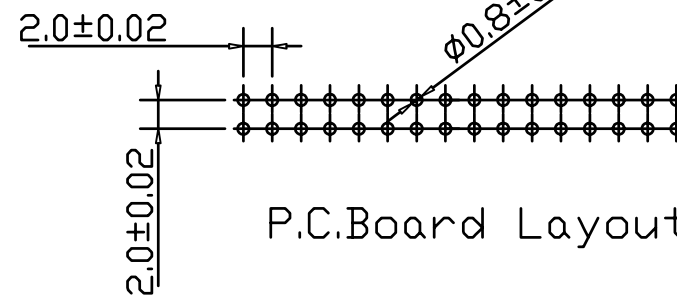
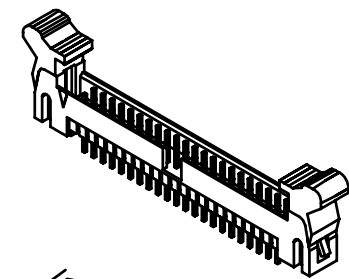


REV	ECN. NO.	APP	DATE
A	EDIÇÃO INICIAL		
B	NOVO CÓDIGO		



P.C.Board Layout




HOW TO ORDER:

2215[S][][]R

1 2 3

- 1- Pin Style
(R)-90°
(S)-180°
- 2- Nr. of Ways
Table
- 3- Terminal Plating
(D)-FLASH Au
(F)-0,38um Au
(G)-0,76um AU
(C)-0,38um Au/Sn
(H)-0,76um Au/Sn

Circuits	Dimension			Circuits	Dimension		
	A	B	C		A	B	C
10	10,0	25,9	8,0	26	26,0	41,9	24,0
12	12,0	27,9	10,0	30	30,0	45,9	28,0
14	14,0	29,9	12,0	32	32,0	47,9	30,0
16	16,0	31,9	14,0	34	34,0	49,9	32,0
20	20,0	35,9	18,0	40	40,0	55,9	38,0
22	22,0	37,9	22,0	44	44,0	59,9	42,0
24	24,0	39,9	22,0	50	50,0	65,9	48,0

 MATRIX TECNOLOGIA IND. E COM. LTDA.		TITLE	DATE DECEMBER 2009
		UNIT mm	
		TOLERANCE: ±0.25	APP. BY
		TITLE SHROUDED HEADER 2,0mm 180°	CHK. BY
		MATL	DR. BY FELIPE
		DWG. NO.	