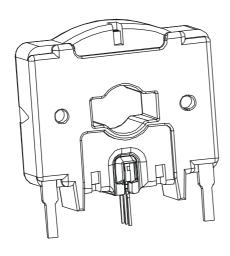
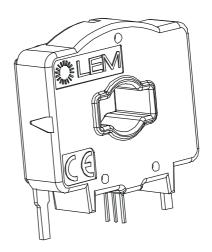


AUTOMOTIVE CURRENT SENSOR HC6H300-S

CE









Introduction

The HC6H Family is for use on the electronic measurement of DC, AC or pulsed currents in high power and low voltage automotive applications with a galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HC6H family gives you the choice of having different current measuring ranges in the same housing.

Features

- Open Loop transducer using the Hall effect
- Low voltage application
- Unipolar + 5 V DC power supply
- Primary current measuring range from 200 A up to 800 A
- Maximum rms primary admissible current: defined by busbar to have T° < + 150 ℃
- Operating temperature range: 40 °C < T° < + 125 °C
- Output voltage: full ratio-metric (in gain and offset)
- Compact design for PCB mounting.

Advantages

- Excellent accuracy
- · Very good linearity
- · Very low thermal offset drift
- Very low thermal gain drift
- Wide frequency bandwidth
- No insertion losses
- · Very good ratio size/current range

Automotive applications

- Starter Generators
- Converters
- Inverters
- Drives

Principle of HC6H Family

The open loop transducers use an Hall effect integrated circuit. The magnetic induction B, contributing to the rise of the Hall voltage, is generated by the primary current I_p to be measured. The control current I_p is supplied by a current source i.e. battery or generator (Fig. 1).

Within the linear region of the hysteresis cycle, B is proportional to:

$$I_p = (B)$$
 constant (a) $x I_p$

The Hall voltage is thus expressed by:

$$V_{\perp} = (K/d) \times I \times constant (a) \times I_{p}$$

Except for $I_{\rm p}$, all terms of this equation are constant. Therefore:

$$V_{H} = constant (b) \times I_{P}$$

The measurement signal $V_{\rm H}$ amplified to supply the user output voltage or current.

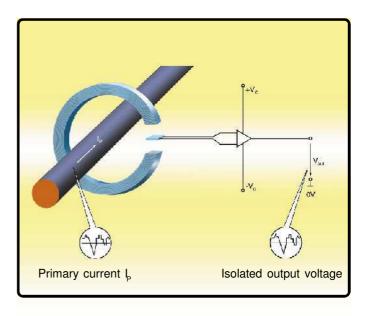


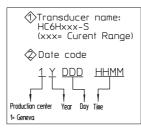
Fig. 1: Principle of the open loop transducer

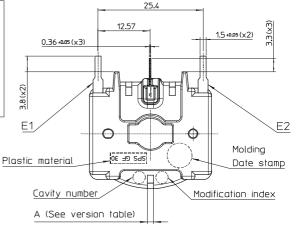


Dimensions HC6Hxxx-S family (in mm. 1mm = 0.0394 inch)

Secondary connection

Terminals	Designations
3	Supply voltage + 5 V DC
1	V _{out}
2	Ground
E1, E2	Ground (*)

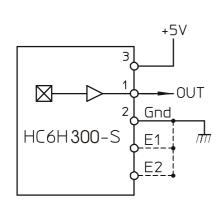


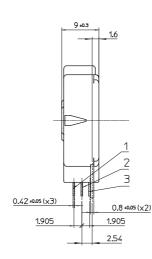


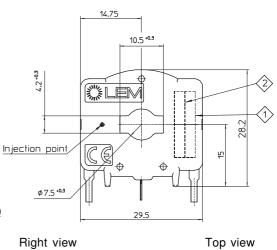
Rear view

(*) Only 1 of these 2 pins could be connected

Connection







Bill of materials

• Plastic case Magnetic core • Pins

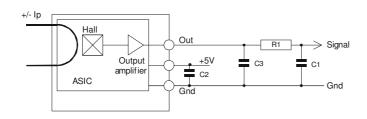
Mass

SPS GF 30 FeSi alloy Copper alloy tin platted (lead free) 23 g

Remarks

• General tolerance \pm 0.2 mm • $V_{\text{OUT}} > \frac{V_c}{2}$ when I_p flows in the direction of the arrow.

Electronic schematic



Power supply decoupling capacitor: C2 = 47 nF EMC protection capacitor : C3 = 4.7 nF

Optional:

High frequency signal noise filter:

R1 > 100 ohms

C1 = defined according to the system frequency bandwidth



Absolute maximum ratings (not operating)

Parameter	Symbol	Unit	Specification	Consitions
Maximun peak primary current (not operating)	I _{P maxi}	Α	Defined by busbar to have T° ≤ 150 °C	
Primary nominal DC or rms current	I _{PN}	Α	Defined by busbar to have T° ≤ 150 °C	
Maximun supply voltage (not operating)	V _{C maxi}	V	7	
Secondary maximum admissible power	P _{S maxi}	mW	150	
Ambient operating temperature	T _A	℃	- 40 < T _A < 125 ℃	
Ambient storage temperature	Ts	℃	- 40 < T _S < 125℃	
Electrostatic discharge voltage	V _{ESD}	V	2000	JESD22-A114-B
Maximum admissible vibration	γ	m.s ⁻²	3)	see note ³⁾
Rms voltage for AC isolation test	V _d	V	2000	IEC 60664-1

Operating characteristics

	O		Specification			Conditions			
	Symbol U	Unit	Mini	Typical	Maxi	Conditions			
	Electrical Data								
Primary current, measuring range	I _{PM}	Α	-300	-	300	@ - 40 °C < T° < 125 °C			
Supply voltage 1)	V_{C}	V	4.75	5.00	5.25	@ - 40 °C < T° < 125 °C			
Analog output voltage	\mathbf{V}_{out}	V	$V_{OUT} = V_C/5 x (2.5 + 0.0067 x I)$		0.0067 x l)	@ - 40 °C < T° < 125 °C			
Sensitivity	G	V/A	0.0066	0.0067	0.0068	@ T _A = 25 ℃			
Offset voltage	Vo	V	2.471	2.5	2.529	@ $V_C = 5.00 \text{ V}$; $T_A = 25 ^{\circ}\text{C}$; $I_P = 0 \text{ A}$			
Current consumption	Ic	mA	-	15	20	@ - 40 °C < T° < 125 °C; 4.75 V < V _C < 5.25 V			
Load resistance	R_L	ΚΩ	2	-	-				
Output internal resistance	\mathbf{R}_{OUT}	Ω	-	-	10				
Performance Data									
Sensitivity error	$\epsilon_{\scriptscriptstyleG}$	%	-2.0	±0.7	2.0	@ $T_A = 25$ °C, $V_C = 5.00$ V; Gth = 0.0067			
Electrical offset current	I _{OE}	Α	-1.9	±0.7	1.9	-@ V _C = 5.00 V: T _A = 25 °C			
Electrical offset current	V _{OE}	mV	-12.7	±4.7	12.7	₩ V _C = 5.00 V, I _A = 25 C			
Magnetic offset current	I _{OM}	Α	-2.4	±1.5	2.4	After excursion to ± I _P ; T _A = 25 ℃			
	V _{OM}	mV	-16	±10	16	G Title execution to 2 -p, 1 A 20 C			
I _{OE} [TCI _{OE}	mA/℃	-21	±12	21	@ - 40 °C < T° < 125 °C; V _C = 5.00 V			
Temperature coefficient of \mathbf{v}_{OF}	TCV _{OE}	mV/°C	-0.14	±0.08	0.14				
Temperature coefficient of G	TCG	%/℃	-0.04	±0.02	0.04	@ - 40 °C < T° < 125 °C; V _C = 5.00 V			
Linearity error	$\epsilon_{\scriptscriptstyle m L}$	% I _P	-1.0	±0.5	1.0	@ I _P ; V _C = 5.00 V, T _A = 25 ℃			
Response time	t _r	μs	-	8	15	@ di/dt = 50 A/µs; I _T = 300 A			
Frequency bandwidth 2)	BW	kHz	20	-	-	@ -3 dB			
Output voltage noise peak-peak	\mathbf{V}_{nop-p}	mV	-	20	25	@ T _A = 25 °C; 0 Hz < f < 1 MHz			
Output voltage noise rms	$\mathbf{V}_{no\;rms}$	mV	-	2	4	@ T _A = 25 °C; 0 Hz < f < 1 MHz			

Notes: 1) The output voltage \mathbf{V}_{OUT} is fully ratio-metric and depends on the supply voltage \mathbf{V}_{C} . The \mathbf{V}_{C} value must be measured and used with the following formula:

$$Ip = \left(Vout - \frac{Vc}{2}\right) \times \frac{1}{G} \times \frac{5}{Vc} \qquad With G in (V/A)$$

- ²⁾ Small signal only to avoid excessive heatings of the magnetic core.
- 3) Depending on the customer application's set up.



PERFORMANCE PARAMETERS DEFINITIONS

Sensitivity:

The Transducer's sensitivity **G** is the slope of the straight line $\mathbf{V}_{\text{OUT}} = \mathbf{f}(\mathbf{I}_{\text{p}})$, it must establish the relation:

$$V_{OLIT}(I_p) = V_c/5 (G \times I_p + 2.5)$$
 (*)

* For all symetrics transducers

Offset voltage:

Is the output voltage when the primary current is null. The ideal value of \mathbf{V}_{O} is $\mathbf{V}_{\mathrm{C}}/2$. So, the difference of \mathbf{V}_{O} - $\mathbf{V}_{\mathrm{C}}/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

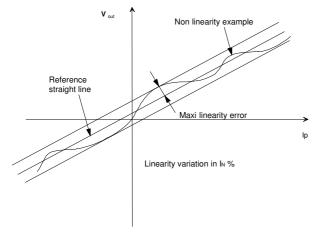
Magnetic offset:

The magnetic offset is the consequence of an over-current on the primary side. It's defined after an excursion of $I_{\rm p\ maxi}$.

Linearity:

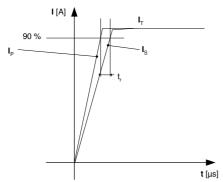
Is the maximum positive or negative discrepancy with a reference straight line $\mathbf{V}_{\text{OUT}} = \mathbf{f}(\mathbf{I}_{\text{p}})$.

Unit: linearity (%) expressed with full scale of $I_{P \text{ maxi}}$



Response time (delay time) t :

Is the time between the primary current signal and the output signal reach at 90 % of its final value



Output noise voltage:

The output voltage noise is the result of the noise floor of the Hall elements and the linear \mathbf{I}_{c} amplifier sensitivity.

Offset drift:

The error of the offset in the operating temperature \mathcal{E} Offset is the relative variation of the offset in the temperature considered with the initial offset at 25 °C. The offset temperature coefficient $\mathsf{TCV}_{\mathsf{OE}}$ ($\mathsf{TCI}_{\mathsf{OE}}$) in the operating temperature is the slope off \mathcal{E} Offset = \mathbf{f} (T).

Sensitivity drift:

The error of the sensitivity in the operating temperature Gain Error is the relative variation of the sensitivity in the temperature considered with the initial sensitivity at $25\,^{\circ}\text{C}$ sensitivity temperature coefficient TCG.

Typical:

Theorical value or usual accuracy recorded during the production.

Environmental test specifications

Name	Standard	Conditions				
Thermal shocks	IEC 60068 Part 2-14	T° - 40 °C to 125 °C /1000 cycles not connected				
Low T°operation at mini supply voltage	IEC 60068 Part 2-1	T° - 40 °C / 1000 H supply voltage = 4.75 V				
High T° operation at maxi supply voltage	IEC 60068 Part 2-2	T°125°C / 1000 H supply voltage = 5.25 V				
Temperature humidity bias	IEC 60068 Part 2-3	T° 85℃ / 85 % RH/ 1000 H				
Mechanical Tests						
Vibration	IEC 60068 Part 2-64	see note 3) page 4				
Drop test	IEC 60068 Part 2-29	Height 750 mm concrete floor each directions				
EMC Test						
Electrostatic discharge	JESD22-A114-B	Applied voltage = ± 2 kV pin to pin number of discharge = 1				
Rms voltage for AC isolation test	IEC 60664 Part 1	2 kV, 50 Hz, 1 min				
Bulk current injected- radiated immunity	ISO 11452 Part 4					