

AUTOMOTIVE CURRENT TRANSDUCER HC2H250-S CLIPS









Introduction

The HC2H CLIPS Family is for the electronic measurement of DC, AC or pulsed currents in high power and low voltage automotive applications with galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HC2H CLIPS family gives you the choice of having different current measuring ranges in the same housing.

Features

- Open Loop transducer using the Hall effect
- Low voltage application
- Unipolar + 5 V DC power supply
- Primary current measuring range from 80 A up to 250 A
- Maximum rms primary current limited by the busbar, the magnetic core or the ASIC temperature T° < + 150°C
- Operating temperature range: 40°C < T° < + 125°C
- Output voltage: full ratiometric (in sensitivity and offset)
- · Compact design for PCB mounting.

Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- Very low thermal sensitivity drift
- Wide frequency bandwidth
- No insertion losses.

Automotive applications

- Electrical Power Steering
- Starter Generators
- Converters ...

Principle of HC2H CLIPS Family

The open loop transducers use an Hall effect integrated circuit.

The magnetic flux density ${\bf B}$, contributing to the rise of the Hall voltage, is generated by the primary current ${\bf I}_{\rm P}$ to be measured.

The current to be measured I_p is supplied by a current source i.e. battery or generator (Fig. 1).

Within the linear region of the hysteresis cycle, ${\bf B}$ is proportional to:

B
$$(I_p)$$
 = constant (a) x I_p

The Hall voltage is thus expressed by:

$$V_{H} = (R_{H}/d) \times I \times constant (a) \times I_{D}$$

Except for \mathbf{I}_{p} , all terms of this equation are constant. Therefore:

$$V_{\perp}$$
 = constant (b) x I_{p}

The measurement signal \mathbf{V}_{H} amplified to supply the user output voltage or current.

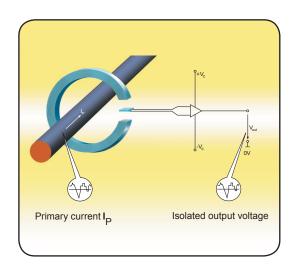
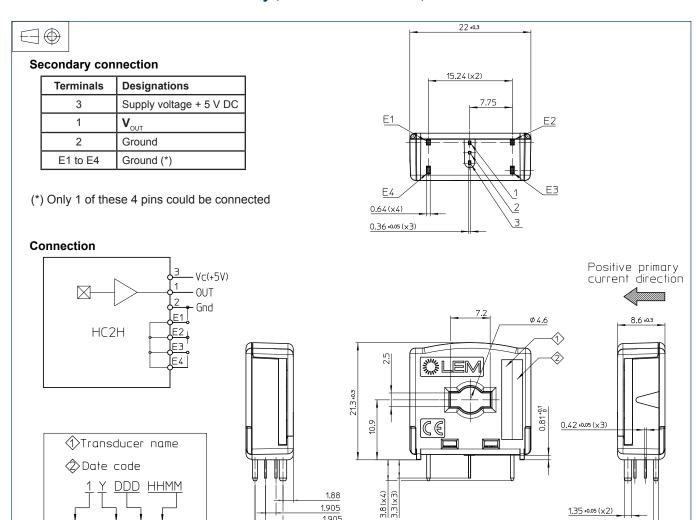


Fig. 1: Principle of the open loop transducer



HC2H250-S CLIPS

Dimensions HC2H250-S CLIPS family (in mm. 1mm = 0.0394 inch)



Bill of materials

Production center

Plastic case PBT GF 30 Magnetic core FeSi alloy Pins Copper alloy tin platted (lead free)

Day

7.5.g Mass

Remarks

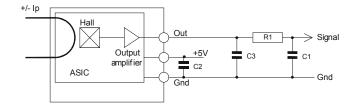
1.905

5.08(x2)

- General tolerance ± 0.2 mm
- $V_{OUT} > \frac{V_c}{2}$ when I_p flows in the direction of the arrow.

0.9 ±0.05 (×2)

Electronic schematic



Power supply decoupling capacitor C2 = 47 nF

EMC protection capacitor C3 = 4.7 nF

Optional:

High frequency signal noise filter:

 $R1 > 100 \Omega$

C1 = defined according to the system frequency bandwidth



HC2H250-S CLIPS

Absolute maximum ratings (not operating)

Parameter	Symbol	Unit	Specification	Conditions
Maximum primary current (not operating)	I _{P max}	А	Defined by busbar to have T° ≤ 150°C	
Primary nominal DC or current rms	I _{PN}	А	Defined by busbar to have T° ≤ 150°C	
Maximum supply voltage (not operating)	V _{C max}	V	7	
Secondary maximum admissible power	P _{S max}	mW	150	
Ambient operating temperature	T _A	°C	- 40 < T _A < 125°C	
Ambient storage temperature	T _s	°C	- 40 < T _S < 125°C	
Electrostatic discharge voltage	V _{ESD}	V	2000	see page 4/4
Maximum admissible vibration	γ	m.s -2	100	see page 4/4
Rms voltage for AC isolation test	V _d	V	2000	

Operating characteristics

Parameter	Symbol	Unit	Specification			Conditions			
			Min	Typical	Max	Conditions			
Electrical Data									
Primary current, measuring range	I _{PM}	Α	-250		250	@ - 40°C < T° < 125°C			
Supply voltage 1)	$\mathbf{V}_{_{\mathrm{C}}}$	V	4.75	5.00	5.25	@ - 40°C < T° < 125°C			
Output voltage (Analog) 1)	$\mathbf{V}_{\mathrm{out}}$	V	$\mathbf{V}_{OUT} = (\mathbf{V}_{C}/5) \times (2.5 + 0.008 \times \mathbf{I}_{P})$		0.008 x I _P)	@ - 40°C < T° < 125°C			
Sensitivity 1)	G	V/A	0.0078	0.008	0.0082	$\textcircled{0}$ $T_A = 25^{\circ}\text{C}$; $V_C = 5 \text{ V}$			
Offset voltage 1)	V _o	V	2.477	2.5	2.523	(2) $\mathbf{V}_{C} = 5 \text{ V}; \mathbf{T}_{A} = 25^{\circ}\text{C}; \mathbf{I}_{P} = 0 \text{ A}$			
Current consumption	I _c	mA	-	13.5	17	@ - 40° C < T° < 125° C; 4.75 V < V_{c} < 5.25 V			
Load resistance	R _L	ΚΩ	2	-	-				
Output internal resistance	R _{OUT}	Ω	-	-	10				
Performance Data (1)									
Sensitivity error	$\epsilon_{_{ m G}}$	%	-2.0	± 0.7	2.0	$\textcircled{T}_{A} = 25^{\circ}\text{C}, V_{C} = 5 \text{ V}; \text{ Gth} = 0.008 \text{ V/A}$			
Electrical offset	I _{OE}	Α	-1.5	± 0.63	1.5	$\textcircled{Q} V_{C} = 5 \text{ V}; \ T_{A} = 25^{\circ}\text{C}$			
	V _{OE}	mV	-12	± 5	12				
Magnetic effect	I _{OM}	Α	-1.4	± 0.9	1.4	@ After excursion to $\pm I_p$; $T_A = 25^{\circ}C$			
Magnetic offset	V _{OM}	mV	-11.2	± 7.2	11.2				
Temperature coefficient of I _{OE}	TCI _{OEAV}	mA/°C	-15.0	± 6.3	15.0	@ - 40°C < T° < 125°C; V _C = 5 V			
Temperature coefficient of V_{OE}	TCV _{OEAV}	mV/°C	-0.12	± 0.05	0.12				
Temperature coefficient of G	TCG _{AV}	%/°C	-0.04	± 0.02	0.04	@ - 40° C < T° < 125° C; V_{C} = 5 V			
Linearity error	\mathcal{E}_{L}	% I _P	-1.5	± 0.8	1.5				
Response time	t _r	μs	-	15	20	@ di/dt = 50 A/μs; I _T = 100 A			
Frequency bandwidth 2)	BW	kHz	20	-	-	@ -3 dB; I _T = 40 A rms			
Phase shift	Δφ	0	-	-	TBD				
Output voltage noise peak-peak	V _{no p-p}	mV	-	13	18	@ T _A = 25°C; 0 Hz < f < 1 MHz			
Output voltage noise rms	V _{no rms}	mV	-	1.5	3	@ T _A = 25°C; 0 Hz < f < 1 MHz			

Notes: 1) The output voltage \mathbf{V}_{OUT} is fully ratiometric. The offset and sensitivity are dependent on the supply voltage \mathbf{V}_{C} relative to the following formula:

$$I_P = \left(V_{\text{OUT}} - \frac{V_c}{2}\right) \times \frac{1}{G} \times \frac{5}{V_c}$$
 with G in (V/A)

²⁾ Small signal only to avoid excessives heatings of the busbar, the magnetic core and the ASIC.



HC2H250-S CLIPS PERFORMANCES PARAMETERS DEFINITIONS

Output noise voltage:

The output voltage noise is the result of the noise floor of the Hall elements and the linear I_c amplifier gain.

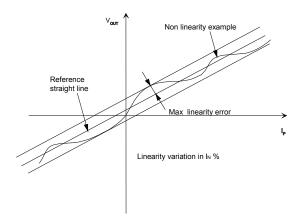
Magnetic offset:

The magnetic offset is the consequence of an over-current on the primary side. It's defined after an excursion of $I_{\rm P\,max}$.

Linearity:

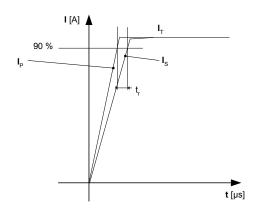
The maximum positive or negative discrepancy with a reference straight line \mathbf{V}_{OUT} = f (\mathbf{I}_{p}).

Unit: linearity (%) expressed with full scale of I_{P max}.



Response time (delay time) t.:

The time between the primary current signal and the output signal reach at 90 % of its final value



Typical:

Theorical value or usual accuracy recorded during the production.

Sensitivity:

The Transducer's sensitivity **G** is the slope of the straight line $V_{out} = f(I_p)$, it must establish the relation:

 $V_{out}(I_{p}) = V_{c}/5 (G \times I_{p} + 2.5) (*)$

(*) For all symetrics transducers.

Offset with temperature:

The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset at 25°C.

The offset variation I_{OT} is a maximum variation the offset in the temperature range:

 $I_{OT} = I_{OE} \max - I_{OE} \min$

The Offset drift \mathbf{TCI}_{OEAV} is the \mathbf{I}_{OT} value divided by the temperature range.

Sensitivity with temperature:

The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25°C.

The sensitivity variation \mathbf{G}_{T} is the maximum variation (in ppm or %) of the sensitivity in the temperature range:

 $\mathbf{G}_{\scriptscriptstyle T}$ = (Sensitivity max - Sensitivity min) / Sensitivity at 25°C.

The sensitivity drift \mathbf{TCG}_{AV} is the \mathbf{G}_{T} value divided by the temperature range.

Offset voltage @ Ip = 0 A:

Is the output voltage when the primary current is null. The ideal value of \mathbf{V}_{O} is $\mathbf{V}_{\text{C}}/2$ at $\mathbf{V}_{\text{C}}=5$ V. So, the difference of \mathbf{V}_{O} - $\mathbf{V}_{\text{C}}/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

Environmental test specifications

Name	Standard	Conditions					
Thermal shocks	IEC 60068 Part 2-14	T° - 40°C to 125°C /500 cycles not connected					
Temperature humidity bias	JESD 22-A101	T° 85°C / 85 % RH/ 1000 H					
Mechanical Tests							
Vibration	IEC 60068 Part 2-64	Room T°, Acceleration 100m/ s2, frequency 20 to 500 Hz / 96 H each axis					
Drop test	ISO 16750-3 & 4.3	Height 1 m, 3 axis, 2 directions per axis, concrete floor					
EMC Test							
Electrostatic discharge	JESD22-A114-B	Applied voltage = ± 2 kV pin to pin number of discharge = 1					
Rms voltage for AC isolation test	ISO 16750-2 (labo)	2 kV, 50 Hz, 1 min					
Bulk current injected- radiated immunity	ISO 11452 Part 4						