

# **STN2907S**

**PNP Silicon Transistor** 

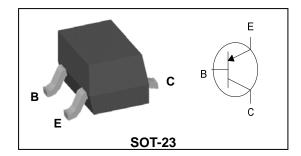
### **Descriptions**

- General purpose application
- Switching application

#### **Features**

- Large collector current: I<sub>C</sub>=-600mA
- Low collector saturation voltage:  $V_{CE(sat)}$ = -0.4V(Max.) @  $I_C$ = -150mA,  $I_B$ = -15mA
- Complementary pair with STN2222S

#### **PIN Connection**



### **Ordering Information**

Type NO.	Marking	Package Code
STN2907S	<u>GA</u> <u>□</u> ① ②	SOT-23

1) Device Code 2) Year&Week Code

### **Absolute maximum ratings**

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Collector-Base voltage	$V_{CBO}$	-60	V
Collector-Emitter voltage	$V_{\sf CEO}$	-40	V
Emitter-Base voltage	$V_{EBO}$	-5	V
Collector current	I <sub>C</sub>	-600	m A
Collector power dissipation	P <sub>C</sub> *	350	m W
Junction temperature	TJ	150	°C
Storage temperature range	$T_{stg}$	-55~ 150	°C

<sup>\* :</sup> Package mounted on 99.5% Alumina 10×8×0.6mm.

#### **Electrical Characteristics**

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_C = -10 \mu A, I_E = 0$	-60	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_{C} = -1  \text{m A}, I_{B} = 0$	-40	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	$I_E = -10 \mu A, I_C = 0$	-5	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -60 \text{ V}, I_{E} = 0$	-	-	-10	nA
DC current gain	h <sub>FE</sub>	$V_{CE}$ = -10V, $I_{C}$ = -10mA	75	-	-	-
Collector-Emitter saturation voltage	$V_{CE(sat)}$	$I_{C}$ = -150mA, $I_{B}$ = -15mA	-	-	-0.4	V
Transition frequency	f <sub>T</sub>	$V_{CE}$ = -20V, $I_{C}$ = -20m A	250	-	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = -10 V$ , $I_{E} = 0$ , $f = 1 MHz$	-	6.0	-	pF

#### **Electrical Characteristic Curves**

Fig.  $1 P_C - T_a$ 

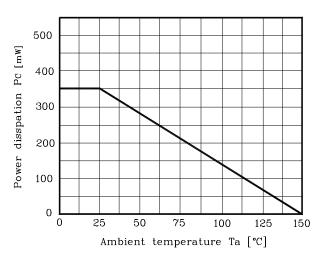


Fig. 3 I  $_{\text{C}}$  -  $\,V_{\text{CE}}$ 

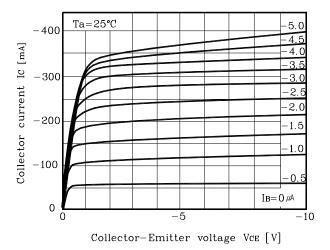


Fig. 5  $h_{FE}$  -  $I_C$ 

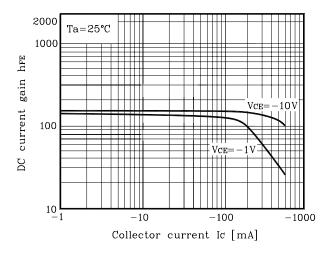


Fig. 2  $I_C$  -  $V_{BE}$ 

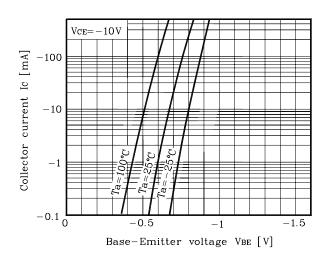


Fig. 4  $V_{CE(sat)}$  -  $I_C$ 

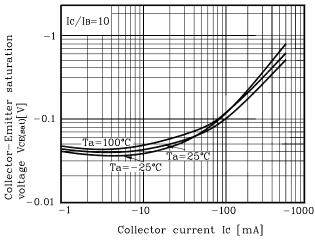
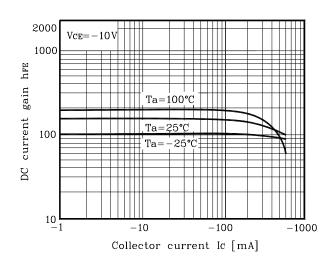
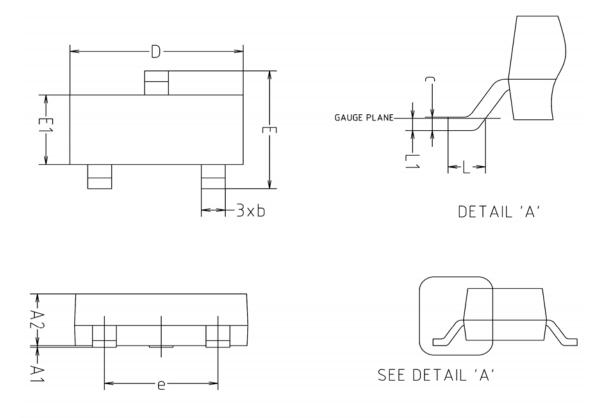


Fig. 6  $h_{FE}$  -  $I_C$ 



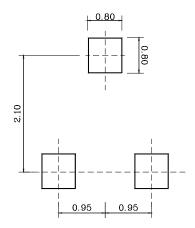
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## **Outline Dimension**



SYMBOL	MILLIMETERS			NOTE	
STRIBUL	MINIMUM	NOMINAL	MAXIMUM	NOTE	
A1	0.00	-	0.10		
A2	0.82	-	1.02		
Ь	0.39	0.42	0.45		
С	0.09	0.12	0.15		
D	2.80	2.90	3.00		
E	2.20	2.40	2.60		
E1	1.20	1.30	1.40		
е	1.90BSC				
L	0.20	-	-		
L1	0.12BSC				

### \*Recommend PCB solder land [Unit: mm]



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