

SRC1210S

NPN Silicon Transistor

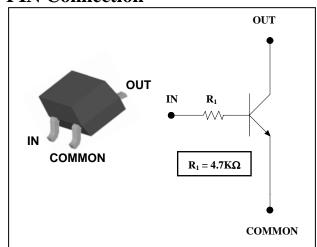
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRC1210S	<u>RCA</u> □ ① ②	SOT-23	

①Device Code ②Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	V _I	20, -5	V
Output current	Io	100	m A
Power dissipation	P_D	200	m W
Junction temperature	T _J	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50 V, V_1 = 0$	-	-	500	nΑ
DC current gain	Gı	$V_0 = 5V$, $I_0 = 10 \text{ m A}$	120	-	-	-
Output voltage	V _{O(ON)}	$I_O = 10 \text{ m A}, I_I = 0.5 \text{ m A}$	-	0.1	0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_0 = 0.2V$, $I_0 = 5 \text{ m A}$	-	0.8	1.2	V
Input voltage (OFF)	V _{I(OFF)}	$V_{O} = 5V$, $I_{O} = 0.1 \text{ m A}$	0.3	0.55	-	V
Transition frequency	f _T *	$V_{O}=10V, I_{O}=5mA, f=1MHz$	-	200	-	MHz
Input current	I_1	$V_1 = 5V, I_0 = 0$	-	-	1.8	m A
Input resistor (Input to base)	R ₁	-	3.3	4.7	6.1	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 P_D - Ta

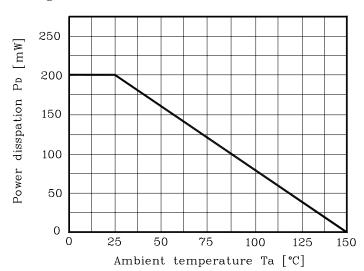


Fig. 2 $I_{\rm O}$ - $V_{\rm I(ON)}$

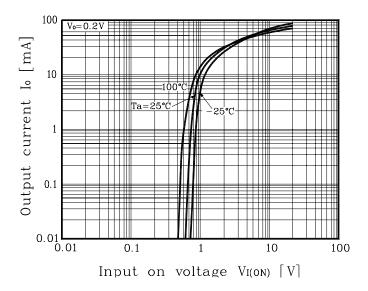


Fig. 3 $I_{\rm O}$ - $V_{\rm I(OFF)}$

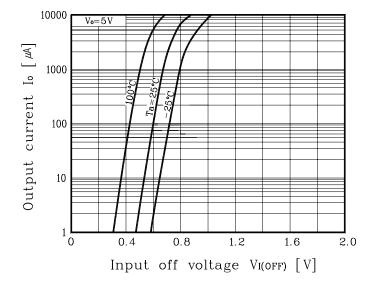
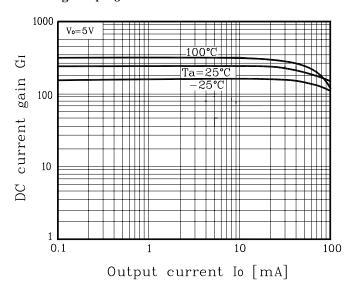


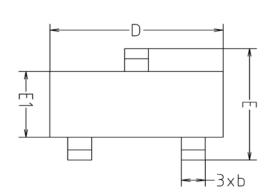
Fig. 4 G_I - I_O

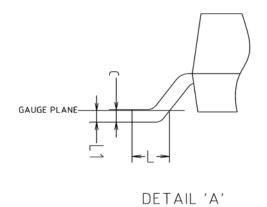


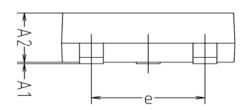
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Outline Dimension



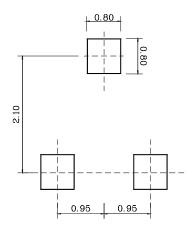






SYMBOL	MILLIMETERS		NOTE	
3111000	MINIMUM	NOMINAL	MAXIMUM	11012
A1	0.00	-	0.10	
A2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

*Recommend PCB solder land [Unit: mm]



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