KODENSHI AUK

SRA2219

PNP Silicon Transistor

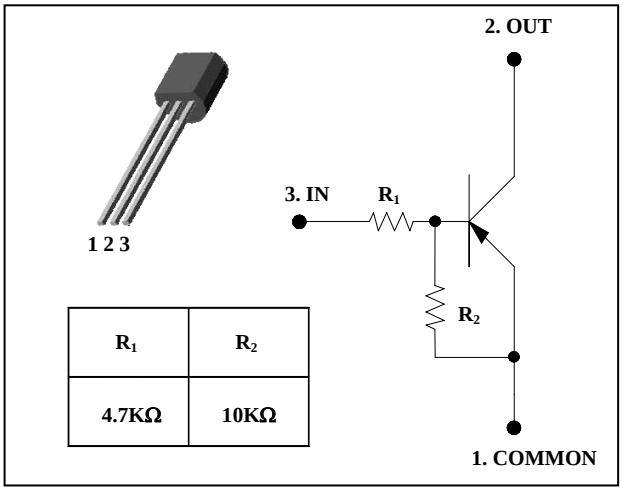
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



R_1	R_2
4.7KΩ	10KΩ

Ordering Information

Type NO.	Marking	Package Code
SRA2219	SRA2219	TO-92

Absolute Maximum Ratings (Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	V_O	-50	V
Input voltage	V_I	-20, 7	V
Output current	I_O	-100	mA
Power dissipation	P_D	625	mW
Junction temperature	T_J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics (Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	$I_{O(OFF)}$	V_O=-50V, V_I=0	-	-	-500	nA
DC current gain	G_I	V_O=-5V, I_O=-10mA	30	-	-	-
Output voltage	$V_{O(ON)}$	I_O=-10mA, I_I=-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	V_O=-0.2V, I_O=-5mA	-	-1.2	-1.6	V
Input voltage (OFF)	$V_{I(OFF)}$	V_O=-5V, I_O=-0.1mA	-0.5	-0.82	-	V
Transition frequency	f_T^*	V_O=-10V, I_O=-5mA, f=1MHz	-	200	-	MHz
Input current	I_I	V_I=-5V, I_O=0	-	-	-1.8	mA
Input resistor (Input to base)	R_1	-	3.3	4.7	6.1	KΩ
Input resistor (Base to common)	R_2	-	7	10	13	KΩ

* : Characteristic of transistor only

Electrical Characteristic Curves

Fig. 1 P_D - Ta

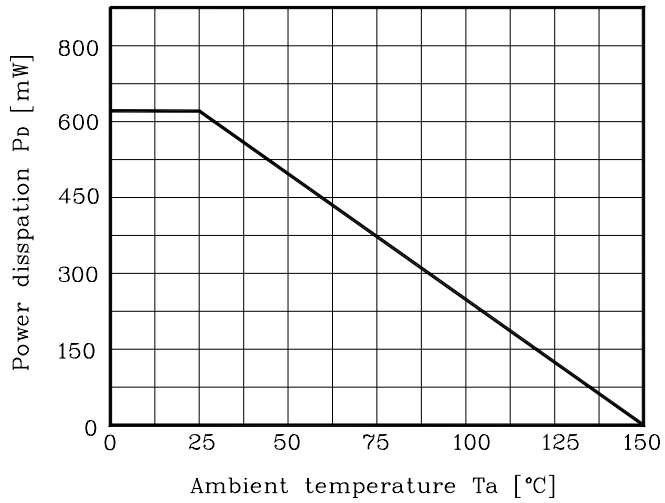


Fig. 2 I_O - $V_{I(ON)}$

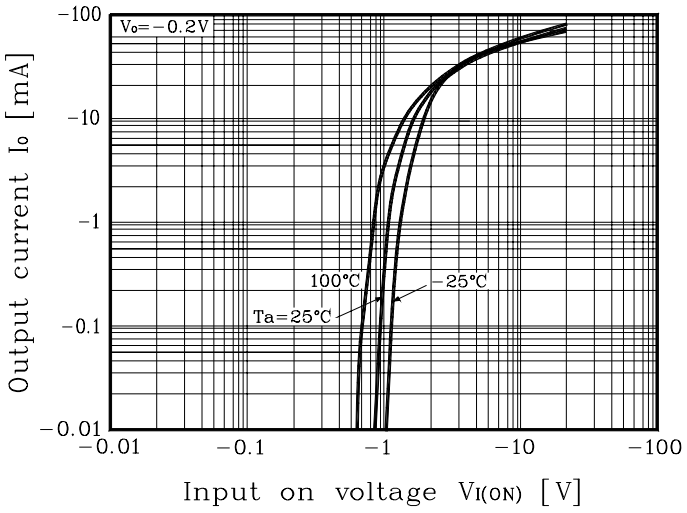


Fig. 3 I_O - $V_{I(OFF)}$

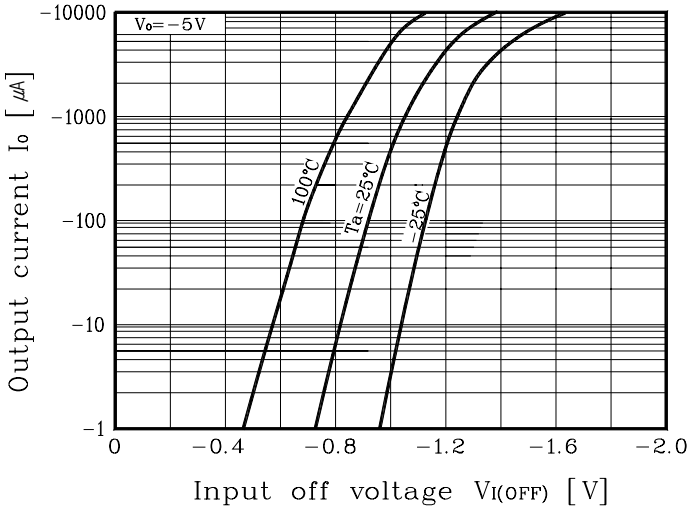
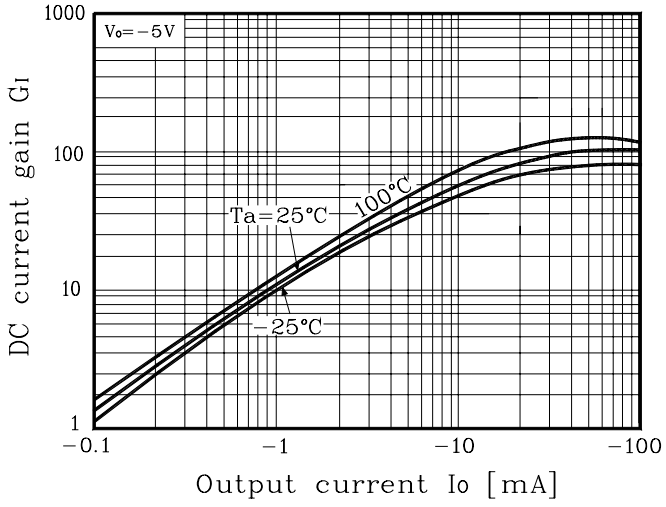
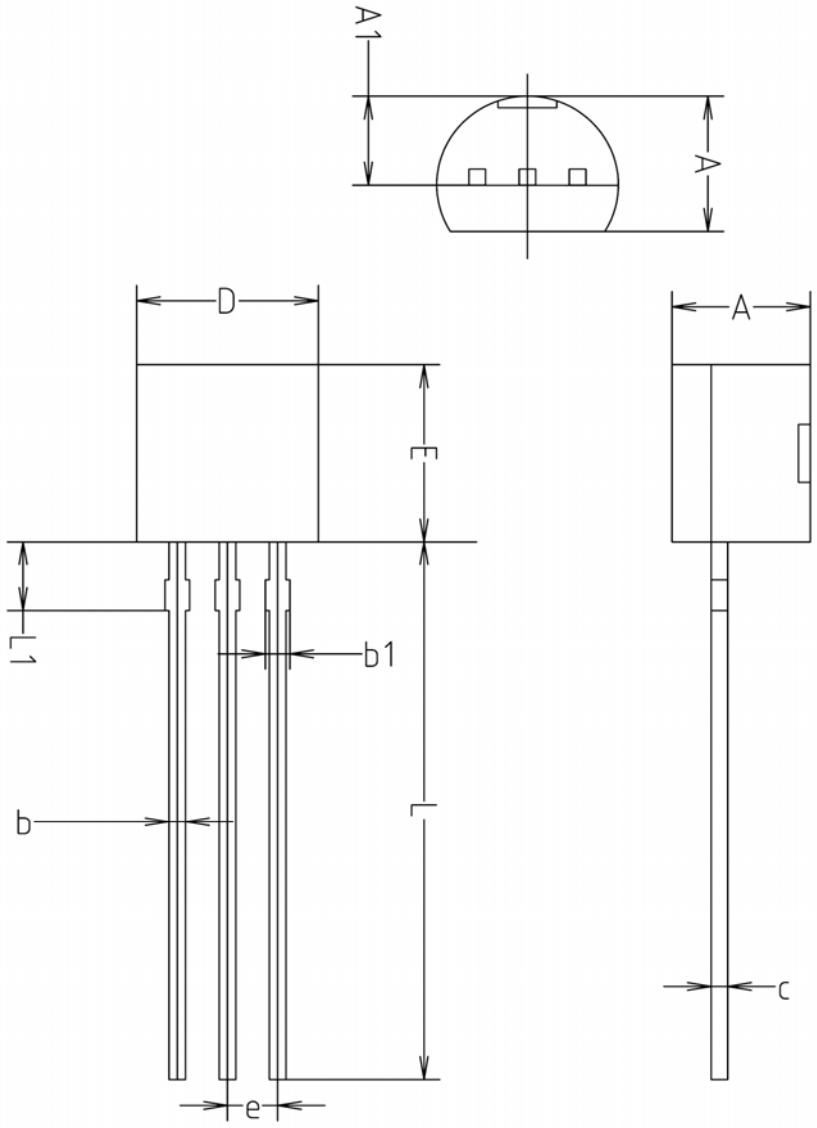


Fig. 4 G_I - I_O



Outline Dimension



SYMBOL	MILLMETERS(mm)		
	MINIMUM	NOMINAL	MAXIMUM
A	3.40	3.50	3.66
A1	2.46	2.51	2.59
b	0.39	0.44	0.53
b1	0.39	–	0.63
c	0.35	0.42	0.47
D	4.48	4.60	4.70
E	4.48	4.60	4.70
e	1.17	1.27	1.37
L	13.70	14.00	14.77
L1	1.55	1.70	2.15

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