

## Description

The SL71063 is a voltage and current control IC which contains precision voltage reference. It is suitable for battery charger, second controller of switching regulator systems, and other battery systems.

## Features

- Constant Voltage & Constant Current Control
- Wide input voltage range : 2.2V to 14V
- Internal Precision Voltage Reference : 1.240V  $\pm$  1%
- Photo Coupler ( PC ) Drive Current : 40mA Typ.
- Operating Current : 1mA Max.
- Easy compensation & Low external component count.
- Package Outline : SOT26.

## Applications

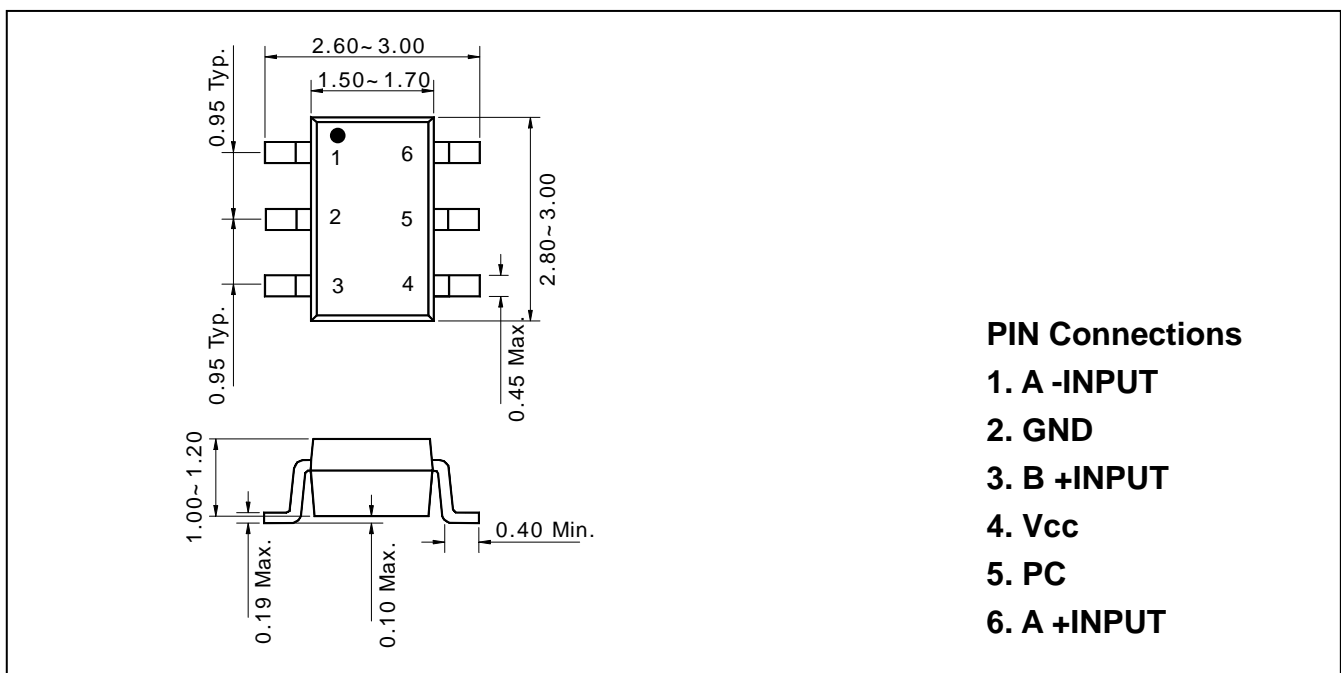
- Adapters
- Battery chargers

## Ordering Information

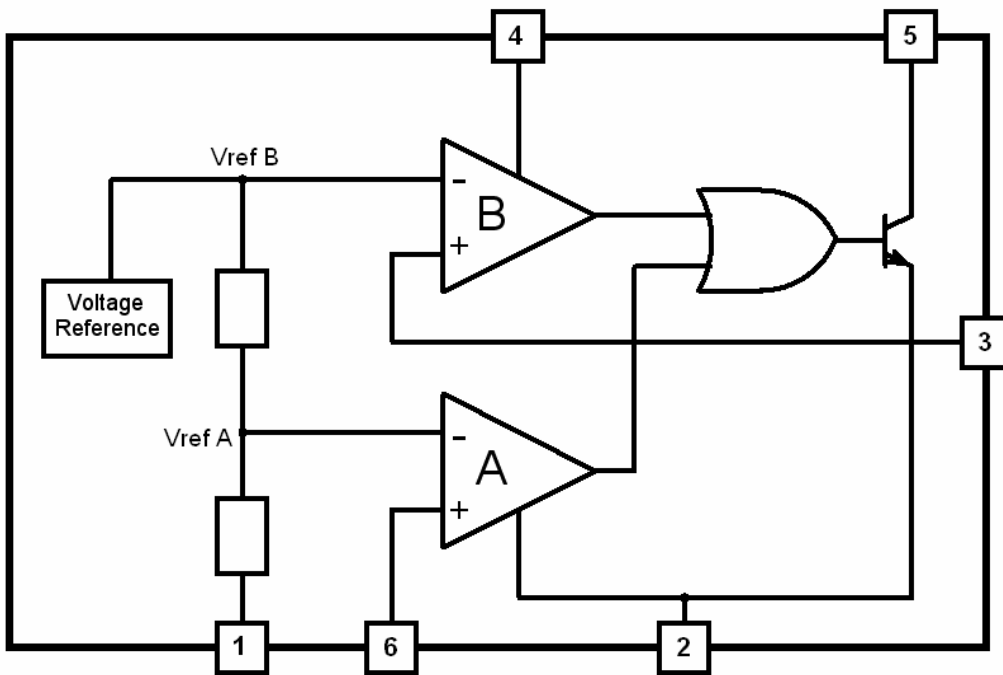
Type NO.	Marking	Package Code
SL71063	763	SOT - 26

## Outline Dimensions

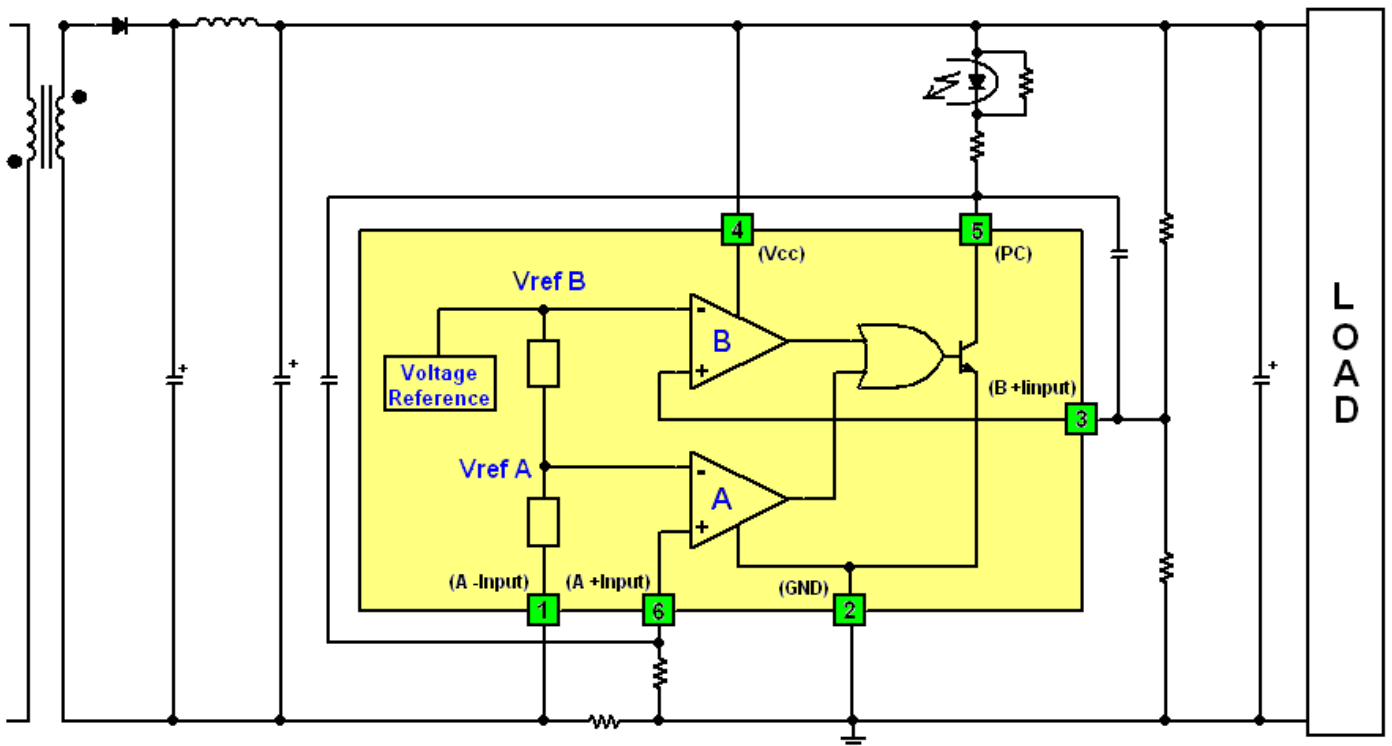
unit : mm



Internal Block Diagram



Typical Application : Adapter or Battery Charger Application Using SL71063



In the above application schematic, the SL71063 is used on the secondary side of a flyback adapter (or battery charger) to provide an accurate control of voltage and current.

The above feedback loop is made with an optocoupler.

## Pin Description

Name	Pin No.	Function
A -INPUT	1	Input Pin of the Current Control Loop
GND	2	Ground Line. 0V Reference For All Voltages
B +INPUT	3	Input Pin of the Voltage Control Loop
Vcc	4	DC Supply Line
PC	5	Photo Coupler (P/C) Drive Pin. Sinking Current Only
A +INPUT	6	Input Pin of the Current Control Loop

## Absolute Maximum Ratings

Characteristic	Symbol	Value	Unit
DC Supply Voltage	Vcc	16	V
Power Dissipation	Pd *	500	mW
Operating Ambient Temperature Range	Topr	-40 to 85	°C
Storage Temperature Range	Tstg	-55 to 150	°C

\* Mount on a glass epoxy circuit board of 30x30mm Pad dimension of 50mm<sup>2</sup>

## Operating Conditions

Characteristic	Symbol	Value	Unit
DC Supply Voltage	V <sub>CC</sub>	2.2 to 14	V

## Electrical Characteristics

T<sub>a</sub> = 25°C and V<sub>CC</sub> = 5V (unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Operating Current	I <sub>CC</sub>	I <sub>PC</sub> = off	-	0.6	1.0	mA
Leakage Current	I <sub>PCLEAK</sub>	V <sup>+</sup> = V <sub>PC</sub> = 14V	-	-	10	μA
Saturation Voltage	V <sub>PCSAT</sub>	I <sub>PC</sub> = 20 mA	-	500	700	mV
PC Terminal Current	I <sub>PC</sub>	-	20	40	70	mA
Output Voltage High	V <sub>OH</sub>	R <sub>L</sub> = 30 kΩ	-	V <sub>CC</sub> - 0.1	-	V

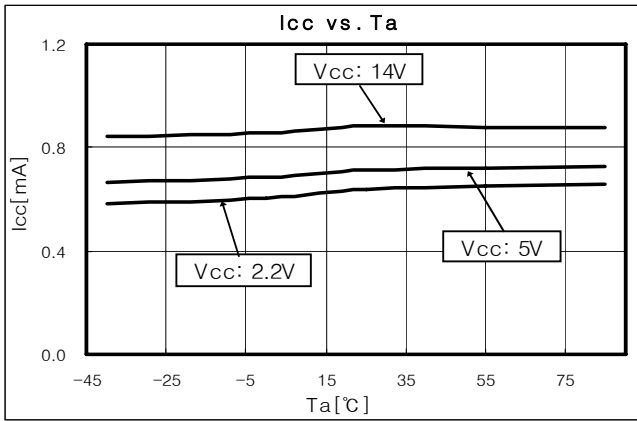
## Electrical Characteristics (Continued)

Ta = 25°C and V<sub>CC</sub> = 5V (unless otherwise specified)

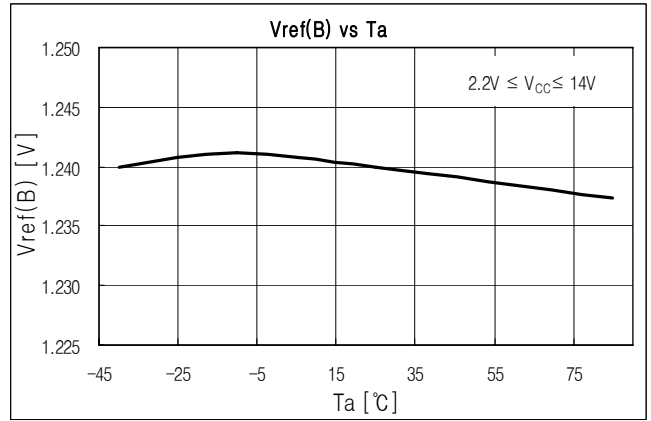
Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>[A CH : Current Control Loop]</b>						
Reference Voltage <sup>1)</sup>	V <sub>REF(A)</sub>	I <sub>PC</sub> = 2.5 mA	69	72	75	mV
Input Bias Current	I <sub>B(A)</sub>	-	-	20	50	μA
Transconduction Gain <sup>2)</sup>	G <sub>M(A)</sub>	-	1.5	8	-	mA/mV
<b>[B Ch : Voltage Control Loop]</b>						
Reference Voltage <sup>3)</sup>	V <sub>REF(B)</sub>	I <sub>PC</sub> = 1.5 mA	1.227	1.240	1.253	V
Input Bias Current	I <sub>B(B)</sub>	-	-	50	500	nA
Transconduction Gain <sup>4)</sup>	G <sub>M(B)</sub>	-	1	3	-	mA/mV

- 1) The internal current sense threshold is set to -72mV. The current control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the trans-conduction operational amplifier.
- 2) When the positive input at ICTRL is lower than -72mV, and the voltage is decreased by 1mV, the sinking current at the output OUT will be increased by 8mA.
- 3) The internal voltage reference is set at 1.240V (bandgap reference). The voltage control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the trans-conductance operational amplifier.
- 4) If the voltage on B +Input (the positive input of the amplifier) is higher than the negative amplifier input (V<sub>ref(B)</sub> = 1.240V), and it is increased by 1mV, the sinking current at the output OUT will be increased by 3mA.

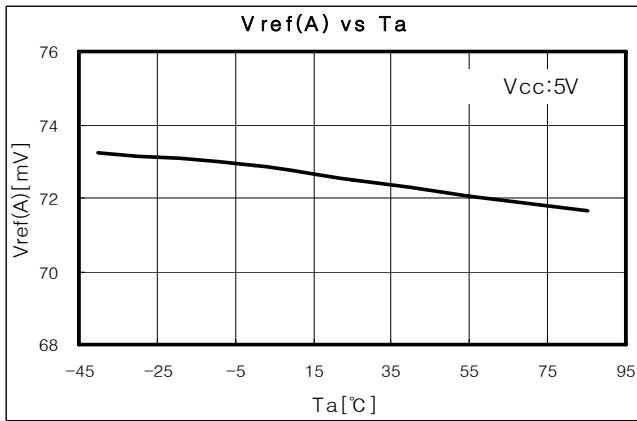
**Fig.1 Supply current vs Ambient Temperature**



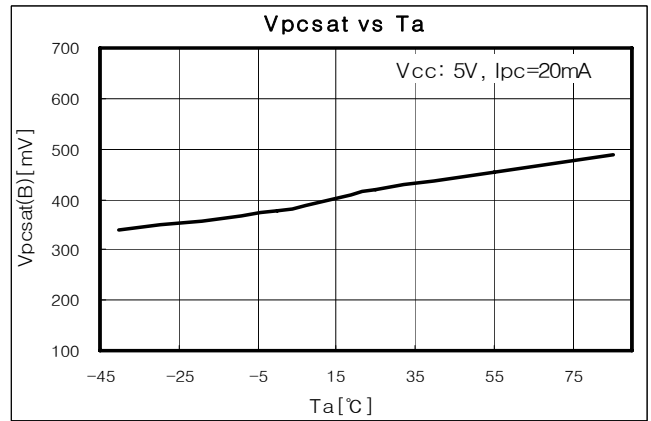
**Fig.2 Vref(B) vs Ambient Temperature**



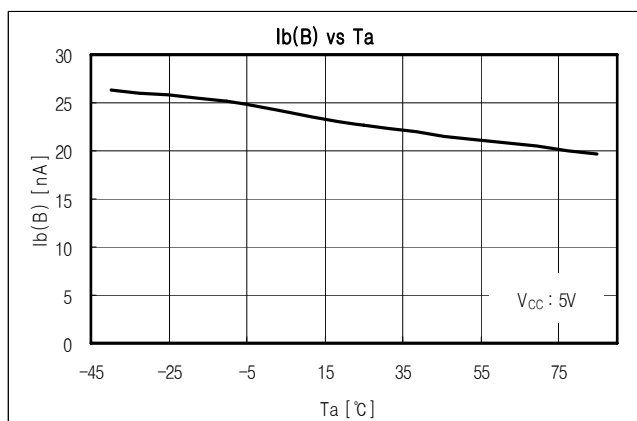
**Fig.3 Vref(A) vs Ambient Temperature**



**Fig.4 Saturation Voltage vs Ambient Temperature**



**Fig. 5 Input Bias Current vs Ambient Temperature**



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