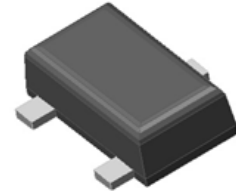


## General Purpose Schottky Barrier Diode

### General Description

These Schottky barrier diodes are designed for high-speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.



SOT-23F



### Features and Benefits

- Low forward drop voltage and low leakage current
- Very low switching time
- Full lead (Pb)-free device and RoHS compliant device
- Available in “Green” device

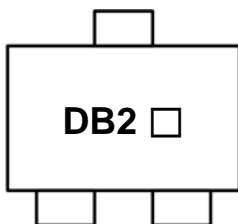
### Applications

- General purpose and high speed switching
- Protection circuit and voltage clamping

### Ordering Information

Part Number	Marking Code	Package	Packaging
SDB310WAF	DB2 □	SOT-23F	Tape & Reel

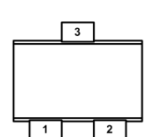
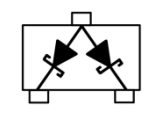
### Marking Information



DB2 = Specific Device Code

□ = Year & Week Code Marking

### Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol
1	Cathode (Diode 1)		
2	Cathode (Diode 2)		
3	Common Anode		

## Absolute Maximum Ratings ( $T_{amb}=25^{\circ}\text{C}$ , Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Peak reverse voltage	$V_{RM}$	40	V
DC reverse voltage	$V_R$	30	V
Repetitive peak forward current	$I_{FRM}$	0.5	A
Forward current	$I_F$	0.2	A
Non-repetitive peak forward surge current( $t=10\text{ms}$ )	$I_{FSM}$	2	A
Power dissipation <sup>1)</sup>	$P_D$	150	mW

<sup>1)</sup> Device mounted on FR-4 board with recommended pad layout.

## Thermal Characteristics ( $T_{amb}=25^{\circ}\text{C}$ , Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient <sup>1)</sup>	$R_{th(j-a)}$	833	$^{\circ}\text{C}/\text{W}$
Operating junction temperature	$T_j$	150	$^{\circ}\text{C}$
Storage temperature range	$T_{stg}$	-55 ~ 150	$^{\circ}\text{C}$

<sup>1)</sup> Device mounted on FR-4 board with recommended pad layout.

## Electrical Characteristics ( $T_{amb}=25^{\circ}\text{C}$ , Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Forward voltage <sup>2)</sup>	$V_{F(1)}$	$I_F=10\text{mA}$	-	-	0.4	V
	$V_{F(2)}$	$I_F=30\text{mA}$	-	-	0.5	V
Reverse leakage current <sup>3)</sup>	$I_R$	$V_R=30\text{V}$	-	-	1	$\mu\text{A}$
Total capacitance	$C_T$	$V_R=1\text{V}$ , $f=1\text{MHz}$	-	-	10	pF
Reverse recovery time	$t_{rr}$	$I_F=I_R=10\text{mA}$ , $I_{R(REC)}=1\text{mA}$	-	-	5	ns

<sup>2)</sup> Pulse test:  $t_p \leq 380\mu\text{s}$ , Duty cycle  $\leq 2\%$

<sup>3)</sup> Pulse test:  $t_p \leq 5\text{ms}$ , Duty cycle  $\leq 2\%$

## Rating and Characteristic Curves

Fig. 1) Typical Forward Characteristics

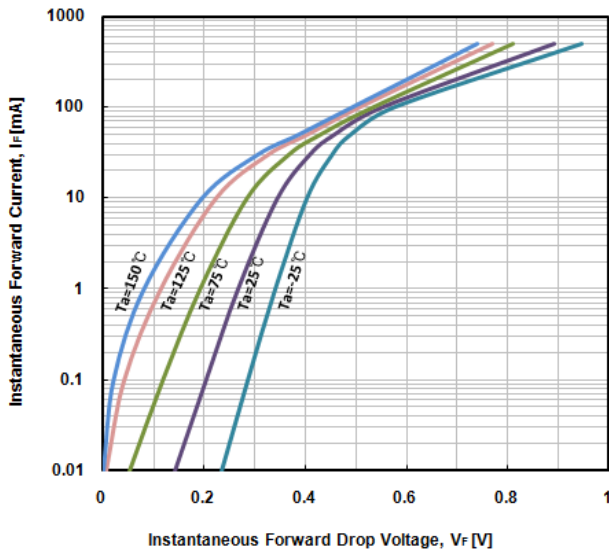


Fig. 2) Typical Reverse Characteristics

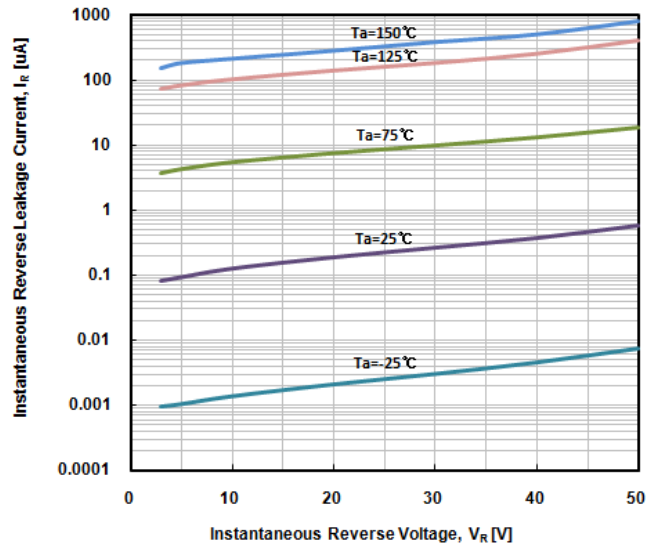


Fig. 3) Typical Total Capacitance Characteristics

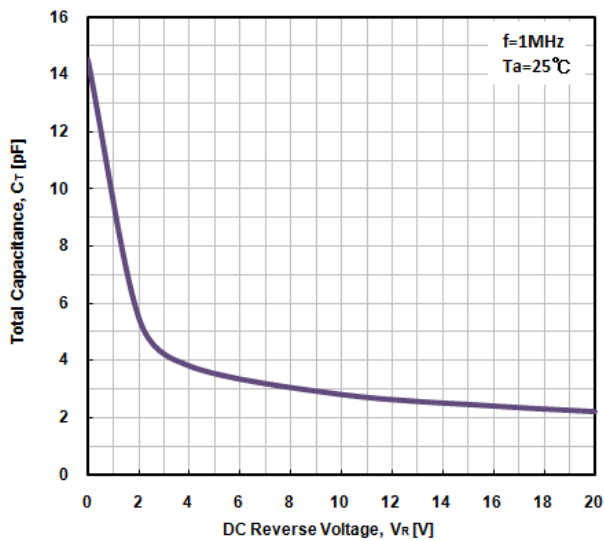


Fig. 4) Power dissipation vs. Ambient temperature

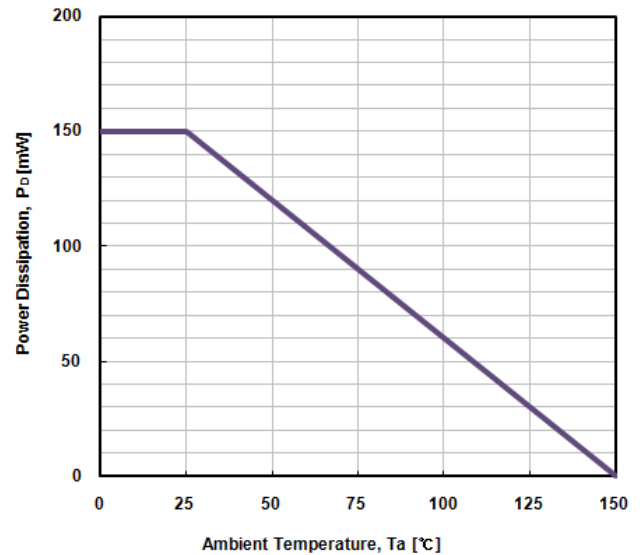
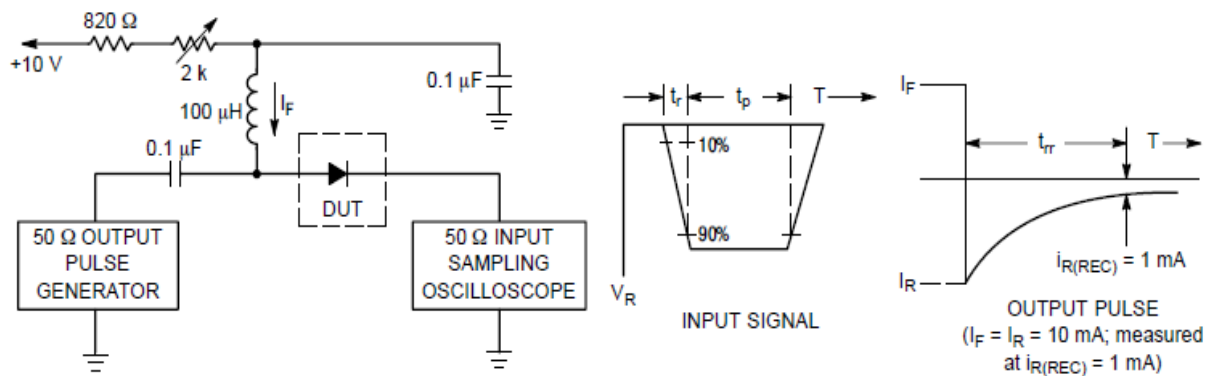
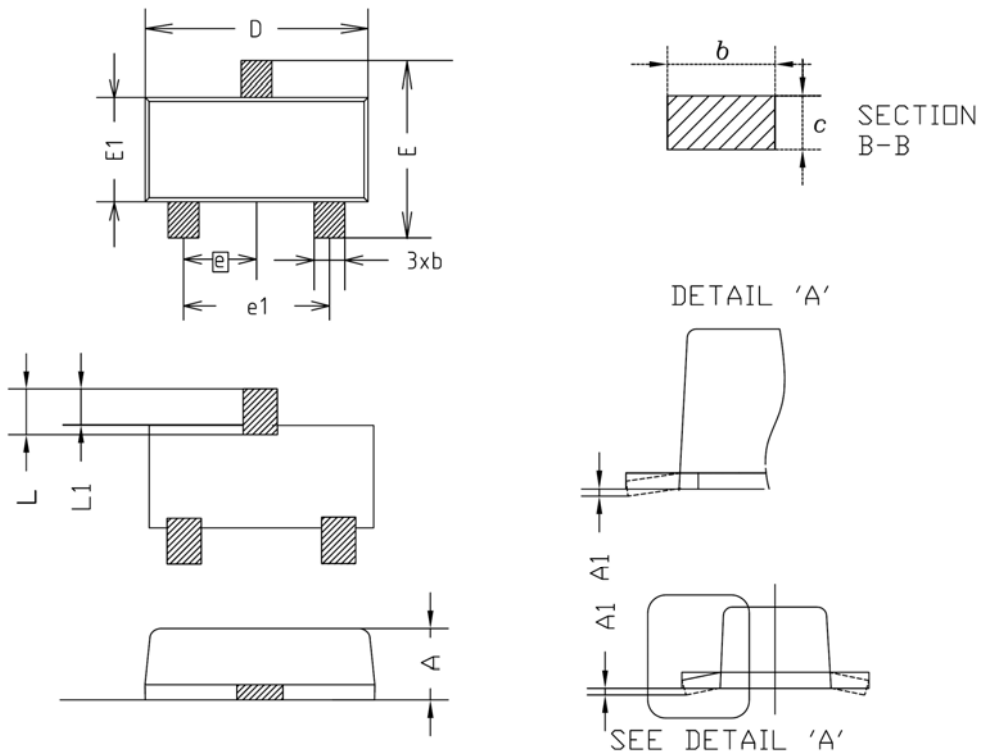


Fig. 5) Reverse recovery time equivalent test circuit

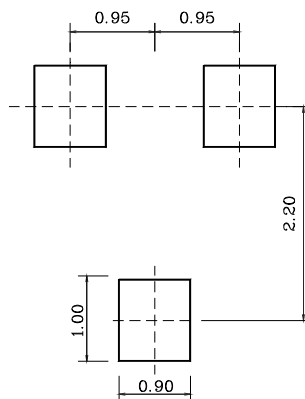


## Package Outline Dimensions



SYMBOL	MILLIMETER(mm)			NOTE
	MINIMUM	NOMINAL	MAXIMUM	
A	0.80	0.90	1.00	
A1	0.00	-	0.10	
b	0.35	0.40	0.45	
c	0.10	0.15	0.20	
D	2.80	2.90	3.00	
E	2.30	2.40	2.50	
E1	1.50	1.60	1.70	
e	0.95BSC			
e1	1.80	1.90	2.00	
L	0.48	0.58	0.68	
L1	0.30	-	0.50	

※ Recommend PCB solder land (Unit : mm)



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