

# **SBT2907F**

**PNP Silicon Transistor** 

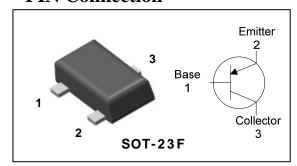
### **Descriptions**

- General purpose application
- Switching application

#### **Features**

- Low Leakage current
- Low collector saturation voltage enabling low voltage operation
- Complementary pair with SBT2222F

#### **PIN Connection**



### **Ordering Information**

Type NO.	Marking	Package Code
SBT2907F	<u>2B</u> □ ① ②	SOT-23F

1) Device Code 2) Year & Week Code

**Absolute maximum ratings** 

Ta=25°C

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	-60	V
Collector-Emitter voltage	$V_{CEO}$	-40	V
Emitter-base voltage	$V_{EBO}$	-5	V
Collector current	I <sub>C</sub>	-600	m A
Collector dissipation	P <sub>C</sub> <sup>*</sup>	350	m W
Junction temperature	Tj	150	°C
Storage temperature range	$T_{stg}$	-55~ 150	°C

<sup>\* :</sup> Package mounted on 99.5% alumina 10×8×0.6mm

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# **SBT2907F**

## **Electrical Characteristics**

Ta=25°C

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	I <sub>C</sub> = -10μA, I <sub>E</sub> = 0	-60	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	I <sub>C</sub> = -1 m A, I <sub>B</sub> = 0	-40	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	I <sub>E</sub> = -10μA, I <sub>C</sub> = 0	-5	-	-	V
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = -40 V, I <sub>E</sub> = 0	-	-	-20	nA
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> = -10V, I <sub>C</sub> = -10mA	100	-	-	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = -150mA, I <sub>B</sub> = -15mA	-	-	-0.4	V
Transition frequency	f <sub>T</sub>	$V_{CE}$ = -5.0V, $I_{C}$ = -20mA, $f$ = 100MHz	200	-	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB}$ = -10V, $I_{E}$ = 0, $f$ = 1MHz	-	ı	8	pF
Turn-on time	t <sub>on</sub>		-	1	45	ns
Delay time	t <sub>d</sub>	$V_{CC}$ = -30 $V_{dc}$ , $I_{C}$ = -150m $A_{dc}$ , $I_{B1}$ = -15m $A_{dc}$	-	-	10	ns
Rise time	t <sub>r</sub>		-	-	40	ns
Turn-off time	t <sub>off</sub>		-	-	100	ns
Storage time	t <sub>s</sub>	$V_{CC}$ = -6.0 $V_{dc}$ , $I_{C}$ = -150m $A_{dc}$ , $I_{B1}$ = $I_{B2}$ = -15m $A_{dc}$	-	-	80	ns
Fall time	t <sub>f</sub>		-	-	30	ns

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### **Electrical Characteristic Curves**

Fig. 1 P<sub>C</sub>-T<sub>a</sub>

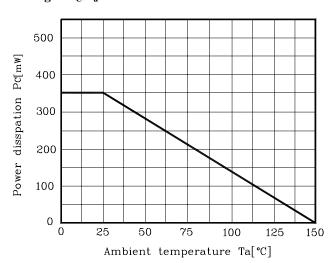


Fig. 2  $h_{FE}I_{C}$ 

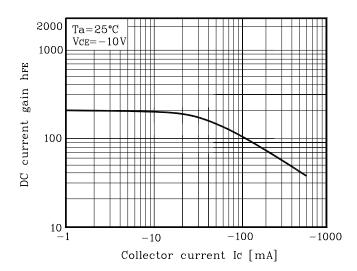


Fig. 3  $V_{\text{CE(sat)}}\text{-}I_{\text{C}}$ 

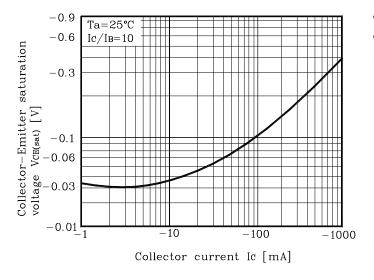
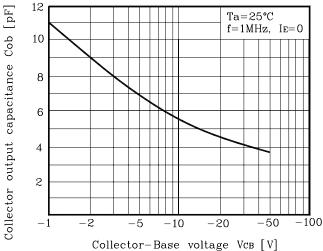


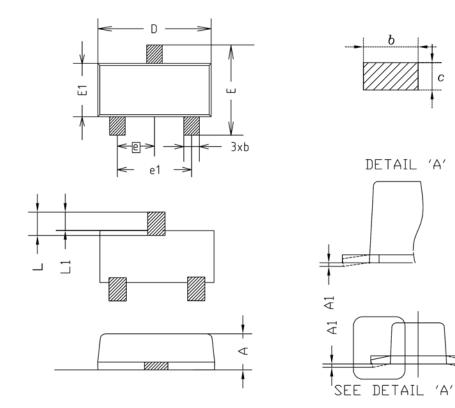
Fig. 4  $C_{ob}$ - $V_{CB}$ 



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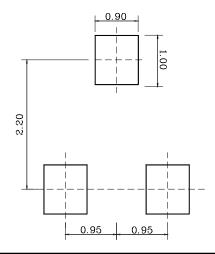
SECTION B-B

## **Outline Dimension**



SYMBOL	1	NOTE		
STINDUC	MINIMUM	NDMINAL	MAXIMUM	NUIE
Α	0.80	0.90	1.00	
A1	0.00	-	0.10	
b	0.35	0.40	0.45	
C	0.10	0.15	0.20	
D	2.80	2.90	3.00	
Ε	2.30	2.40	2.50	
E1	1.50	1.60	1.70	
е	0.95BSC			
e1	1.80	1.90	2.00	
L	0.48	0.58	0.68	
L1	0.30	-	0.50	

### \*Recommend PCB solder land [Unit: mm]



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