

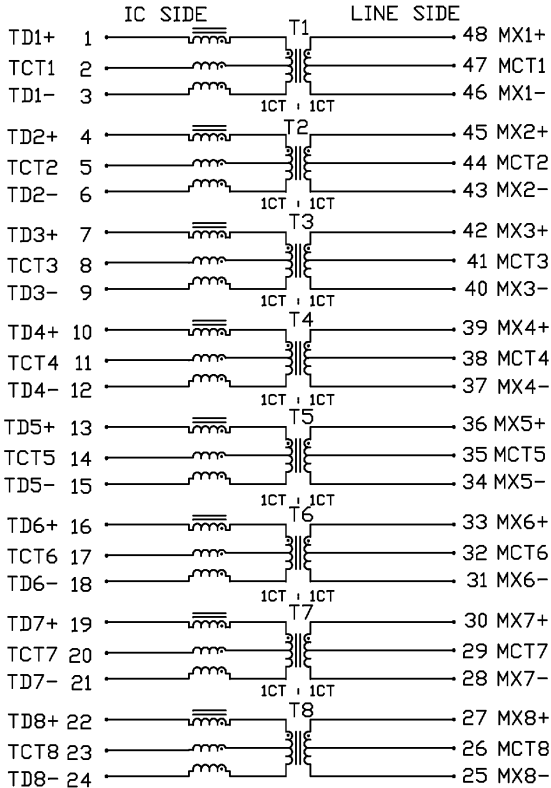
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SCHMATIC

ELECTRICAL SPECIFICATIONS @25°C

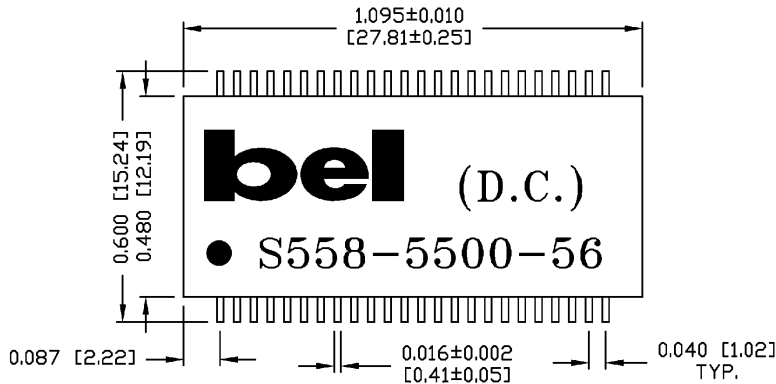


<p>URNS RATIO IC : LINE</p> <p>POLARITY</p> <p>QCL @100kHz, 100mVrms 8mA DC BIAS</p> <p>INSERTION LOSS 0.3MHz TO 100MHz 100MHz TO 125MHz</p> <p>RETURN LOSS 0.5MHz-40MHz 40MHz-100MHz</p> <p>DC RESISTANCE DCR (START-FINISH) DCR 1/2 WDG DCR IMBALANCE</p> <p>CROSSSTALK 300kHz - 100MHz</p> <p>CM TO CM REJ 300kHz - 100MHz</p> <p>CM TO DM REJ 300kHz - 100MHz</p> <p>HIPOT</p> <p>OPERATING TEMPERATURE</p>	<p>1CT : 1CT</p> <p>PER DOT CONVENTION</p> <p>350µH MIN.</p> <p>0.8dB MAX 1.2dB MAX</p> <p>18dB MIN 12-20 LOG (F/80MHz) dB MIN</p> <p>1.15OHMS TYP 1.0OHMS MAX ±0.065OHMS MAX</p> <p>33dB MIN</p> <p>30dB MIN</p> <p>35dB MIN</p> <p>1500Vrms</p> <p>0°C TO +70°C</p>
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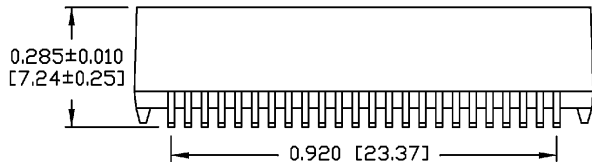
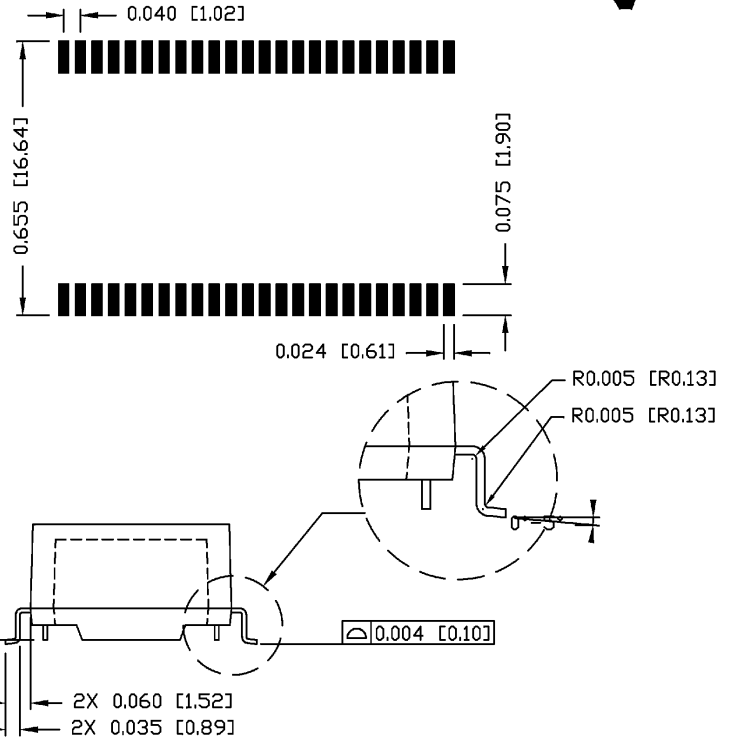
ORIGINATED BY CHAN CH	DATE 05-13-08	TITLE ELECTRICAL SPECIFICATION	PART NO. / DRAWING NO. S558-5500-56	STANDARD DIM. TOL. IN INCH .X .XX .XXX	[] METRIC DIM. AS REFERENCE UNIT : INCH [mm] SCALE : N/A	REV. : A SIZE : A4 PAGE : 2	<p>COMPONENTS OF A CONNECTED PLANET</p>
DRAWN BY YH Lai	DATE 05-13-08		FILE NAME S558550056A.DWG				

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SUGGESTED PCB PAD LAYOUT



NOTES:

- STANDARD MARKING REFER TO DOC. HAND-WORK-04.
- PACKAGE CODE: "QV5001".

ORIGINATED BY LAWRENCE TSANG	DATE 05-13-08	TITLE MECHANICAL OUTLINE	PART NO. / DRAWING NO. S558-5500-56	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REFERENCE	 COMPONENTS OF A CONNECTED PLANET	
DRAWN BY YH Lai	DATE 05-13-08		FILE NAME S558550056A.DWG	.X	UNIT : INCH [mm]		REV. : A
				.XX	SCALE : N/A		SIZE : A4
				.XXX	±0.005	PAGE : 3	