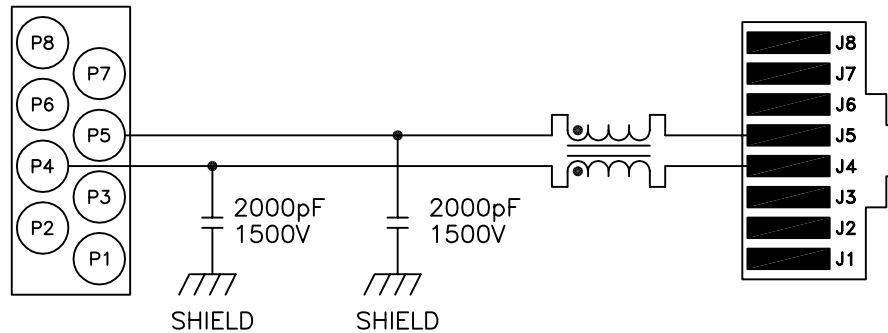




NOTE:

1.0 PINS WITHOUT ELECTRICAL CONNECTIONS ARE OMITTED.

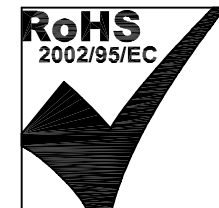
ELECTRICAL SPECIFICATIONS:

1.0 INDUCTANCE: P4-J4=P5-J5 : 4mH NOMINAL 0.01V, 10KHz

2.0 DC RESISTANCE: P4-J4=P5-J5 : .5 OHM MAX.

3.0 COMMON MODE ATTENUATION:	FREQUENCY (MHz)	TYPICAL (dB)
	1	50
	5	55
	20	65
	70	40
	200	35
	500	20

4.0 CURRENT RATING: 280mA CONTINUOUS, 5A FUSING



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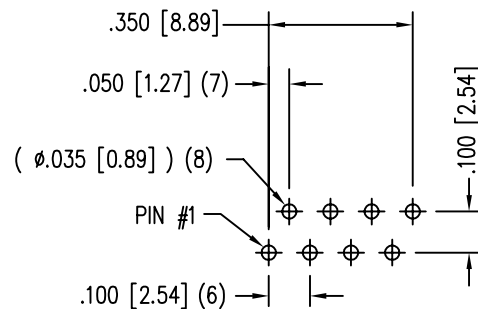
<http://www.stewartconnector.com>

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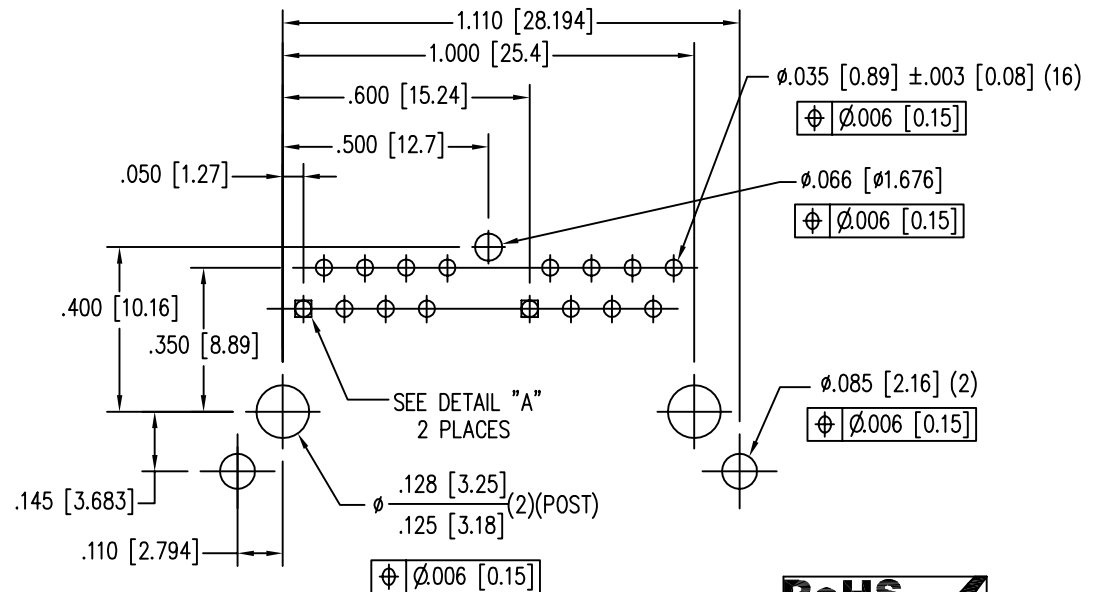
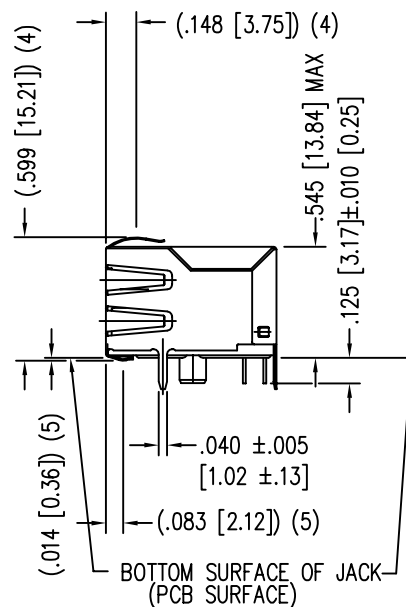
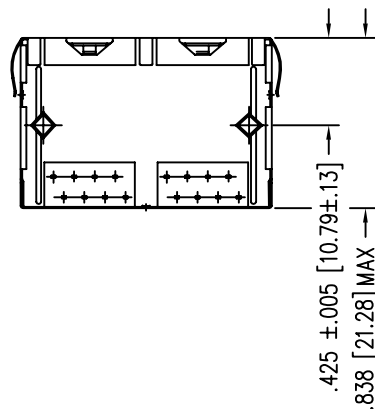
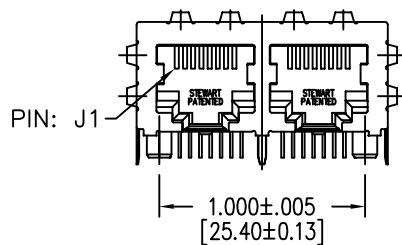
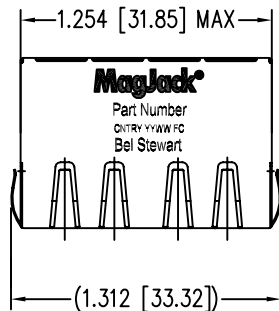
SHEET
1 OF 3

DRAWING NO.
SI-60068-F

REV.
09

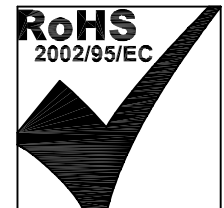


DETAIL "A"
TYPICAL HOLE LAYOUT
2 PLACES



P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE

ALL CENTERLINE DIMENSIONS ARE BASIC.



NOTES:

1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,
50 MICRO-INCHES MIN. IN CONTACT AREA.
2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]
5. WAVE SOLDER COMPATIBLE - PREHEAT 125°C/90SECS.

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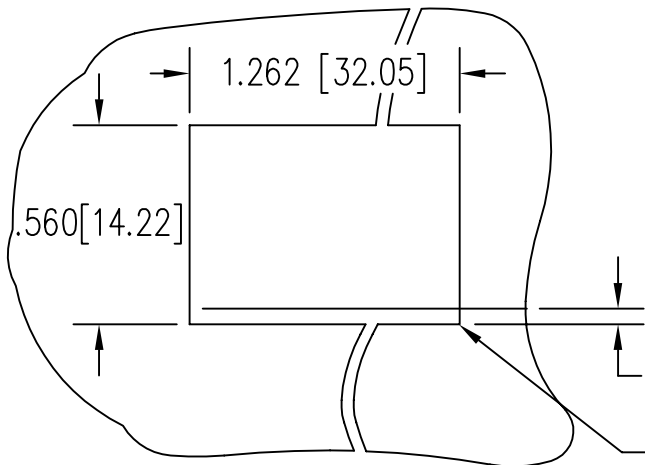
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SHEET
2 OF 3

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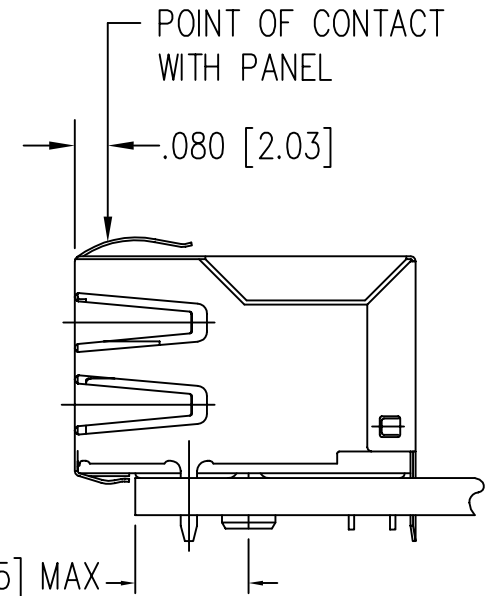
REV.
07



SUGGESTED PANEL OPENING

.000 (TOP OF PCB TO BOTTOM OF OPENING)

.010 [.254] MAX. RADIUS(4)



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]

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SHEET

3 OF 3

DRAWING NO.

SI-60068-F

REV.

07