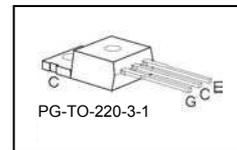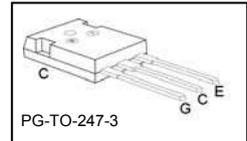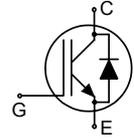# IKP20N60T
IKW20N60T

TrenchStop® Series

Low Loss DuoPack : IGBT in **TrenchStop®** and Fieldstop technology with soft, fast recovery anti-parallel EmCon HE diode

- Very low $V_{CE(sat)}$ 1.5 V (typ.)
- Maximum Junction Temperature 175 °C
- Short circuit withstand time – 5μs
- Designed for :
    - Frequency Converters
    - Uninterrupted Power Supply
- **TrenchStop®** and Fieldstop technology for 600 V applications offers :
    - very tight parameter distribution
    - high ruggedness, temperature stable behavior
    - very high switching speed
    - low $V_{CE(sat)}$
- Positive temperature coefficient in $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Very soft, fast recovery anti-parallel EmCon HE diode
- Qualified according to JEDEC[1] for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : http://www.infineon.com/igbt/

PG-TO-247-3

PG-TO-220-3-1

Type	V_{CE}	I_C	$V_{CE(sat),Tj=25°C}$	$T_{j,max}$	Marking	Package
IKP20N60T	600V	20A	1.5V	175°C	K20T60	PG-TO-220-3-1
IKW20N60T	600V	20A	1.5V	175°C	K20T60	PG-TO-247-3

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CE}	600	V
DC collector current, limited by T_{jmax}	I_C		A
$T_C = 25°C$		40	
$T_C = 100°C$		20	
Pulsed collector current, t_p limited by T_{jmax}	I_{Cpuls}	60	
Turn off safe operating area ($V_{CE} \leq 600V$, $T_j \leq 175°C$)	-	60	
Diode forward current, limited by T_{jmax} $T_C = 25°C$	I_F	40	
$T_C = 100°C$		20	
Diode pulsed current, t_p limited by T_{jmax}	I_{Fpuls}	60	
Gate-emitter voltage	V_{GE}	±20	V
Short circuit withstand time[2] $V_{GE} = 15V$, $V_{CC} \leq 400V$, $T_j \leq 150°C$	t_{SC}	5	μs
Power dissipation $T_C = 25°C$	P_{tot}	166	W
Operating junction temperature	T_j	-40...+175	°C
Storage temperature	T_{stg}	-55...+175	
Soldering temperature, 1.6mm (0.063 in.) from case for 10s	-	260	

[1] J-STD-020 and JESD-022
[2] Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		0.9	K/W
Diode thermal resistance, junction – case	R_{thJCD}		1.5	
Thermal resistance, junction – ambient	R_{thJA}		62 40	

Electrical Characteristic, at T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value min.	Value Typ.	Value max.	Unit
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	V_{GE}=0V, I_C=0.2mA	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	V_{GE} = 15V, I_C=20A				
		T_j=25°C	-	1.5	2.05	
		T_j=175°C	-	1.9	-	
Diode forward voltage	V_F	V_{GE}=0V, I_F=20A				
		T_j=25°C	-	1.65	2.05	
		T_j=175°C	-	1.6	-	
Gate-emitter threshold voltage	$V_{GE(th)}$	I_C=290µA, V_{CE}=V_{GE}	4.1	4.9	5.7	
Zero gate voltage collector current	I_{CES}	V_{CE}=600V, V_{GE}=0V				µA
		T_j=25°C	-	-	40	
		T_j=175°C	-	-	1000	
Gate-emitter leakage current	I_{GES}	V_{CE}=0V, V_{GE}=20V	-	-	100	nA
Transconductance	g_{fs}	V_{CE}=20V, I_C=20A	-	11	-	S
Integrated gate resistor	R_{Gint}			-		Ω

Dynamic Characteristic

Parameter	Symbol	Conditions	Value min.	Value Typ.	Value max.	Unit
Input capacitance	C_{iss}	V_{CE}=25V,	-	1100	-	pF
Output capacitance	C_{oss}	V_{GE}=0V,	-	71	-	
Reverse transfer capacitance	C_{rss}	f=1MHz	-	32	-	
Gate charge	Q_{Gate}	V_{CC}=480V, I_C=20A V_{GE}=15V	-	120	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E	TO-247-3-21	-	13	-	nH
		TO-220-3-1		7		
Short circuit collector current[1]	$I_{C(SC)}$	V_{GE}=15V, t_{SC}≤5µs V_{CC} = 400V, T_j ≤ 150°C	-	183.3	-	A

[1] Allowed number of short circuits: <1000; time between short circuits: >1s.
[2] Leakage inductance L_σ and Stray capacity C_σ due to dynamic test circuit in Figure E.

Switching Characteristic, Inductive Load, at T_j=25 °C

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T_j=25°C, V_{CC}=400V, I_C=20A, V_{GE}=0/15V, R_G=12 Ω, $L_\sigma^{2)}$=131nH, $C_\sigma^{2)}$=31pF Energy losses include "tail" and diode reverse recovery.	-	18	-	ns
Rise time	t_r		-	14	-	
Turn-off delay time	$t_{d(off)}$		-	199	-	
Fall time	t_f		-	42	-	
Turn-on energy	E_{on}		-	0.31	-	mJ
Turn-off energy	E_{off}		-	0.46	-	
Total switching energy	E_{ts}		-	0.77	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	T_j=25°C, V_R=400V, I_F=20A, di_F/dt=880A/µs	-	41	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.31	-	µC
Diode peak reverse recovery current	I_{rrm}		-	13.3	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	711	-	A/µs

Switching Characteristic, Inductive Load, at T_j=175 °C

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T_j=175°C, V_{CC}=400V, I_C=20A, V_{GE}=0/15V, R_G= 12 Ω $L_\sigma^{1)}$=131nH, $C_\sigma^{1)}$=31pF Energy losses include "tail" and diode reverse recovery.	-	18	-	ns
Rise time	t_r		-	18	-	
Turn-off delay time	$t_{d(off)}$		-	223	-	
Fall time	t_f		-	76	-	
Turn-on energy	E_{on}		-	0.51	-	mJ
Turn-off energy	E_{off}		-	0.64	-	
Total switching energy	E_{ts}		-	1.15	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	T_j=175°C V_R=400V, I_F=20A, di_F/dt=880A/µs	-	176	-	ns
Diode reverse recovery charge	Q_{rr}		-	1.46	-	µC
Diode peak reverse recovery current	I_{rrm}		-	18.9	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	467	-	A/µs

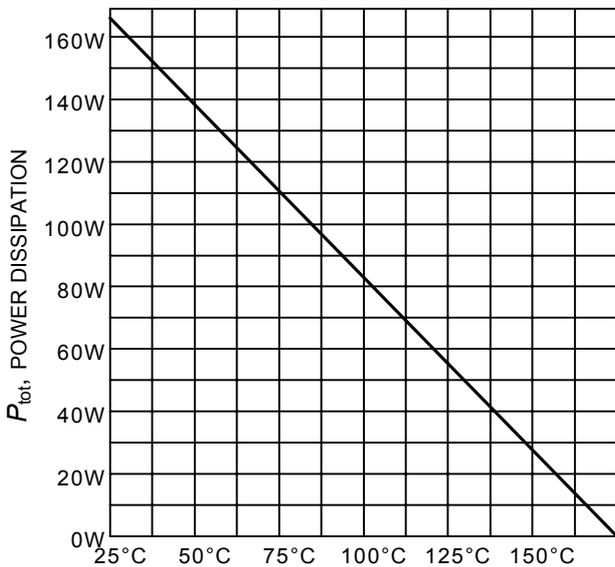[1] Leakage inductance L_σ and Stray capacity C_σ due to dynamic test circuit in Figure E.
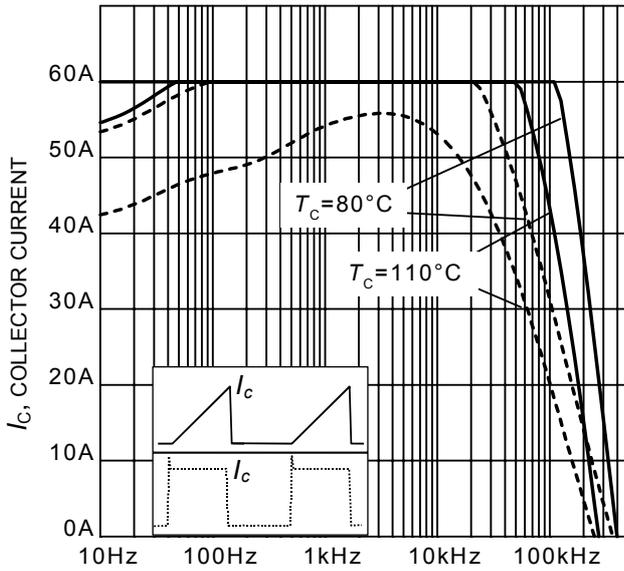
Figure 1. Collector current as a function of switching frequency
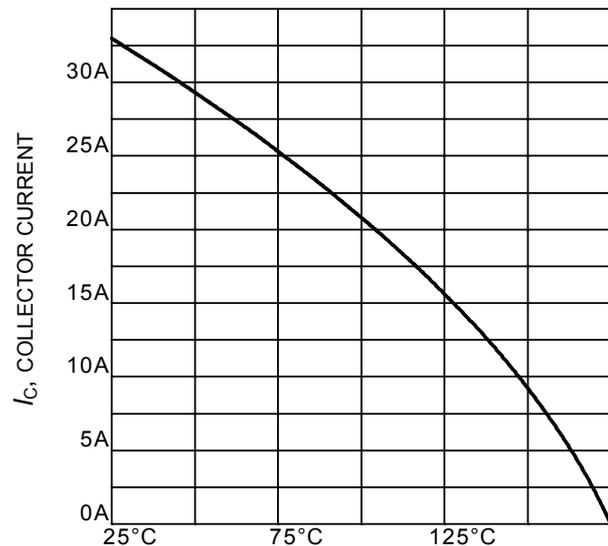($T_j \le 175°C$, $D = 0.5$, $V_{CE} = 400V$, $V_{GE} = 0/+15V$, $R_G = 12\Omega$)

f, SWITCHING FREQUENCY

I_C, COLLECTOR CURRENT

Figure 2. Safe operating area
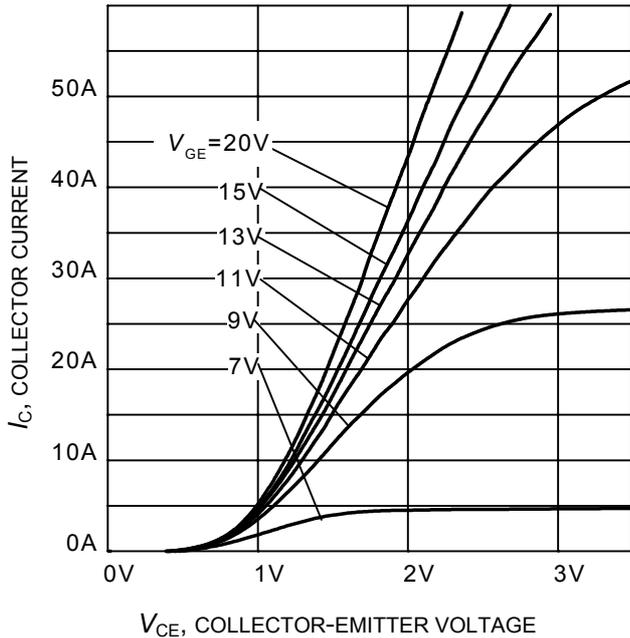($D = 0$, $T_C = 25°C$, $T_j \le 175°C$; $V_{GE}=15V$)
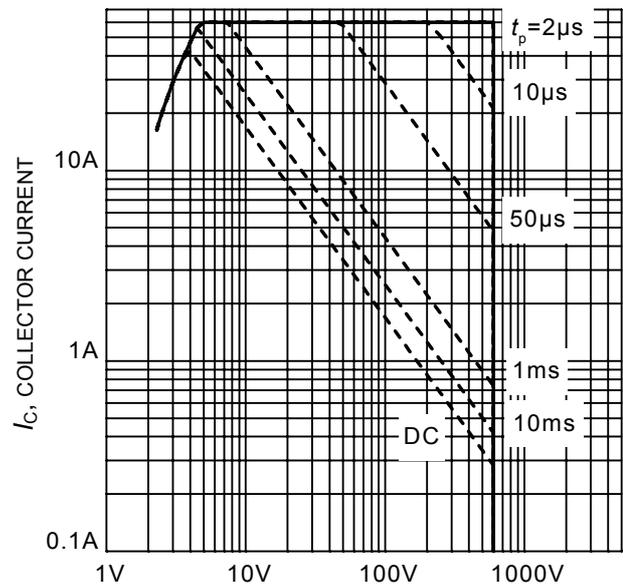
V_{CE}, COLLECTOR-EMITTER VOLTAGE

I_C, COLLECTOR CURRENT

Figure 3. Power dissipation as a function of case temperature
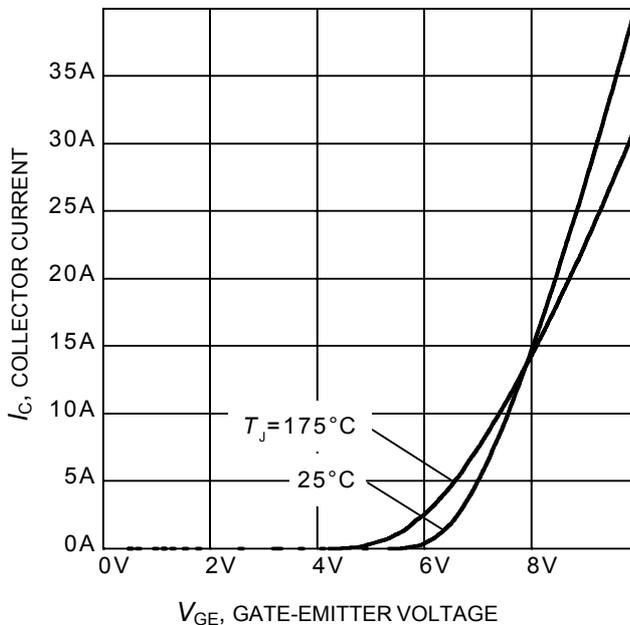($T_j \le 175°C$)

T_C, CASE TEMPERATURE

P_{tot}, POWER DISSIPATION

Figure 4. Collector current as a function of case temperature
($V_{GE} \ge 15V$, $T_j \le 175°C$)

T_C, CASE TEMPERATURE

I_C, COLLECTOR CURRENT

Figure 5. Typical output characteristic
(T_j = 25°C)
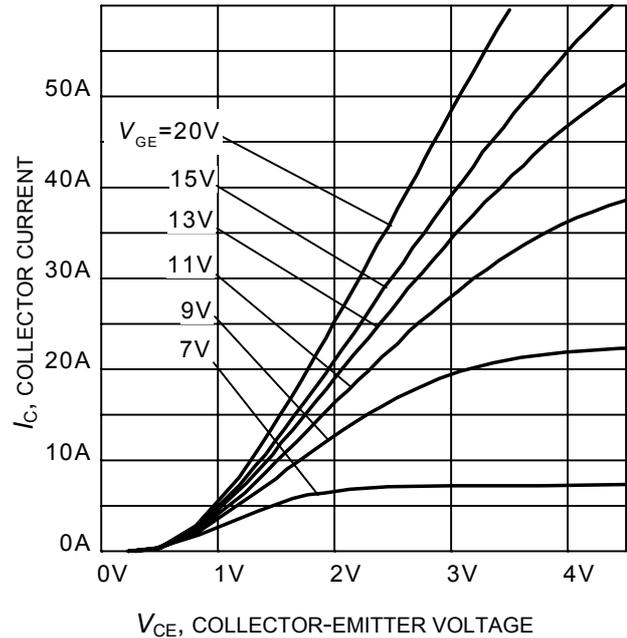
Figure 6. Typical output characteristic
(T_j = 175°C)

Figure 7. Typical transfer characteristic
(V_{CE}=10V)
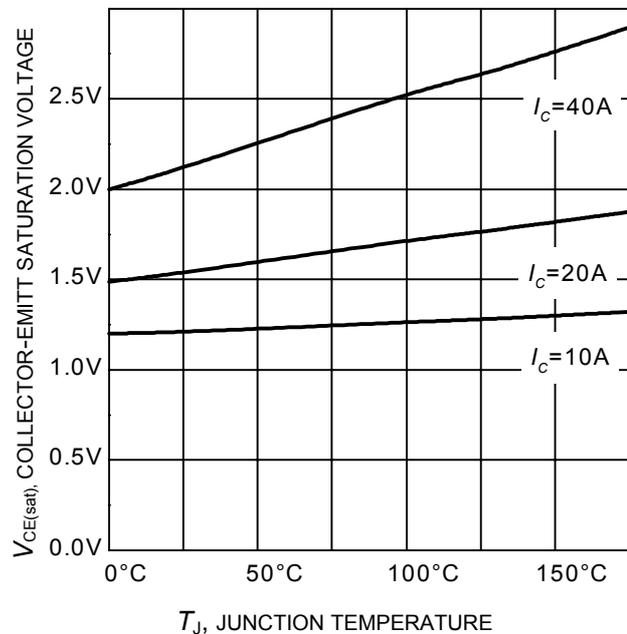
Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
(V_{GE} = 15V)
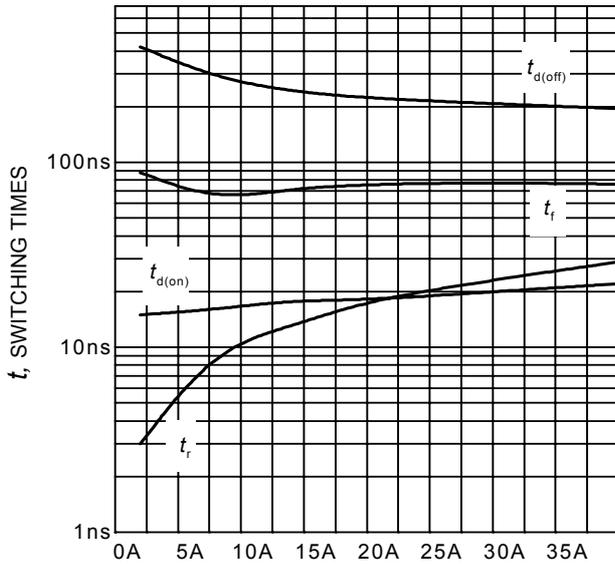
Figure 9. Typical switching times as a function of collector current
(inductive load, T_J=175°C, V_{CE} = 400V, V_{GE} = 0/15V, R_G = 12Ω, Dynamic test circuit in Figure E)

Figure 10. Typical switching times as a function of gate resistor
(inductive load, T_J = 175°C, V_{CE}= 400V, V_{GE} = 0/15V, I_C = 20A, Dynamic test circuit in Figure E)
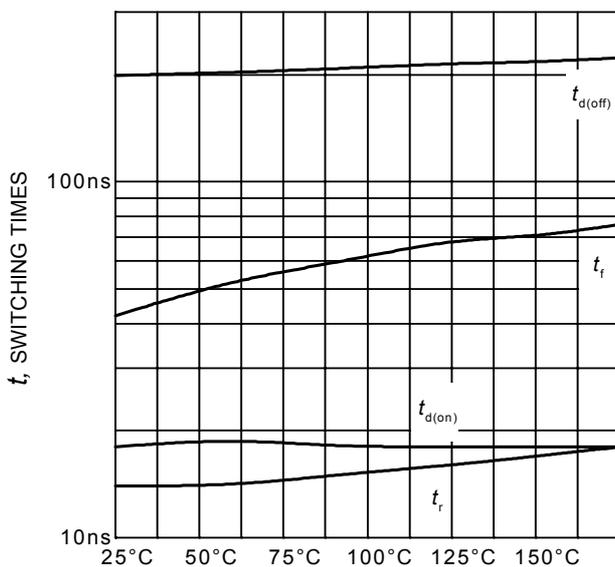
Figure 11. Typical switching times as a function of junction temperature
(inductive load, V_{CE} = 400V, V_{GE} = 0/15V, I_C = 20A, R_G=12Ω, Dynamic test circuit in Figure E)
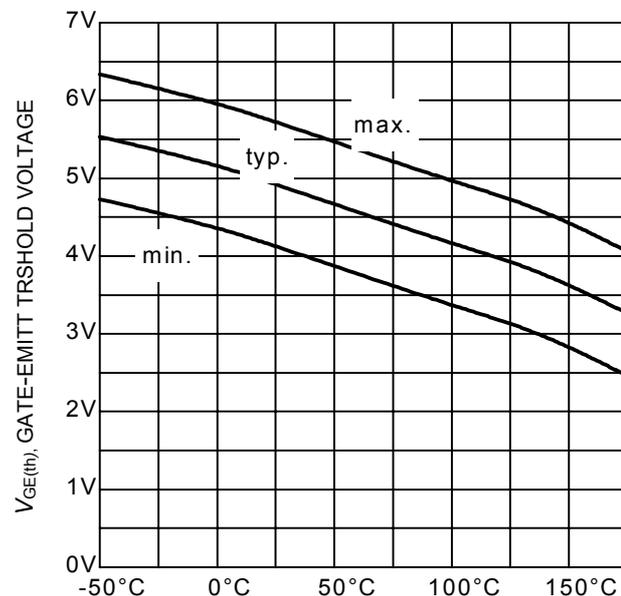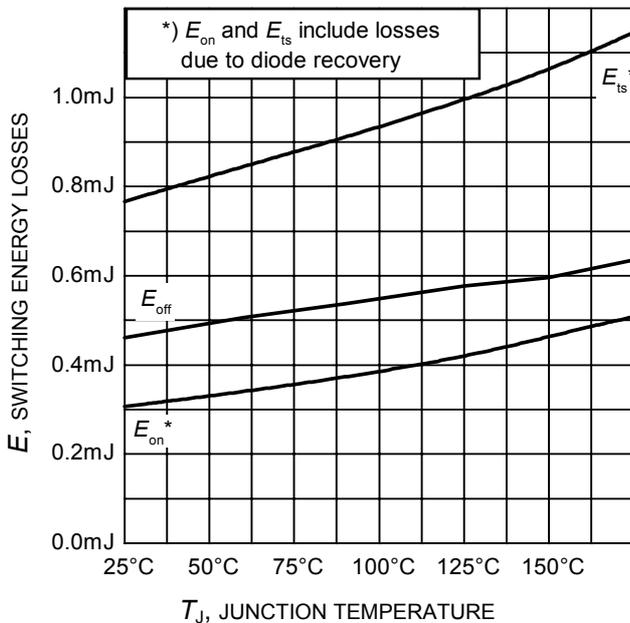

**Figure 12. Gate-emitter threshold voltage as a function of junction temperature**
(I_C = 0.29mA)
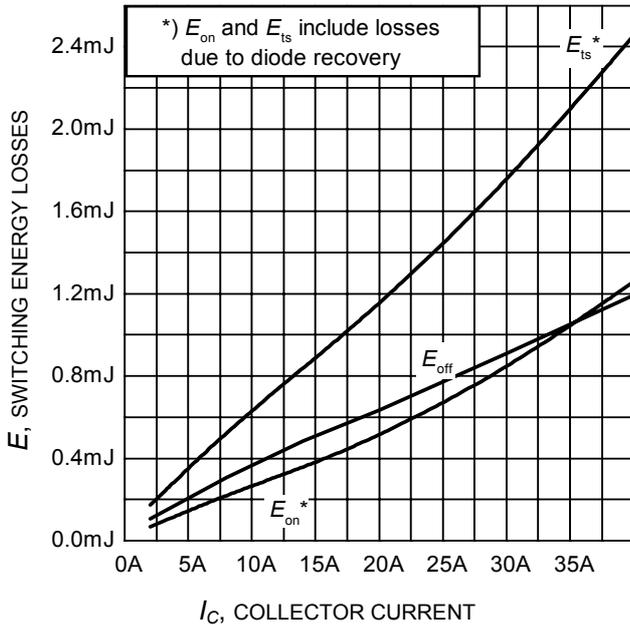
Figure 13. Typical switching energy losses as a function of collector current (inductive load, $T_J = 175°C$, $V_{CE} = 400V$, $V_{GE} = 0/15V$, $R_G = 12\Omega$, Dynamic test circuit in Figure E)
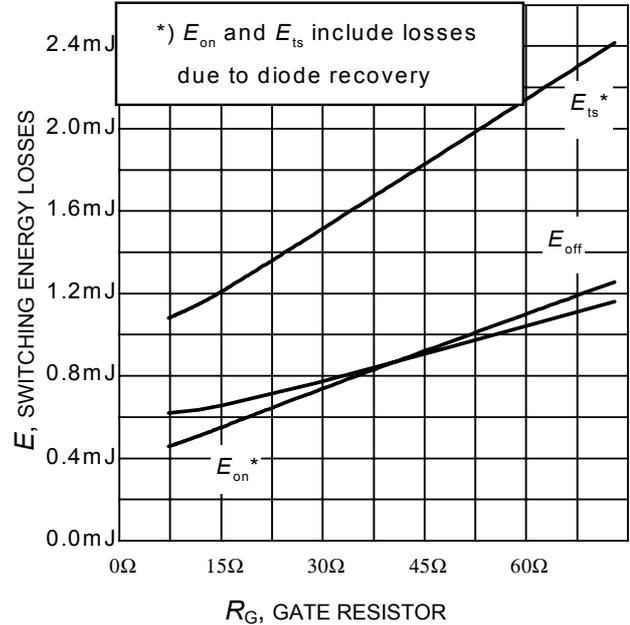

Figure 14. Typical switching energy losses as a function of gate resistor (inductive load, $T_J = 175°C$, $V_{CE} = 400V$, $V_{GE} = 0/15V$, $I_C = 20A$, Dynamic test circuit in Figure E)
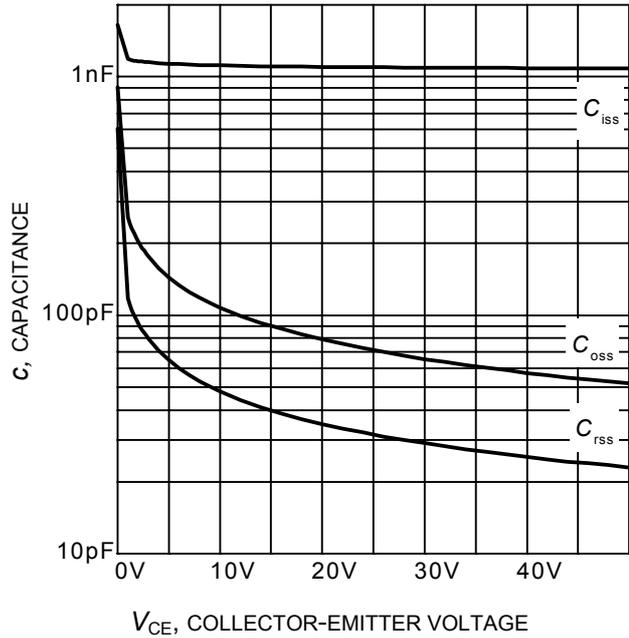

Figure 15. Typical switching energy losses as a function of junction temperature (inductive load, $V_{CE} = 400V$, $V_{GE} = 0/15V$, $I_C = 20A$, $R_G = 12\Omega$, Dynamic test circuit in Figure E)
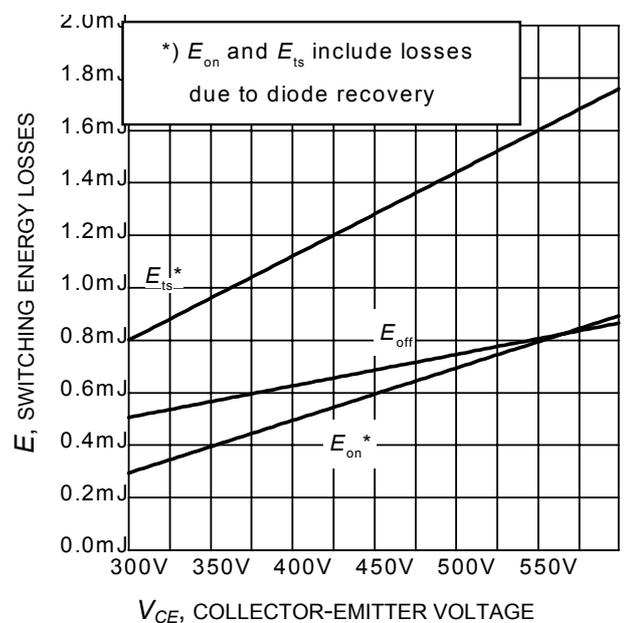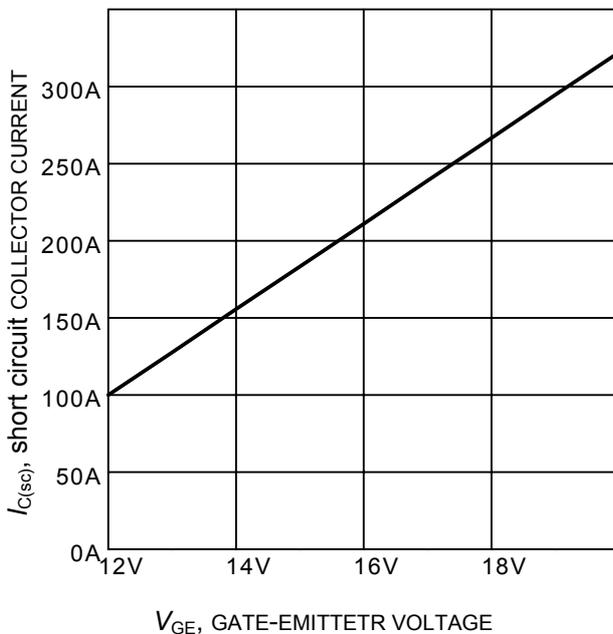

Figure 16. Typical switching energy losses as a function of collector emitter voltage (inductive load, $T_J = 175°C$, $V_{GE} = 0/15V$, $I_C = 20A$, $R_G = 12\Omega$, Dynamic test circuit in Figure E)
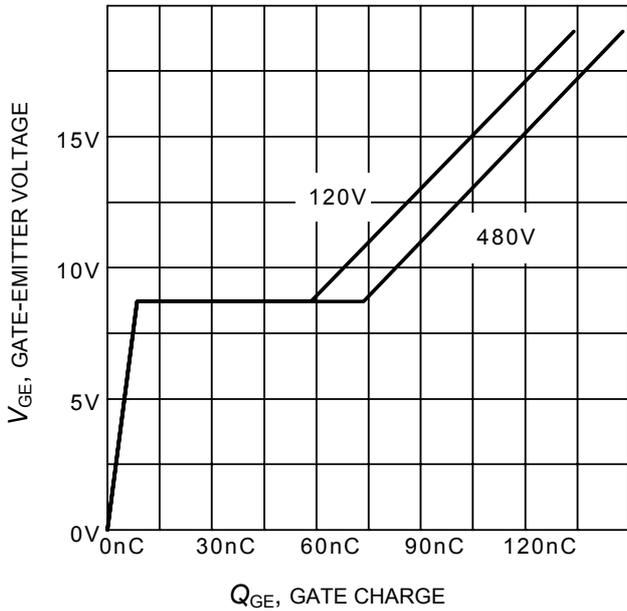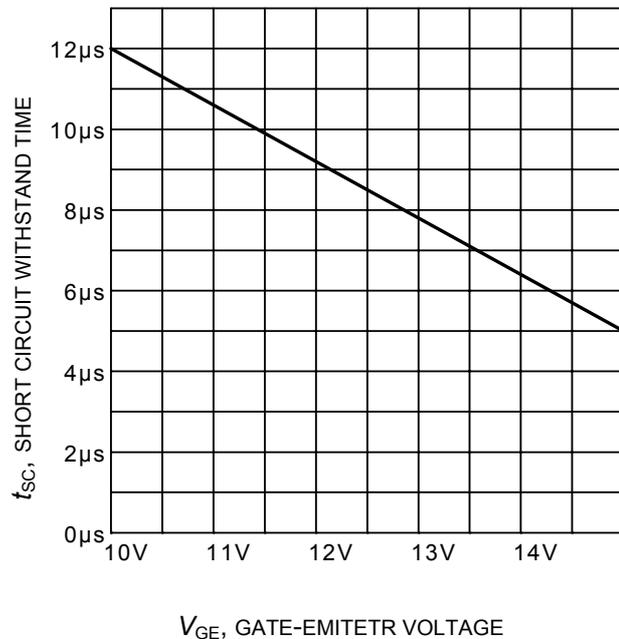
Figure 17. Typical gate charge
(I_C=20 A)

Figure 18. Typical capacitance as a function of collector-emitter voltage
(V_{GE}=0V, f = 1 MHz)

Figure 19. Typical short circuit collector current as a function of gate-emitter voltage
(V_{CE} ≤ 400V, T_j ≤ 150°C)

Figure 20. Short circuit withstand time as a function of gate-emitter voltage
(V_{CE}=600V, start at T_J=25°C, T_{Jmax}<150°C)
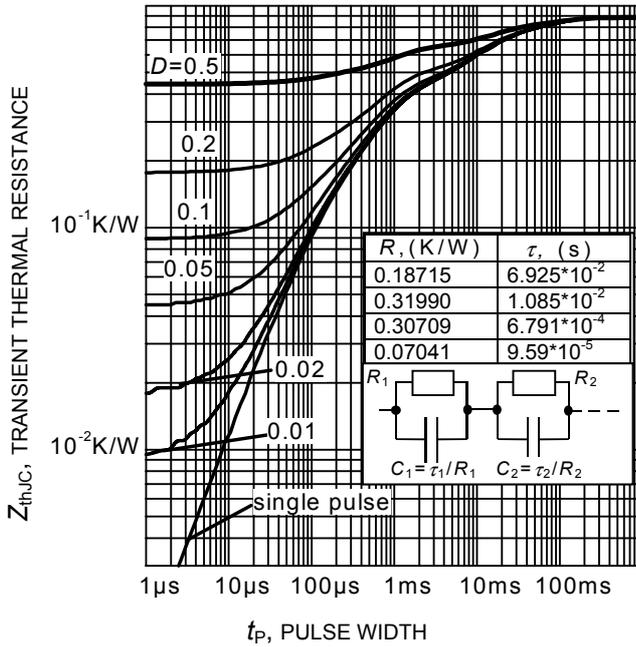
Figure 21. IGBT transient thermal resistance
($D = t_p / T$)

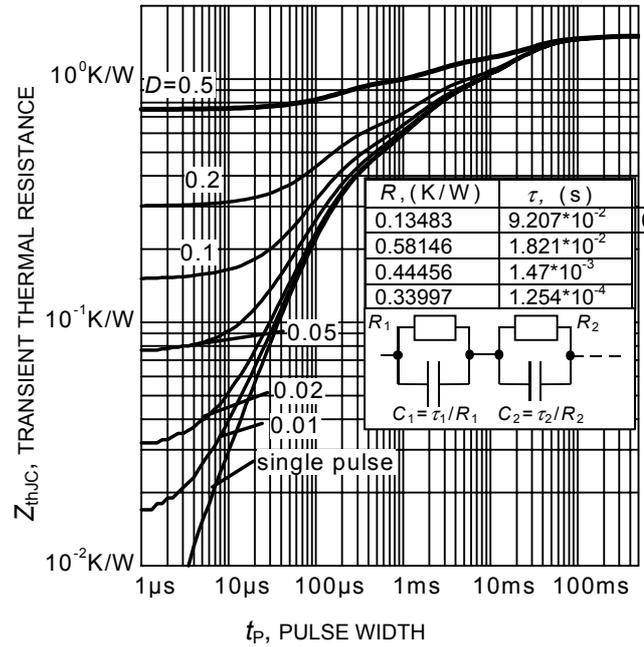| R, (K/W)	τ, (s)
0.18715	6.925×10^{-2}
0.31990	1.085×10^{-2}
0.30709	6.791×10^{-4}
0.07041	9.59×10^{-5}

Figure 22. Diode transient thermal impedance as a function of pulse width
($D = t_P / T$)

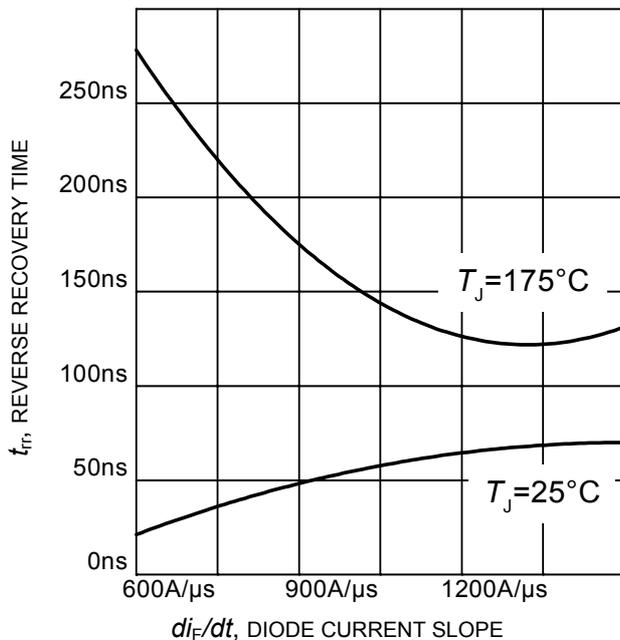| R, (K/W)	τ, (s)
0.13483	9.207×10^{-2}
0.58146	1.821×10^{-2}
0.44456	1.47×10^{-3}
0.33997	1.254×10^{-4}

Figure 23. Typical reverse recovery time as a function of diode current slope
(V_R=400V, I_F=20A, Dynamic test circuit in Figure E)

Figure 24. Typical reverse recovery charge as a function of diode current slope
(V_R = 400V, I_F = 20A, Dynamic test circuit in Figure E)

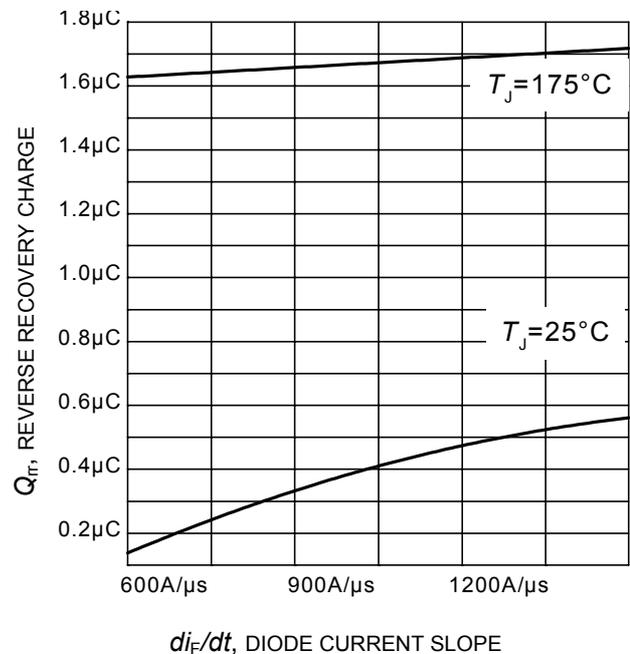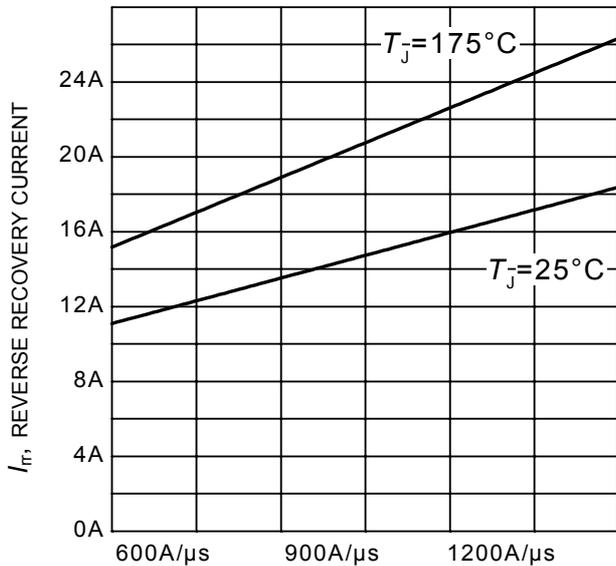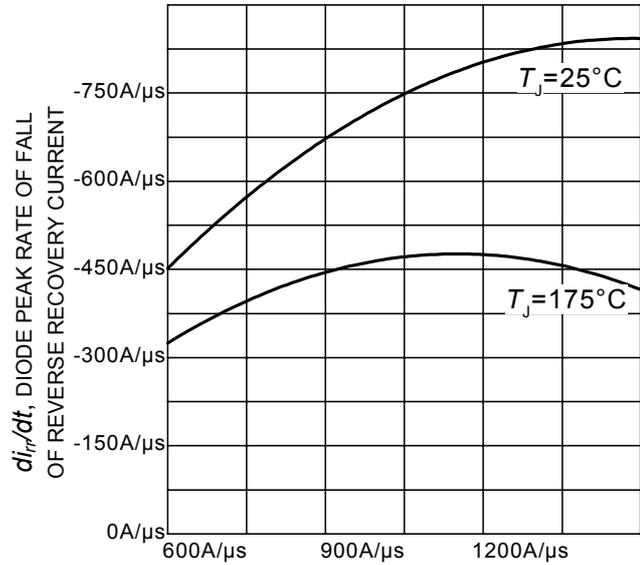**Figure 25. Typical reverse recovery current as a function of diode current slope**
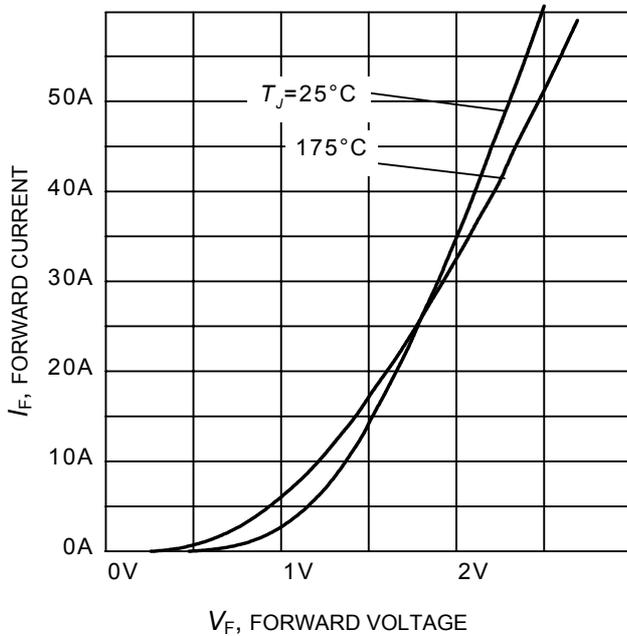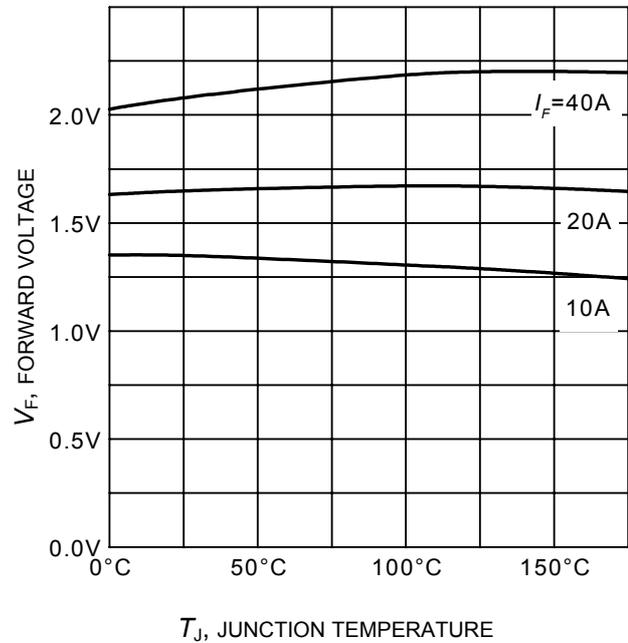($V_R = 400V$, $I_F = 20A$,
Dynamic test circuit in Figure E)

Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope
($V_R=400V$, $I_F=20A$,
Dynamic test circuit in Figure E)

Figure 27. Typical diode forward current as a function of forward voltage

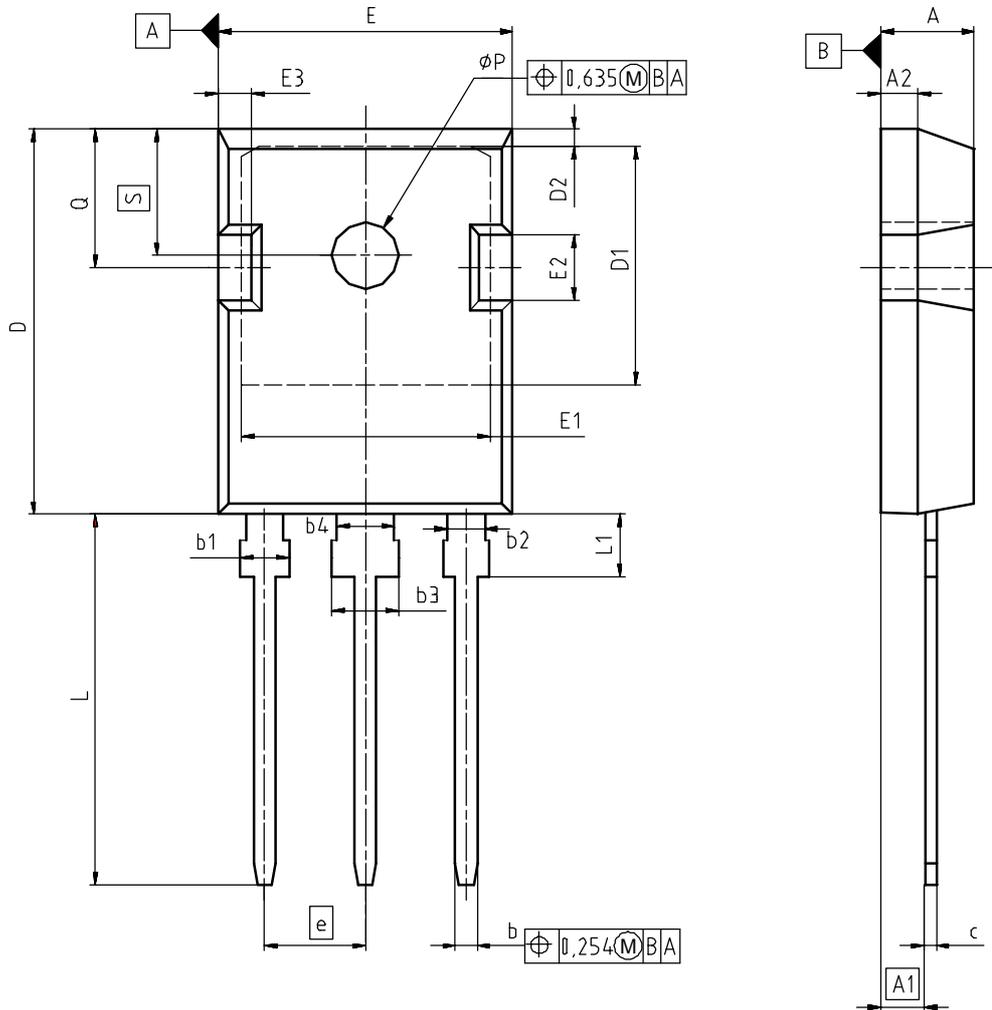**Figure 28. Typical diode forward voltage as a function of junction temperature**

PG-TO-220-3-1

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4,30	4,57	0,169	0,180
A1	1,17	1,40	0,046	0,055
A2	2,15	2,72	0,085	0,107
b	0,65	0,86	0,026	0,034
b1	0,95	1,40	0,037	0,055
b2	0,95	1,15	0,037	0,045
b3	0,65	1,15	0,026	0,045
c	0,33	0,60	0,013	0,024
D	14,81	15,95	0,583	0,628
D1	8,51	9,45	0,335	0,372
D2	12,19	13,10	0,480	0,516
E	9,70	10,36	0,382	0,408
E1	6,50	8,60	0,256	0,339
e	2,54		0,100	
e1	5,08		0,200	
N	3		3	
H1	5,90	6,90	0,232	0,272
L	13,00	14,00	0,512	0,551
L1	-	4,80	-	0,189
øP	3,60	3,89	0,142	0,153
Q	2,60	3,00	0,102	0,118

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23-08-2007

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05

PG-TO247-3

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.90	5.16	0.193	0.203
A1	2.27	2.53	0.089	0.099
A2	1.85	2.11	0.073	0.083
b	1.07	1.33	0.042	0.052
b1	1.90	2.41	0.075	0.095
b2	1.90	2.16	0.075	0.085
b3	2.87	3.38	0.113	0.133
b4	2.87	3.13	0.113	0.123
c	0.55	0.68	0.022	0.027
D	20.82	21.10	0.820	0.831
D1	16.25	17.65	0.640	0.695
D2	1.05	1.35	0.041	0.053
E	15.70	16.03	0.618	0.631
E1	13.10	14.15	0.516	0.557
E2	3.68	5.10	0.145	0.201
E3	1.68	2.60	0.066	0.102
e	5.44		0.214	
N	3		3	
L	19.80	20.31	0.780	0.799
L1	4.17	4.47	0.164	0.176
øP	3.50	3.70	0.138	0.146
Q	5.49	6.00	0.216	0.236
S	6.04	6.30	0.238	0.248

DOCUMENT NO.
Z8B00003327

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7.5mm

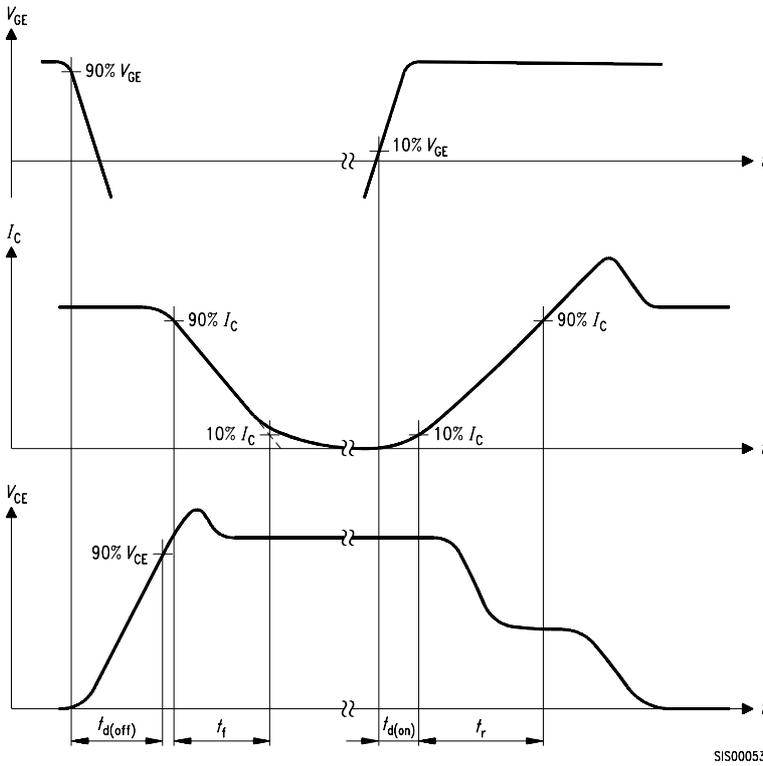EUROPEAN PROJECTION

ISSUE DATE
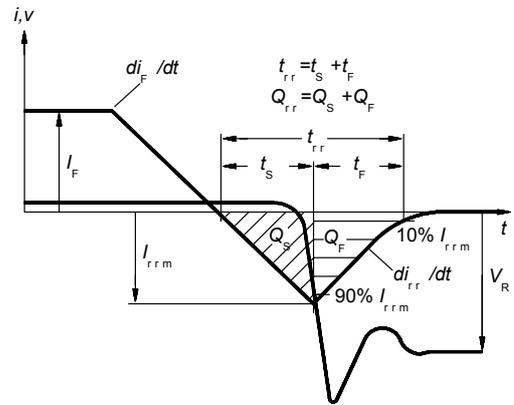17-12-2007

REVISION
03

Figure A. Definition of switching times
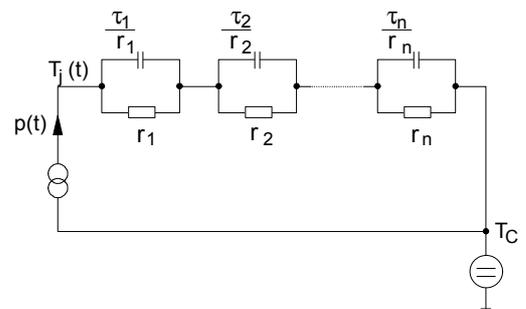

Figure C. Definition of diodes switching characteristics
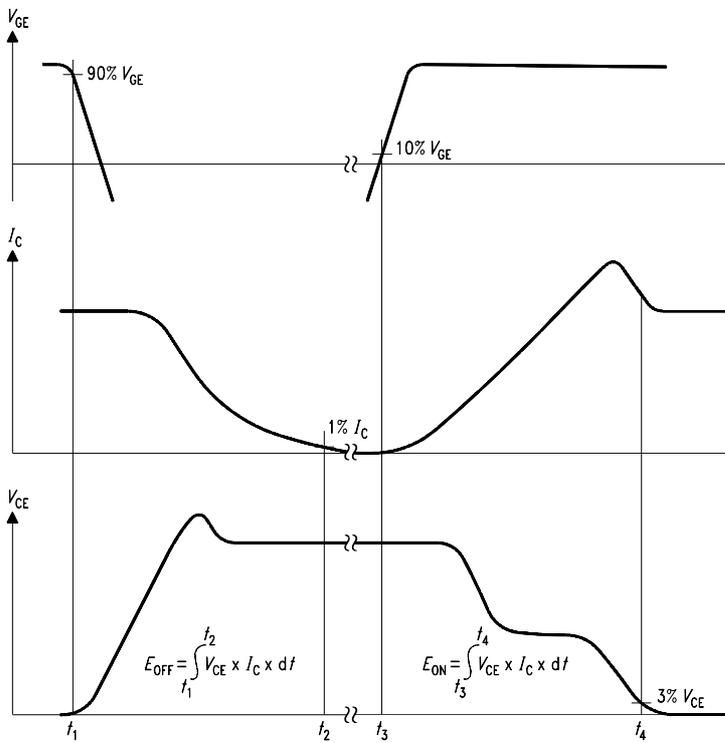

Figure D. Thermal equivalent circuit

$$E_{OFF} = \int_{t_1}^{t_2} V_{CE} \times I_C \times dt$$

$$E_{ON} = \int_{t_3}^{t_4} V_{CE} \times I_C \times dt$$

Figure B. Definition of switching losses
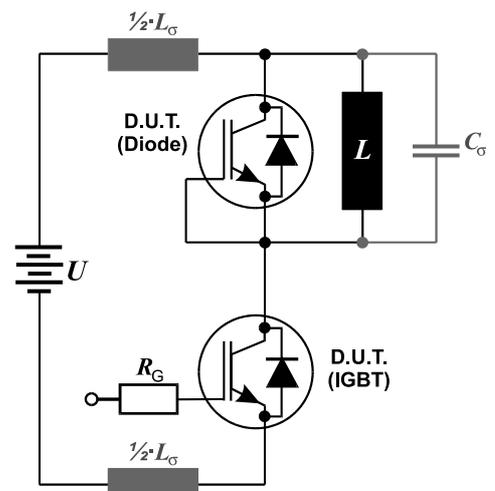
Figure E. Dynamic test circuit

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Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (www.infineon.com).

Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office. Infineon Technologies components may be used in life-support devices or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.