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RoHS  
2002/95/EC



ELECTRICAL CHARACTERISTICS @ 25° C

OPERATING TEMPERATURE RANGE: -40° C TO +85° C

URNS RATIO: (P4-P5-P6) : (J3-J6) : 1CT : 1CT ± 3%  
(P1-P2-P3) : (J1-J2) : 1CT : 1CT ± 3%

INDUCTANCE: (P4-P6) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias  
(P3-P1) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT) : 0.3uH MAX. @ 1MHz  
P3-P1 (WITH J2 AND J1 SHORT) : 0.3uH MAX. @ 1MHz

INTERWINDING CAPACITANCE: (P6,P5,P4) TO (J6,J3) : 30pF MAX. @ 1MHz  
(P3,P2,P1) TO (J2,J1) : 30pF MAX. @ 1MHz

DC RESISTANCE: (J6-J3)=(J2-J1) : 1.5 ohms Max.

RETURN LOSS: (P4-P6)=100 OHMS AND (P1-P3)=100 OHM REF.

1MHz TO 30MHz : 18dB MIN.  
60MHz TO 80MHz : 12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J3-J6).

DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3) : 1500 Vrms  
(J3, J6) TO (P4, P6) : 1500 Vrms

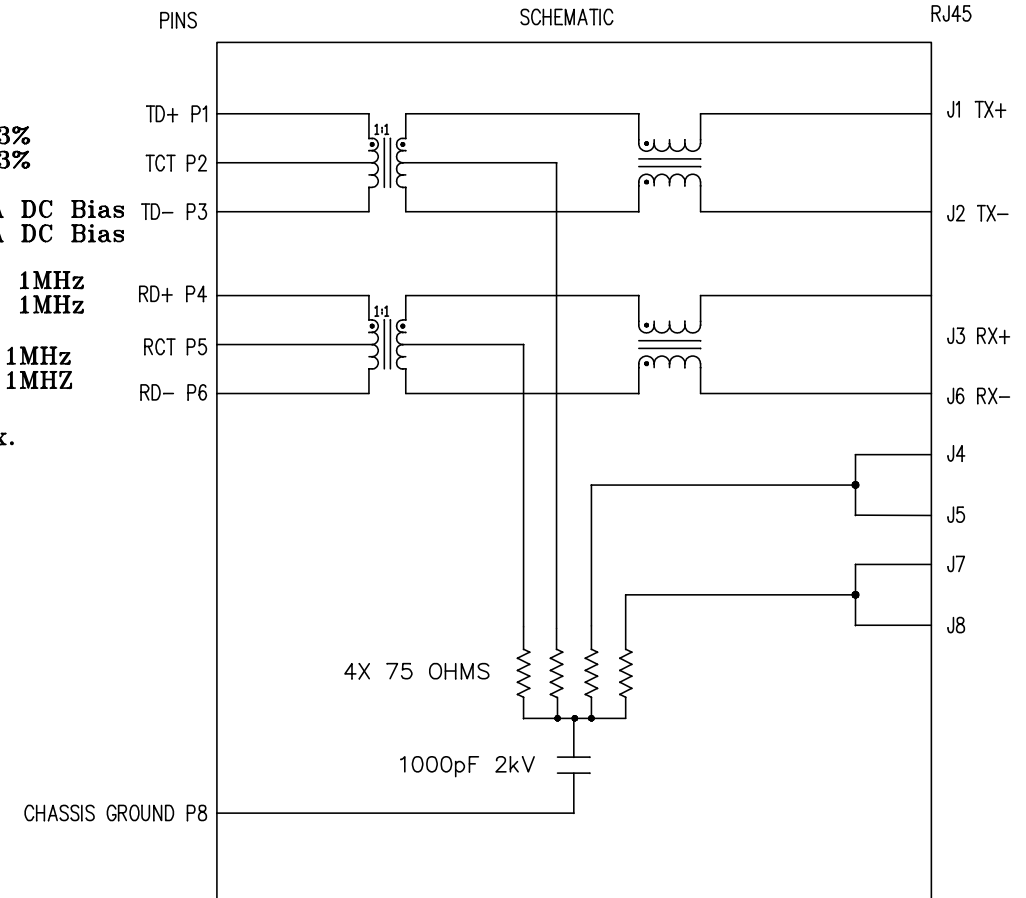
INSERTION LOSS: RS=RL=100 ohms : 1.1 dB TYP  
100KHz TO 100MHz

RISE TIME: RS=100 OHMS AND RL = 100 OHMS : 3.0 nS MAX  
OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX  
PULSE WIDTH= 112nS

CROSS TALK:1-100 MHz : 40 dB TYP

COMMON TO COMMON MODE ATTENUATION:

30MHz TO 100MHz : 35dB TYP



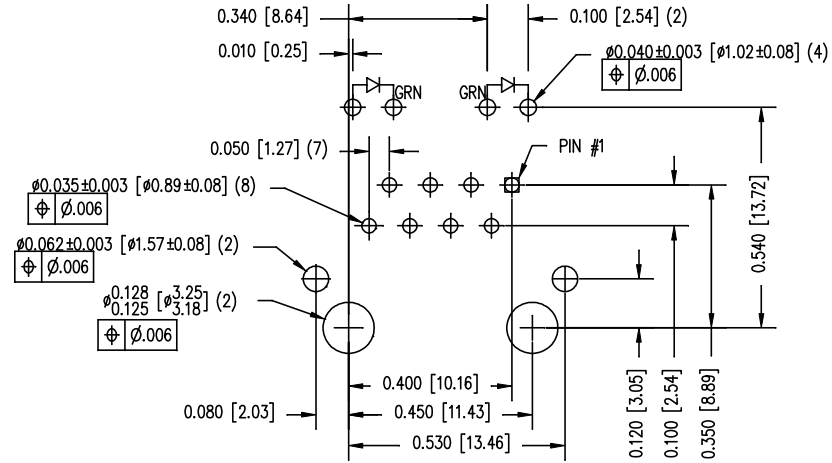
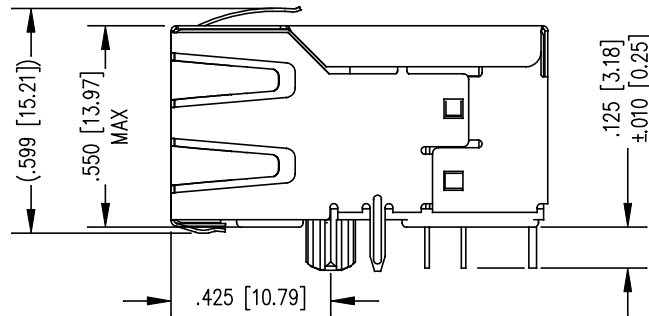
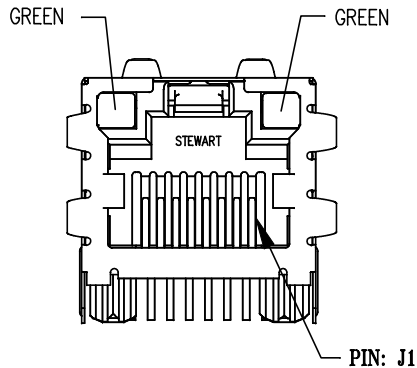
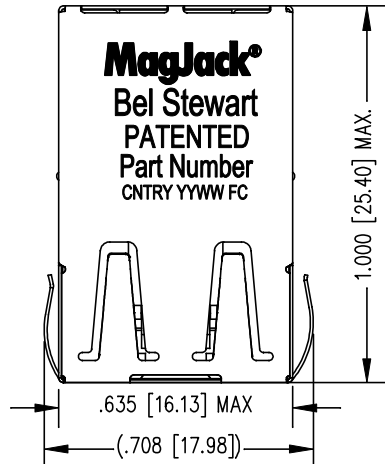
NOTES

1.0 COMPLIES WITH THE EUROPEAN DIRECTIVE 2002/95/EC, CALLING FOR THE ELIMINATION OF LEAD AND OTHER HAZARDOUS SUBSTANCES FROM ELECTRONIC PRODUCTS.

ORIGINATED BY Zeng Xiao Chun	DATE 2011-02-23	TITLE <b>MagJack®</b> 10/100BT, TAB UP, SHIELDED PATENTED	PART NO. / DRAWING NO. SI-55006-F	STANDARD DIM. TOL. IN INCH .X .XX .XXX ±0.005	[ ] METRIC DIM. AS REFERENCE UNIT : INCH [mm] SCALE : N/A 	REV. : 01 SIZE : A4 PAGE : 2	<b>bel</b> COMPONENTS OR A CONNECTED PLANET
DRAWN BY Zhang Ru Sheng	DATE 2011-02-23		FILE NAME SI-55006-F.DWG				

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P.C.B. RECOMMENDED HOLE LAYOUT  
SEEN FROM COMPONENT SIDE  
ALL CENTERLINE DIMENSIONS ARE BASIC

NOTES:

- CONNECTOR MATERIALS:  
HOUSING: THERMOPLASTIC UL94 V-0  
CONTACT/SHIELD: COPPER ALLOY  
SHIELD PLATING: NICKEL OR TIN  
CONTACT PLATING: SELECTIVE GOLD,  
50 MICRO-INCHES MIN. IN CONTACT AREA.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.  
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- REFLOW AND WAVE SOLDER COMPATIBLE -260°C FOR 10 SECONDS MAX.

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Forward V (MAX)	* (TYP)
GREEN	565 nm	2.5 V	2.2 V

ORIGINATED BY	DATE
Zeng Xiao Chun	2011-02-23
DRAWN BY	DATE
Zhang Ru Sheng	2011-02-23

TITLE	<b>MagJack®</b>
	<b>10/100BT, TAB UP, SHIELDED</b>
	<b>PATENTED</b>

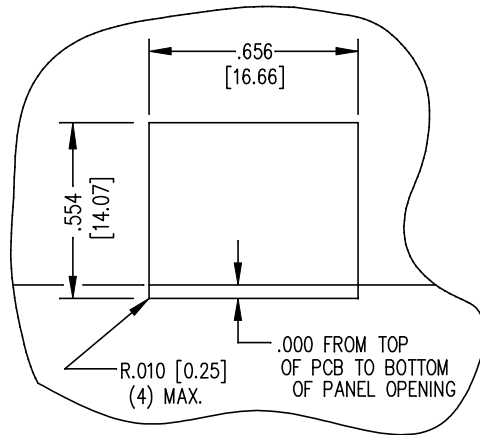
PART NO. / DRAWING NO.	<b>SI-55006-F</b>
FILE NAME	<b>SI-55006-F.DWG</b>

STANDARD DIM.	[ ] METRIC DIM. AS REFERENCE
TOL. IN INCH	UNIT : INCH [mm]
.X	REV. : 01
.XX	SCALE : N/A
.XXX	±.005
	SIZE : A4
	PAGE : 3

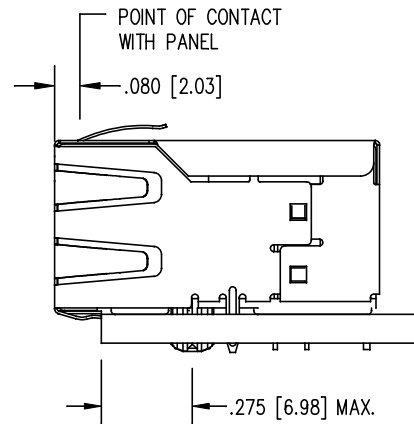


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SUGGESTED PANEL OPENING



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE  $\pm 0.005$  [0.13]

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[ ] METRIC DIM. AS REFERENCE	
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DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A	SIZE : A4
Zhang Ru Sheng	2011-02-23		SI-55006-F.DWG	.XX		PAGE : 4
				.XXX		$\pm 0.005$

