

PATENTED: THIS PRODUCT IS COVERED BY U.S. PATENT 5,736,910



DOCUMENT / PART NO.: SI-50170-G

TITLE: 10/100BT,TAB DOWN,SHIELDED

ISSUE	DESCRIPTION OF CHANGE			REVIEWED BY APPRO			PPRO	VED	вү	EC	N#							
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ROHS 2002/95/EC

J1 TX+

J2 TX-

J3 RX+

J6 RX-

J4

J5

J7

J8

ELECTRICAL CHARACTERISTICS @ 25°C

1.0 TURNS RATIO: (P4-P5-P6) : (J3-J6) : 1CT : 1CT ± 3% (P3-P2-P1) : (J2-J1) : 1CT : 1CT ± 3%

2.0 INDUCTANCE: (P4-P6) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias (P3-P1) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT): 0.3uH MAX. @ 1MHz P3-P1 (WITH J2 AND J1 SHORT): 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P6,P5,P4) TO (J6,J3) : 30pf MAX @ 1MHz (P3,P2,P1) TO (J2,J1) : 30pf MAX. @ 1MHZ

5.0 DC RESISTANCE: (J6-J3)=(J2-J1) : 1.35 ohms Max.

6.0 RETURN LOSS(P4-P6)=100 OHMS AND (P1-P3)=100 OHM REF.

1MHz TO 30MHz : 18dB MIN. 60MHz TO 80MHz : 12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J3-J6).

7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3) : 1500 Vrms (J3, J6) TO (P4, P6) : 1500 Vrms

8.0 INSERTION LOSS:RS=RL=100 ohms

100KHz TO 100MHz : 1.1 dB TYP

9.0 RISE TIME:RS=100 OHMS AND RL = 100 OHMS

OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1-100 MHz : 40 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATIONOMHZ TO 100MHZ: 35dB TYP



4X 75 DHMS

1000pF 2kV

SCHEMATIC

PINS

TD+ P1

TCT P2

TD- P3

RD+ P4

RCT P5

Chassis GND P8

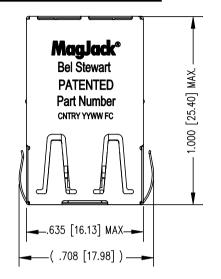
ORIGINATED BY	DATE	TITLE	M
Zeng Xiao Chun	08-01-2011		
DRAWN BY	DATE		TAB
Zhang Ru Sheng	08-01-2011		IAD

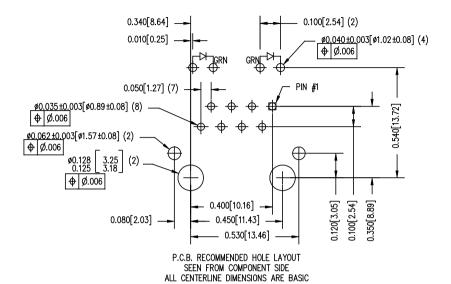
MagJack®
10/100BT,
TAB DOWN,SHIELDED
PATENTED

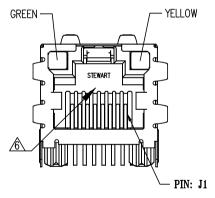
PART NO. / DRAWING NO.	STANDA	RD DIM.	[] METRIC DIM. AS REFERENCE		
SI-50170-G	TOL. IN INCH		UNIT : INCH [mm]	REV.: A0	
FILE NAME	.x .xx		SCALE: N/A	SIZE: A4	
SI-50170-G.DWG			SCALE. NA	SIZE . A4	
SI-50170-G.DVVG	XXX.	±0.005		PAGE: 2	

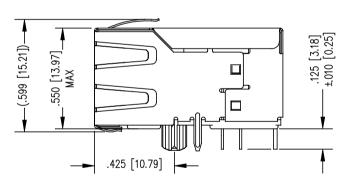












I	LED SPECIFICATION							
STANDARD LED WAVELENGTH FORWARD V (MAX) *(TYP)								
	GRFFN	565 nm	2.5 V	2.2 V				
	0112211			Z.Z V				
	l YELLOW	l 590 nm l	l 2.5 V I	2.1 V				

*WITH A FORWARD CURRENT OF 20 mA (TYP)

NOTES:

1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN

CONTACT PLATING: NICKEL OR IIN

CONTACT PLATING: SELECTIVE GOLD,

50 MICRO-INCHES MIN. IN CONTACT AREA.

- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- 5. REFLOW AND WAVE SOLDER COMPATIBLE—260°C FOR 10 SECONDS MAX.

1 JACK PORT CONTAINS ENVIRONMENTAL PROTECTION COATING.

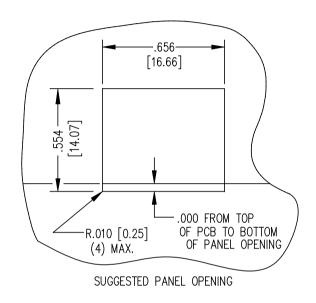
ORIGINATED BY	DATE	TITLE
X C Zeng	08-01-2011	
DRAWN BY	DATE	1
R S Zhang	08-01-2011	

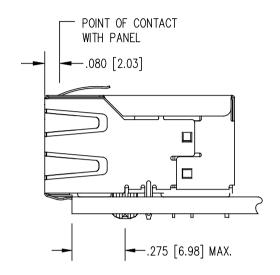
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SI-50170-G.DWG	.xxx	±0.005	+	PAGE: 3	









- 1. THE SUGGESTED PANEL OPENING IS INTENDED

 TO GIVE THE USER THE ABILITY TO HAVE

 REASONABLE JACK / PANEL CLEARANCES

 YET MAINTAIN RELIABLE GROUNDING

 CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED

 TO BE ±.005 [0.13]

ORIGINATED BY	DATE	TITLE
X C Zeng	08-01-2011	
DRAWN BY	DATE	
R S Zheng	08-01-2011	

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