

LED1 POLARITY			LED2 POLARITY		
PIN 21	PIN 22	COLOR	PIN 19	PIN 20	COLOR
-	+	YELLOW	+	-	GREEN
+	-	GREEN			

PRELIMINARY

RoHS
2002/95/EC

ELECTRICAL CHARACTERISTICS @ 25°C

URNS RATIO

TP1	1CT : 1CT ±2%
TP2	1CT : 1CT ±2%
TP3	1CT : 1CT ±2%
TP4	1CT : 1CT ±2%

CL @ 100kHz/100mVRMS
8mA DC BIAS

350μH MIN.

INS. LOSS

100kHz TO 100MHz	-0.8 dB MAX
100MHz TO 125MHz	-1.2 dB MAX

RET. LOSS (MIN)

0.5MHz-40MHz	-18 dB
40MHz-100MHz	-12+20LOG(f/80MHz) dB

CM TO CM REJ

100kHz - 100MHz	-30 dB MIN
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CM TO DM REJ

100kHz - 100MHz	-35 dB MIN
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HIPOT (Isolation Voltage):

1500 Vrms or 2250VDC

LED 1

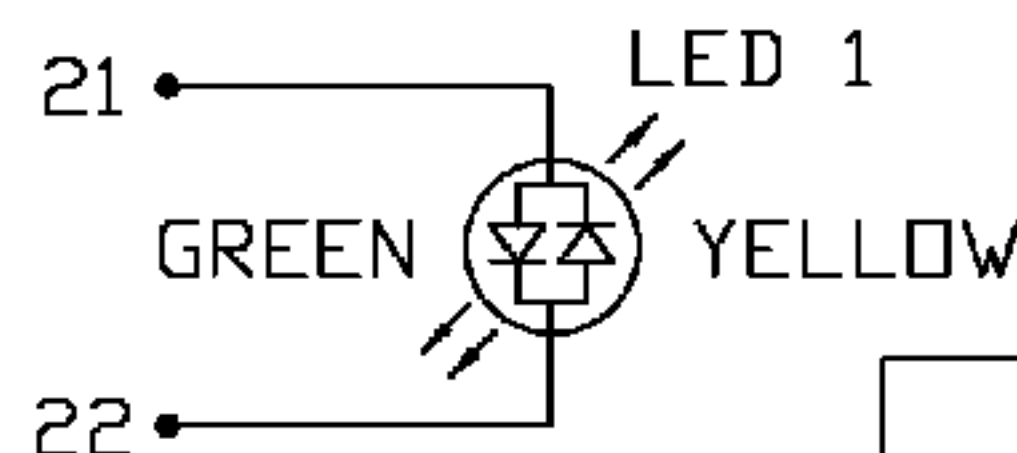
VF (FORWARD VOLTAGE)	IF=20mA	GREEN	2.2V TYP.
		YELLOW	2.1V TYP.
λD (DOMINANT WAVELENGTH)	IF=20mA	GREEN	565nm TYP.
		YELLOW	590nm TYP.

LED 2

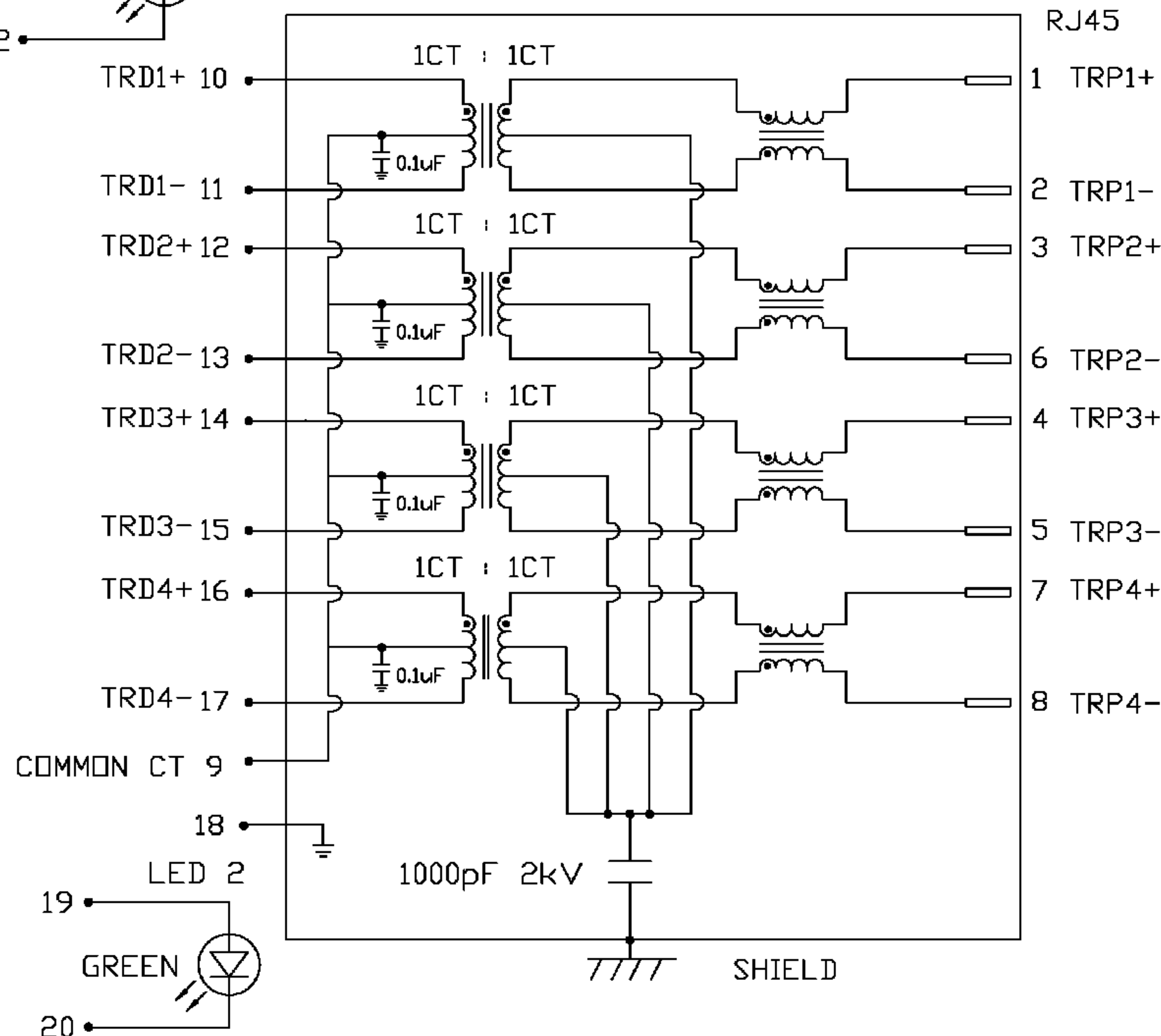
VF (FORWARD VOLTAGE)	IF=20mA	GREEN	2.2V TYP.
λD (DOMINANT WAVELENGTH)	IF=20mA	GREEN	565nm TYP.

USB ELECTRICAL:

INSULATION RESISTANCE: 1000 MEGA Ω (MIN)
CONTACT RESISTANCE: 25 MILLI Ω (MAX)
DIELECTRIC WITHSTANDING: 750 VAC



SCHEMATIC



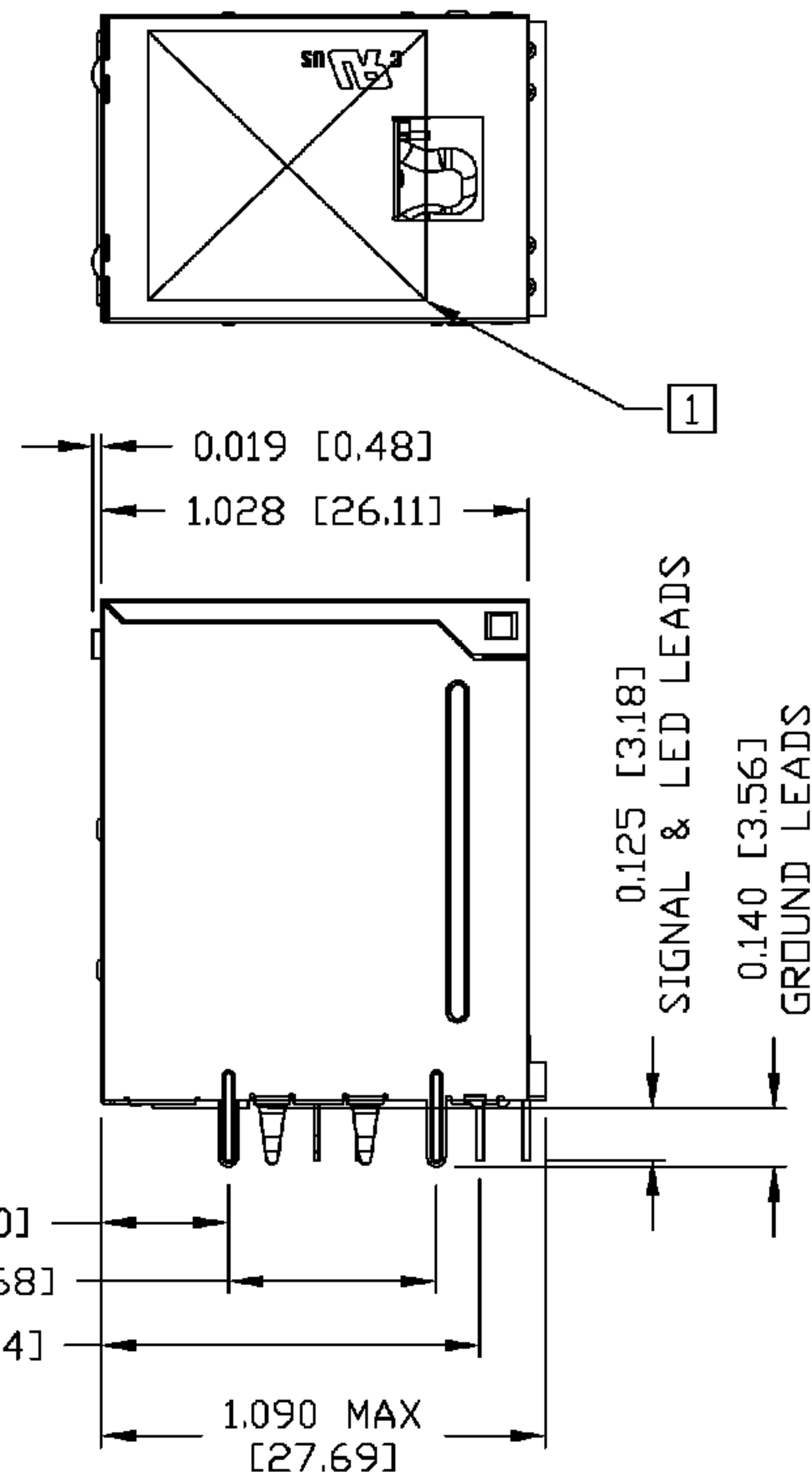
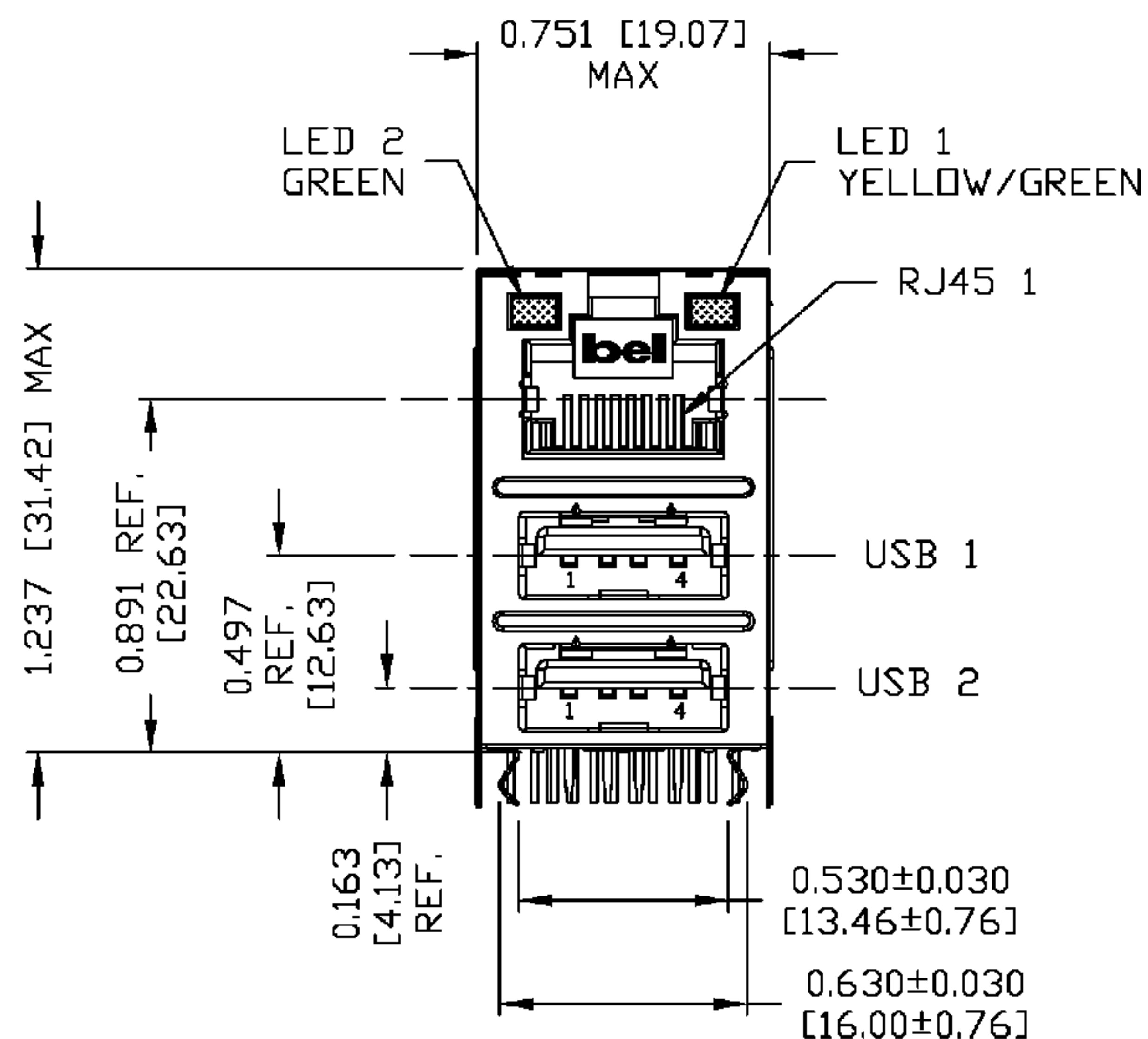
ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[] METRIC DIM. AS REFERENCE
CHUNG	04-18-06	gigabit MagJack® Combo (Staggered pin, 8 cores) C892-1LX1-E3-F	C8921LX1E3-F	TOL. IN INCH	UNIT : INCH [mm] REV. : PC
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A SIZE : A4
DE	04-18-06		C8921LX1E3-FPC.DWG	.XX .XXX	PAGE : 2

bel
COMPONENTS ON A
CONNECTED
PLANET

MECHANICAL SPECIFICATION

PRELIMINARY

RoHS
2002/95/EC

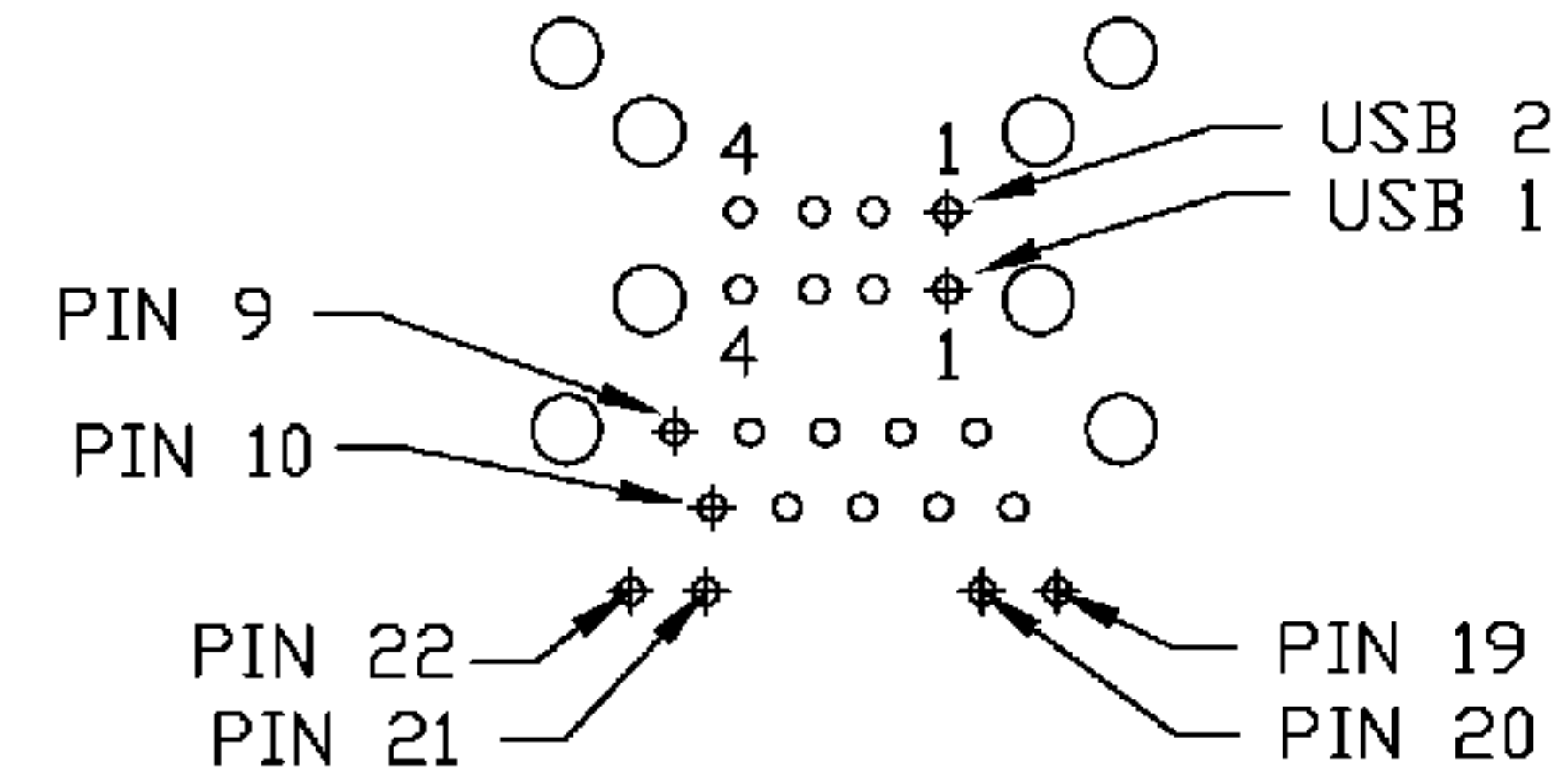
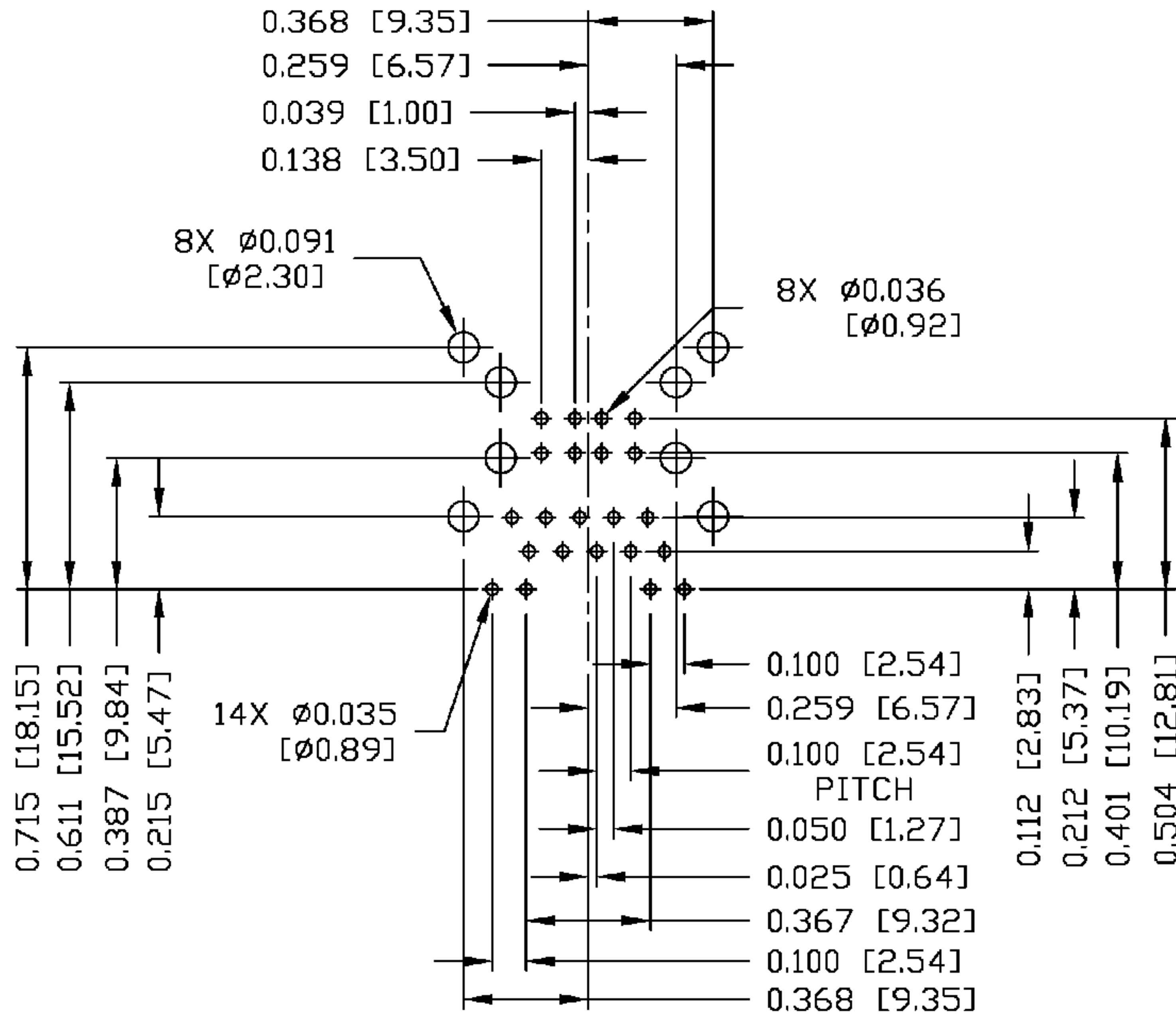


ORIGINATED BY QIN	DATE 04-18-06	TITLE gigabit MagJack® Combo (Staggered pin, 8 cores) C892-1LX1-E3-F	PART NO. / DRAWING NO. C8921LX1E3-F	STANDARD DIM. TOL. IN INCH .X .XX .XXX ±0.010	[] METRIC DIM. AS REFERENCE	
					UNIT : INCH [mm]	REV. : PC
DRAWN BY TING	DATE 04-18-06		FILE NAME C8921LX1E3-FPC.DWG		SCALE : N/A	SIZE : A4
						PAGE : 3

bel
COMPONENTS FOR A
CONNECTED
PLANET

Recommended PCB footprint Component Side

PRELIMINARY



RJ45 MECHANICAL:

PLASTIC HOUSING: THERMOPLASTIC PBT
FLAMMABILITY RATING UL 94V-0
CONTACTS PLATING: 50 MICRO-INCH HARD GOLD PLATING
PINS: TIN-COATED CP WIRE, DIA 0.018 INCH.
METAL SHIELD: PRE-PLATED NICKEL ON COPPER ALLOY.
(ALL GROUND LEADS ARE SOLDER DIPPED)

USB MECHANICAL:

MATING FORCE: 35 NEWTONS MAXIMUM
UNMATING FORCE: 10 NEWTONS MINIMUM
CONTACT: PHOSPHOR BRONZE
30 MICRO-INCH GOLD PLATING ON CONTACT
AREA AND TIN ON SOLDER TAIL.

1. MARK PART WITH MFG LOGO, MFG NAME, PART NUMBER, AND
DATE CODE.

UL UL RECOGNIZED - FILE #E196366 AND E169987.

RoHS COMPLIANCE, PER EU DIRECTIVE 2002/95/EC.

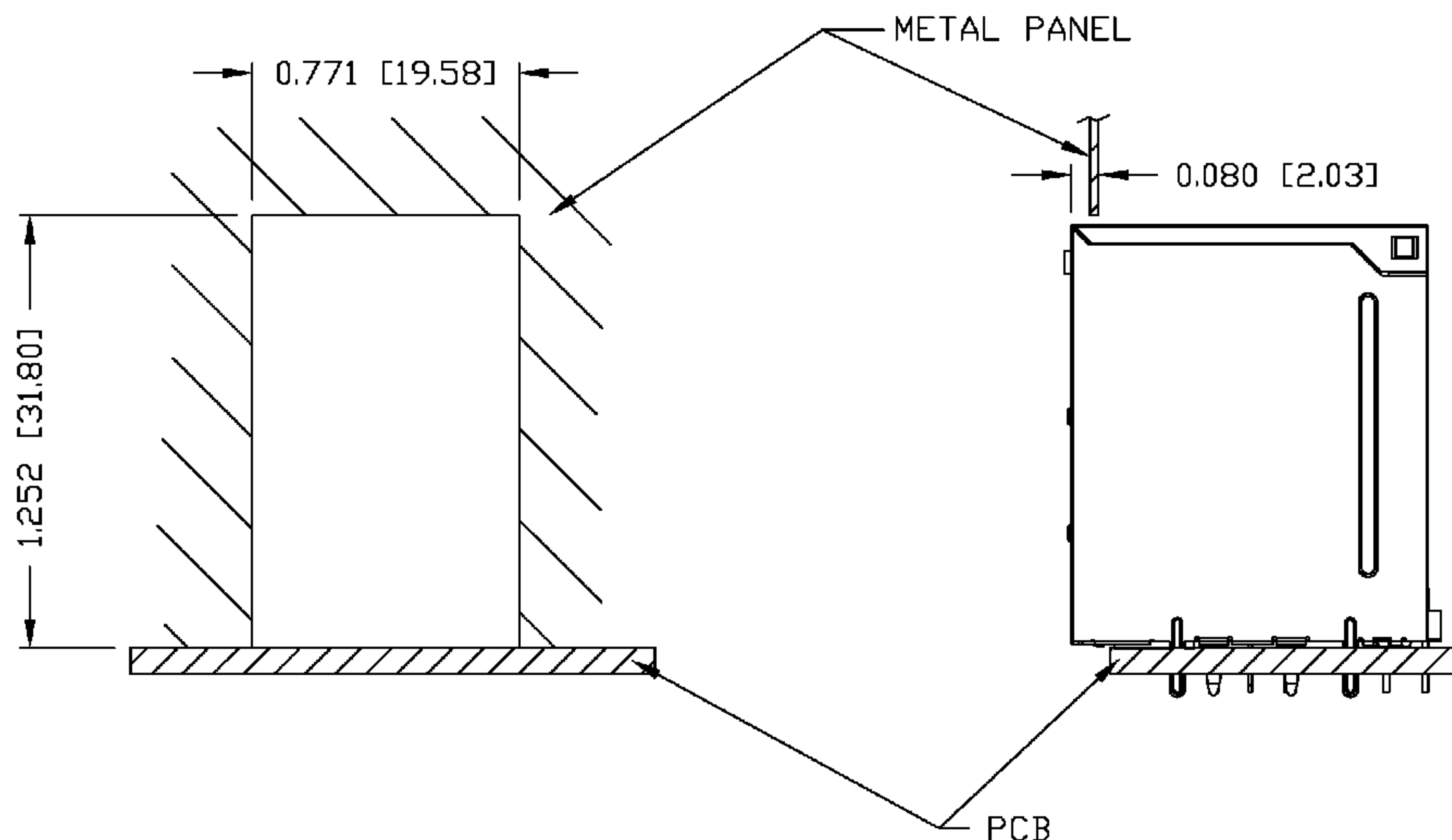
JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS.

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REFERENCE
QIN	04-18-06	gigabit MagJack® Combo (Staggered pin, 8 cores) C892-1LX1-E3-F	C8921LX1E3-F	.X	UNIT : INCH [mm] REV. : PC
DRAWN BY	DATE		FILE NAME	.XX	SCALE : N/A SIZE : A4
TING	04-18-06		C8921LX1E3-FPC.DWG	.XXX	±0.004
					PAGE : 4





SUGGESTED PANEL OPENING



NOTE:

THE DISTANCE OF PANEL INSIDE SURFACE RELATIVE TO FRONT SURFACE OF PART IS ONLY A SUGGESTION.
IN CASE THIS DISTANCE IS DIFFERENT, THE REQUIRED PANEL OPENING DIMENSIONS CHANGE ACCORDINGLY.

PACKING INFORMATION

PACKING TRAY : 0200-9999-H3 (TOP)
0200-9999-H4 (BOTTOM)

PACKING QUANTITY : 35 PCS FINISHED GOODS PER TRAY
9 TRAYS (315 PCS FINISHED GOODS) PER CARTON BOX

NOTE : CARDBOARDS ARE PLACED BETWEEN LAYERS OF PACKING TRAY INSIDE CARTON BOX
(INCLUDE THE UPPERMOST AND LOWERMOST TRAY)

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REFERENCE
QIN	04-18-06	gigabit MagJack® Combo (Staggered pin, 8 cores) C892-1LX1-E3-F	C8921LX1E3-F		UNIT : INCH [mm] REV. : PC
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A
TING	04-18-06		C8921LX1E3-FPC.DWG	.XX	SIZE : A4
				.XXX ±0.004	PAGE : 5