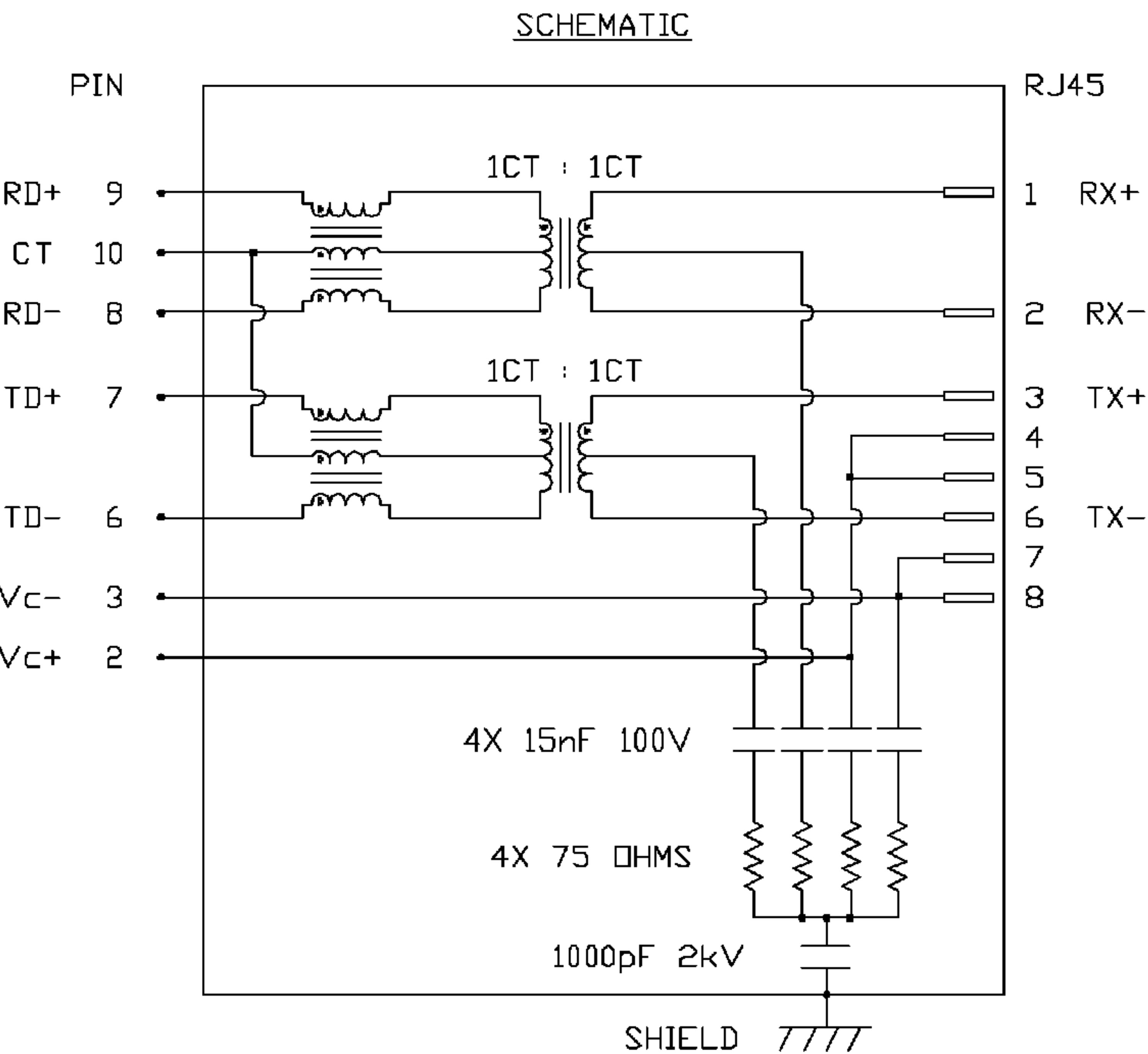


ELECTRICAL CHARACTERISTICS @ 25°C

|                                                                   |                                                                              |
|-------------------------------------------------------------------|------------------------------------------------------------------------------|
| URNS RATIO                                                        | 1CT : 1CT ±2%                                                                |
| DCL @ 0.1V, 100KHz<br>8mA DC BIAS                                 | 350μ H MIN.                                                                  |
| INSERTION LOSS<br>100KHz TO 100MHz                                | -1.5 dB MAX.                                                                 |
| RETURN LOSS (MIN.)<br>2MHz-30MHz<br>60MHz-80MHz                   | -16 dB MIN.<br>-10 dB MIN.                                                   |
| LEAKAGE INDUCTANCE<br>PRIMARY W/ SECONDARY SHORTED                | 0.25μH MAX. @ 1MHz                                                           |
| D.C. RESISTANCE<br>(9-8)=(7-6)<br>(RJ1-RJ2)=(RJ3-RJ6)             | 1.40 OHM MAX.<br>1.0 OHM MAX.                                                |
| INTERWINDING CAPACITANCE<br>PER CHANNEL (PRI. to SEC.)            | 40 pF MAX.                                                                   |
| XTALK<br>100KHz TO 100MHz                                         | -30 dB MIN.                                                                  |
| HIPOT (Isolation Voltage)<br>100% OF PRODUCT TESTED IN PRODUCTION | 2250 VDC                                                                     |
| BALANCED DC LINE CURRENT                                          | 350 mA MAX. @ 57 VDC CONTINUOUS<br>500 mA MAX. @ 57 VDC FOR 200 MILLISECONDS |



|       |          |
|-------|----------|
| DRWN  | DATE     |
| DE    | 12-08-04 |
| CHKD  | DATE     |
| CHUNG | 12-08-04 |

|                                             |
|---------------------------------------------|
| TITLE                                       |
| 2X8 Stacked MagJack™<br>FOR 10/100 VOIP/PDE |

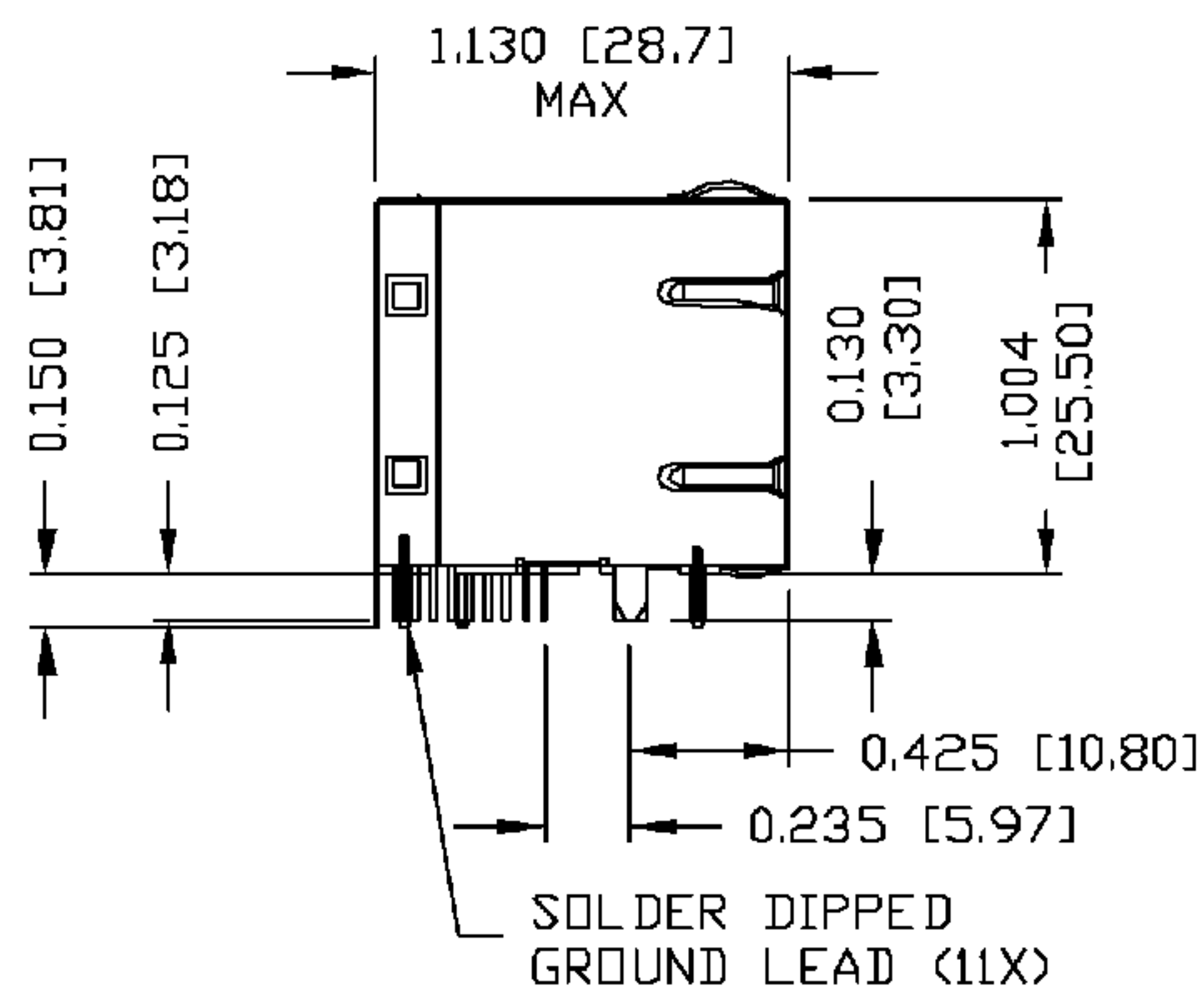
|                        |
|------------------------|
| PART NO. / DRAWING NO. |
| 0833-2X8R-59           |
| FILE NAME              |
| 08332X8R59PC.DWG       |

| STANDARD DIM.<br>TOL. IN INCH | [ ] METRIC DIM. AS REFERENCE |
|-------------------------------|------------------------------|
| .X                            | UNIT : INCH [mm]             |
| .XX                           | SCALE : N/A                  |
| .XXX                          | REV. : PC                    |
|                               | SIZE : A4                    |
|                               | PAGE : 2                     |



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PRELIMINARY



NOTES:

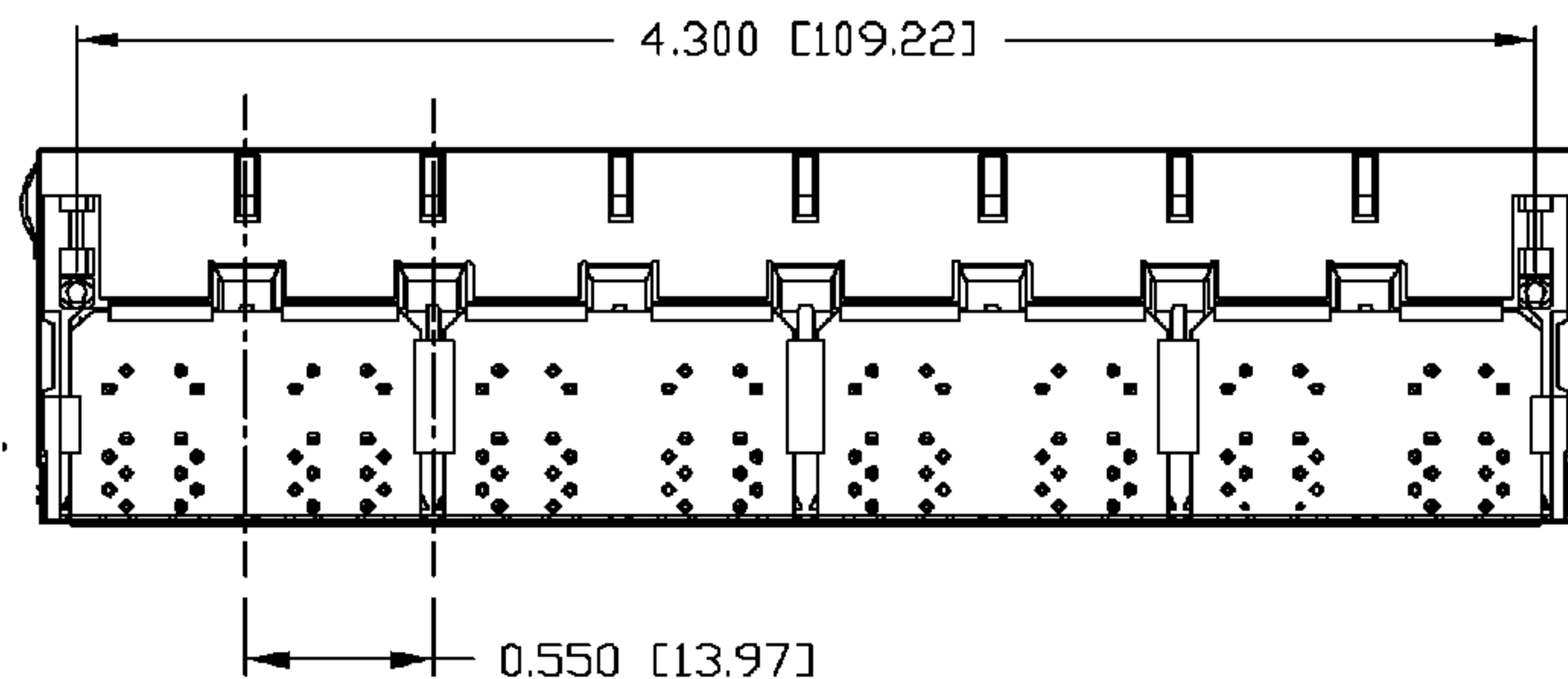
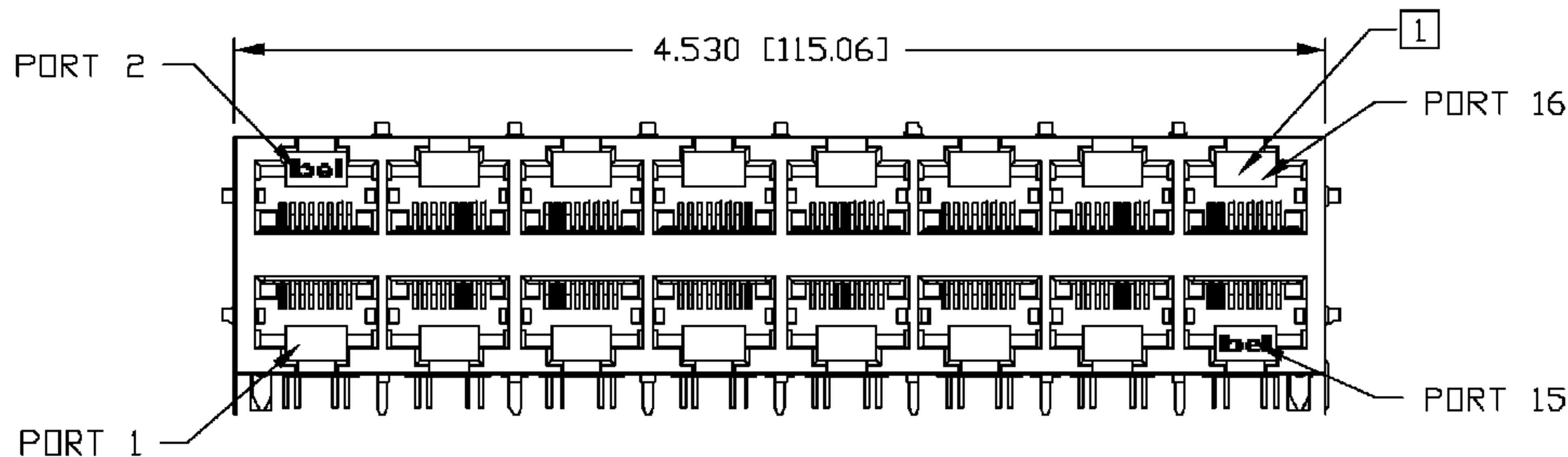
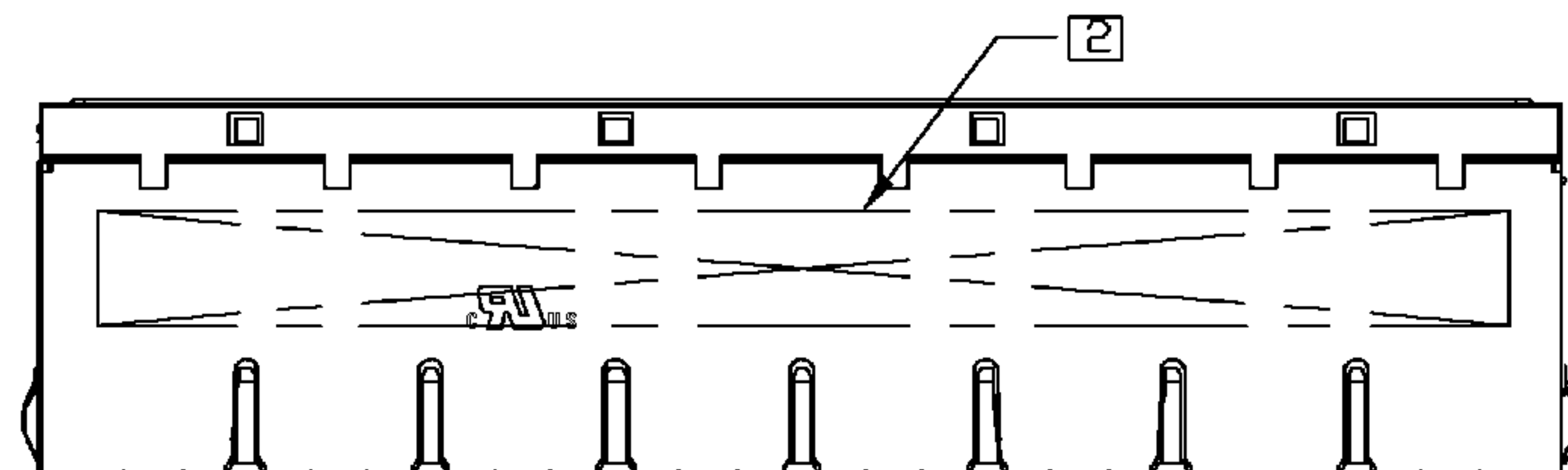
PLASTIC HOUSING: THERMOPLASTIC PBT  
FLAMMABILITY RATING UL 94V-0  
CONTACT PLATING: 50 MICRO-INCH HARD GOLD PLATING  
OUTPUT PINS: TIN-COATED COPPER WIRE, DIA 0.018 INCH.  
SOLDERABILITY: PER MIL STD. 202, METHOD 208.  
METAL SHIELD: NICKEL PLATED COPPER ALLOY.

(ALL GROUND LEADS ARE SOLDER DIPPED)

1 JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F.

2 MARK PART WITH MFG LOGO , MFG NAME , PART NUMBER, AND DATE CODE

c usUL RECOGNIZED - FILE #E196366 AND E169987.



DRWN QIAN  
DATE 12-08-04

CHKD TAD/KEUNG  
DATE 12-08-04

TITLE  
2X8 Stacked MagJack™  
FOR 10/100 VOIP/POE

PART NO. / DRAWING NO.  
0833-2X8R-59

FILE NAME  
08332X8R59PC.DWG

STANDARD DIM.  
TOL. IN INCH

.X  
.XX  
.XXX ±0.010

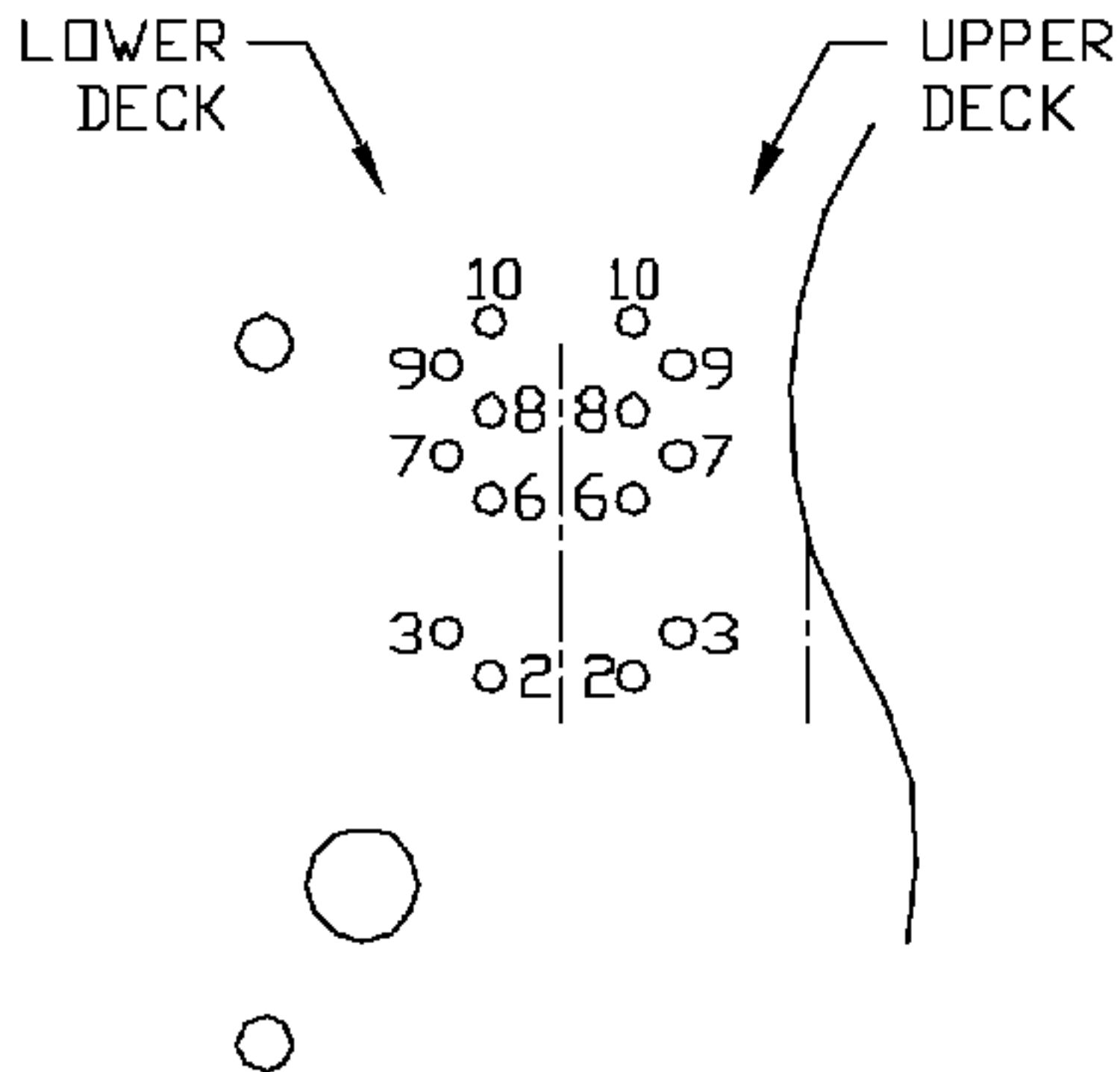
[ ] METRIC DIM. AS REFERENCE

UNIT : INCH [mm] REV. : PC

SCALE : N/A SIZE : A4

PAGE : 3

**bel**  
COMPONENTS FOR A  
CONNECTED  
PLANET

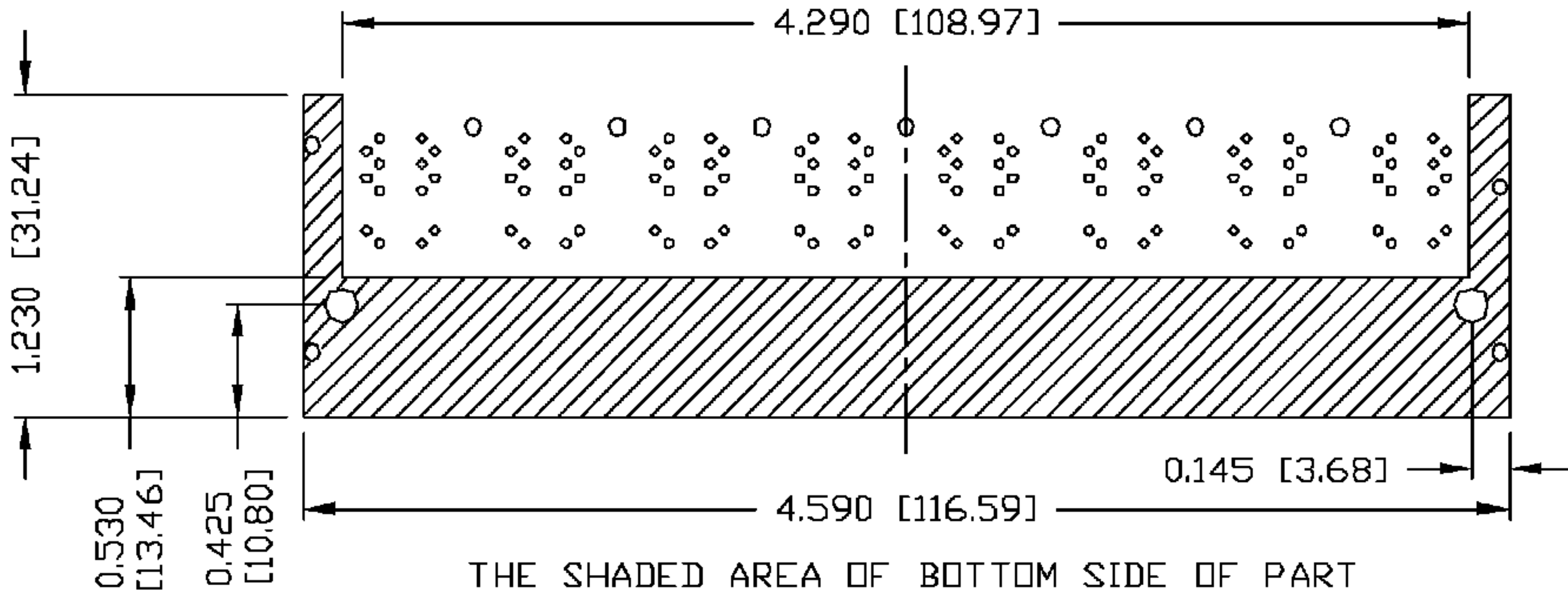
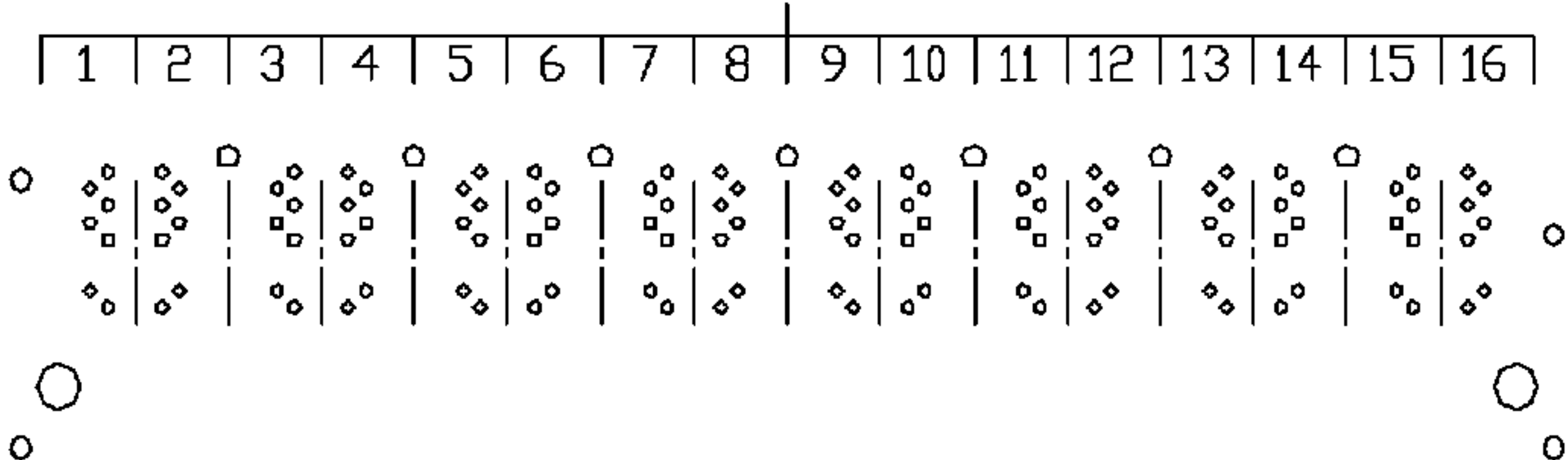


PIN-OUT INFORMATION  
(COMPONENT SIDE VIEW)

|    |           |
|----|-----------|
| 10 | COMMON CT |
| 9  | RD+       |
| 8  | RD-       |
| 7  | TD+       |
| 6  | TD-       |
| 5  | NO PIN    |
| 4  | NO PIN    |
| 3  | Vc-       |
| 2  | Vc+       |
| 1  | NO PIN    |

UNUSED PIN LOCATIONS MUST BE  
REMOVED TO ENSURE 2KV ISOLATION.

PORT ASSIGNMENT  
(COMPONENT SIDE VIEW)



THE SHADED AREA OF BOTTOM SIDE OF PART  
MAY BE COVERED BY METAL SHIELD.

|           |          |
|-----------|----------|
| DRWN      | DATE     |
| QIAN      | 12-08-04 |
| CHKD      | DATE     |
| TAD/KEUNG | 12-08-04 |

|                                             |
|---------------------------------------------|
| TITLE                                       |
| 2X8 Stacked MagJack™<br>FOR 10/100 VOIP/POE |

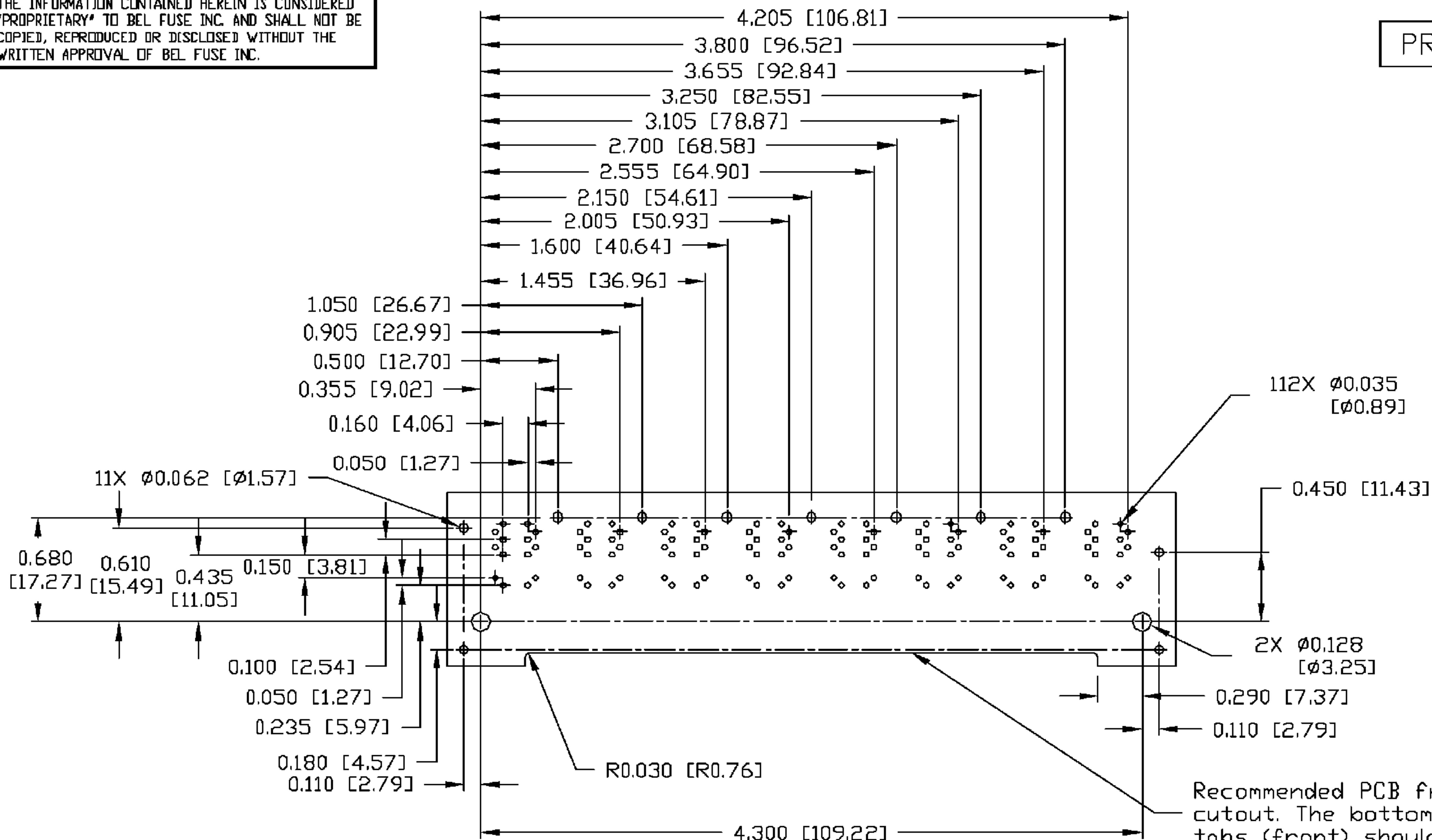
|                        |
|------------------------|
| PART NO. / DRAWING NO. |
| 0833-2X8R-59           |
| FILE NAME              |
| 08332X8R59PC.DWG       |

| STANDARD DIM.<br>TOL. IN INCH |                                                                                       | [ ] METRIC DIM. AS REFERENCE                                                          |           |
|-------------------------------|---------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------|-----------|
| .X                            |  | UNIT : INCH [mm]                                                                      | REV. : PC |
| .XX                           |                                                                                       | SCALE : N/A                                                                           | SIZE : A4 |
| .XXX                          |                                                                                       |  | PAGE : 4  |
|                               | ±0.010                                                                                |                                                                                       |           |



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PRELIMINARY



Recommended PCB front edge  
cutout. The bottom shield ground  
tabs (front) should not be seated  
on the top of the PCB.

SUGGESTED FOOTPRINT  
(COMPONENT SIDE VIEW)

DRWN QIAN  
DATE 12-08-04

CHKD TAO/KEUNG  
DATE 12-08-04

TITLE  
2X8 Stacked MagJack™  
FOR 10/100 VOIP/POE

PART NO. / DRAWING NO.  
0833-2X8R-59

FILE NAME  
08332X8R59PC.DWG

| STANDARD DIM.<br>TOL. IN INCH |        |
|-------------------------------|--------|
| .X                            |        |
| .XX                           |        |
| .XXX                          | ±0.004 |

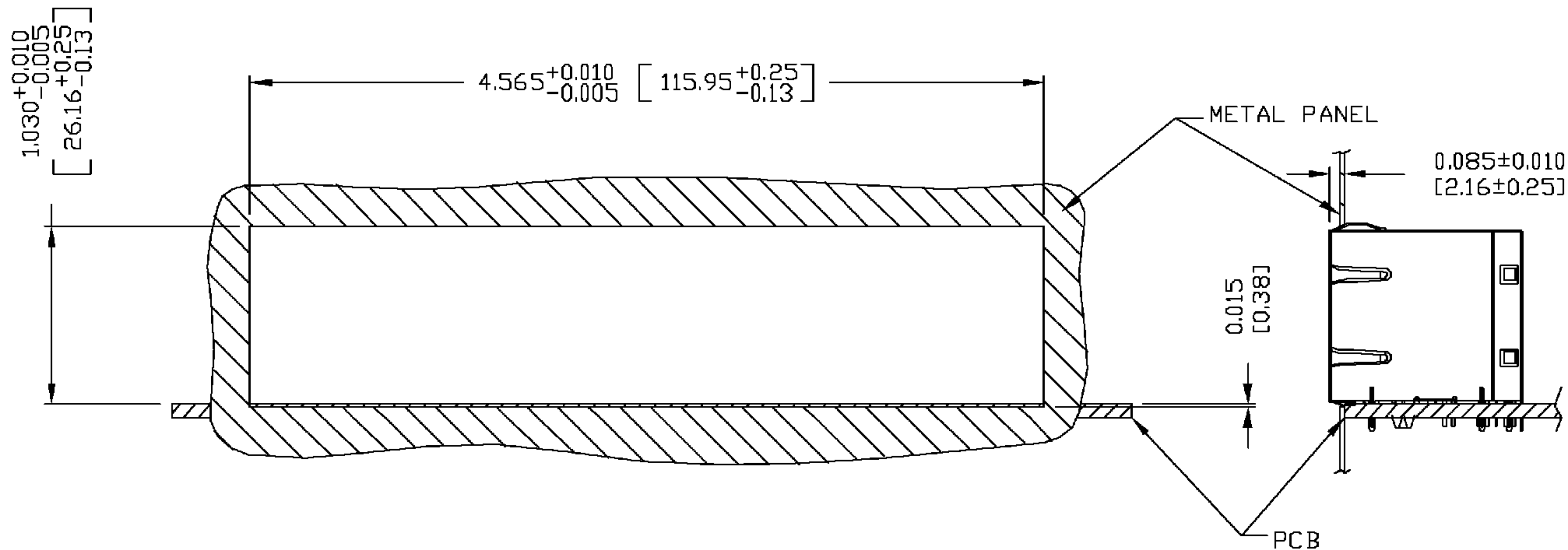
| [ ] METRIC DIM. AS REFERENCE |           |
|------------------------------|-----------|
| UNIT : INCH [mm]             | REV. : PC |
| SCALE : N/A                  | SIZE : A4 |
|                              | PAGE : 5  |

**bel**  
COMPONENTS FOR A  
CONNECTED  
PLANET

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SUGGESTED PANEL OPENING

PRELIMINARY



NOTE:

THE DISTANCE OF PANEL INSIDE SURFACE RELATIVE TO FRONT SURFACE OF PART IS ONLY A SUGGESTION.  
IN CASE THIS DISTANCE IS DIFFERENT, THE REQUIRED PANEL OPENING DIMENSIONS CHANGE ACCORDINGLY.

PACKING INFORMATION

PACKING TRAY : 0200-9999-B2 (TOP)  
0200-9999-B1 (BOTTOM)

PACKING QUANTITY : 10 PCS FINISHED GOODS PER TRAY  
7 TRAY (70 PCS FINISHED GOODS) PER CARTON BOX

NOTE : CARDBOARDS ARE PLACED BETWEEN LAYERS OF PACKING TRAY INSIDE CARTON BOX  
(INCLUDE THE UPPERMOST AND LOWERMOST TRAY)

DRWN DATE  
QIAN 12-08-04

CHKD DATE  
TAD/KEUNG 12-08-04

TITLE  
2X8 Stacked MagJack™  
FOR 10/100 VOIP/POE

PART NO. / DRAWING NO.  
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| STANDARD DIM.<br>TOL. IN INCH |        | [ ] METRIC DIM. AS REFERENCE |           |
|-------------------------------|--------|------------------------------|-----------|
| .X                            |        | UNIT : INCH [mm]             | REV. : PC |
| .XX                           |        | SCALE : N/A                  | SIZE : A4 |
| .XXX                          | ±0.004 |                              | PAGE : 6  |

