

<TRANSISTOR ARRAY>

M63840FP

**8-UNIT 500mA DARLINGTON TRANSISTOR ARRAY WITH CLAMP DIODE
SOURCE TYPE**

DESCRIPTION

M63840FP are eight-circuit output-sourcing Darlington transistor array. The circuits are made of PNP and NPN transistors. Both the semiconductor integrated circuits perform high-current driving with extremely low input-current supply.

FEATURES

- High breakdown voltage ($BV_{CEO} \geq 40V$)
- High-current driving ($I_o(max) = -500mA$)
- With clamping diodes
- Wide operating temperature range ($T_a = -40 \sim +85^\circ C$)
- Driving available with PMOS IC output of 6 ~ 16V or with TTL output
- Output current-sourcing type

APPLICATIONS

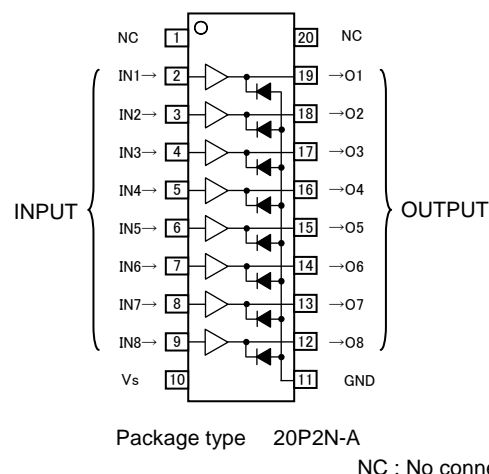
Drives of relays, printers, LEDs, fluorescent display tubes and lamps, and interfaces between MOS-bipolar logic systems and relays, solenoids, or small motors.

FUNCTION

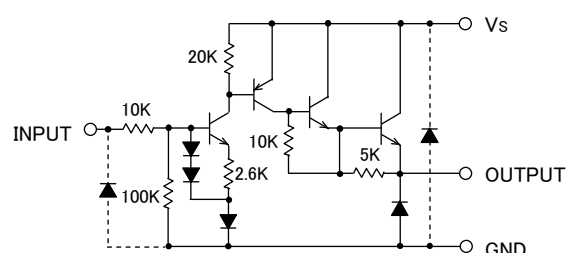
The M63840FP each have eight circuits, which are made of input inverters and current-sourcing outputs. The outputs are made of PNP transistors and NPN Darlington transistors. The PNP transistor base current is constant. A clamping diode is provided between each output and GND. VS and GND are used commonly among the eight circuits.

The inputs have resistance of 10k Ω , and voltage of up to 15V is applicable. Output current is 500 mA maximum. Supply voltage VS is 50V maximum.

PIN CONFIGURATION



CIRCUIT DIAGRAM



The eight circuits share the VS and GND.

The diode, indicated with the dotted line, is parasitic, and cannot be used.

Unit : Ω

ABSOLUTE MAXIMUM RATINGS (Unless otherwise noted, $T_a = -40 \sim +85^\circ C$)

Symbol	Parameter	Conditions	Ratings	Unit
V_{CEO} #	Collector-emitter voltage	Output, L	-0.5 ~ +40	V
V_s	Supply voltage		40	V
V_i	Input voltage		-0.5 ~ +15	V
I_o	Output current	Current per circuit output, H	-500	mA
I_F	Clamping diode forward current		-500	mA
V_R #	Clamping diode reverse voltage		40	V
P_d	Power dissipation	$T_a = 25^\circ C$, when mounted on board	1.10	W
T_{opr}	Operating temperature		-40 ~ +85	$^\circ C$
T_{stg}	Storage temperature		-55 ~ +125	$^\circ C$

: Unused Input pins must be connected to GND.

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SOURCE TYPE****RECOMMENDED OPERATING** (Unless otherwise noted, $T_a = -40 \sim +85^\circ\text{C}$)

Symbol	Parameter	Limits			Unit
		min	typ	max	
V_s	Supply voltage	0	—	40	V
I_o	Output current (Current per 1 circuit when 8 circuits are coming on simultaneously)	Duty Cycle no more than 5%	0	—	mA
		Duty Cycle no more than 30%	0	—	
V_{IH}	"H" input voltage	2.0	—	12	V
V_{IL}	"L" input voltage	0	—	0.8	V

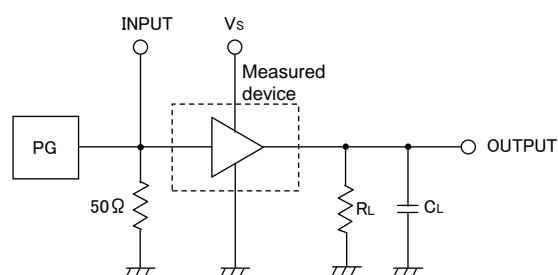
ELECTRICAL CHARACTERISTICS (Unless otherwise noted, $T_a = 25^\circ\text{C}$)

Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
$I_{S(\text{leak})}$ #	Supply leak current	$V_s = 40\text{V}$, $V_i = 0.8\text{V}$	—	—	100	μA
$V_{CE(\text{sat})}$	Collector-emitter saturation voltage	$V_s = 10\text{V}$, $V_i = 2.0\text{V}$, $I_o = -350\text{mA}$	—	1.7	2.0	V
		$V_s = 10\text{V}$, $V_i = 2.0\text{V}$, $I_o = -100\text{mA}$	—	1.5	2.0	
I_i	Input current	$V_i = 2.4\text{V}$	—	36	52	μA
		$V_i = 3.85\text{V}$	—	180	5.0	
I_s	Supply current	$V_s = 40\text{V}$, $V_i = 2\text{V}$ (all input)	—	—	2.5	mA
V_F #	Clamping diode forward voltage	$I_F = -350\text{mA}$	—	-1.3	-2.0	V
I_R	Clamping diode reverse current	$V_R = 40\text{V}$	—	—	100	μA

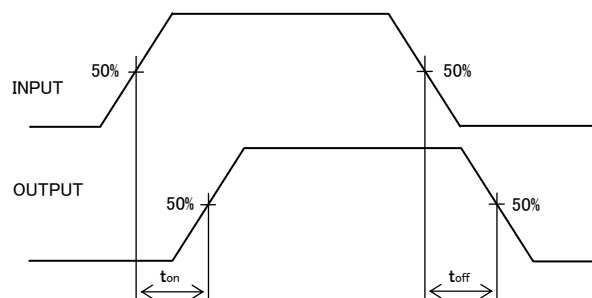
: Unused Input pins must be connected to GND.

SWITCHING CHARACTERISTICS (Unless otherwise noted, $T_a = 25^\circ\text{C}$)

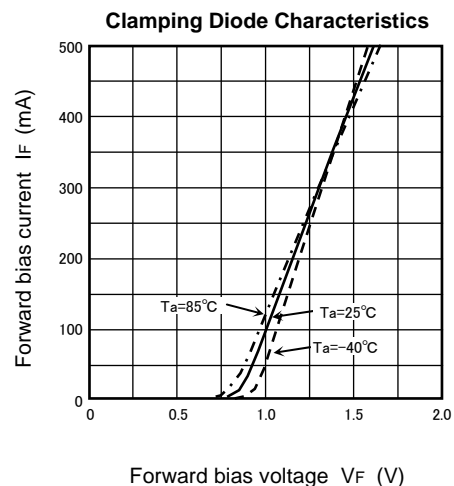
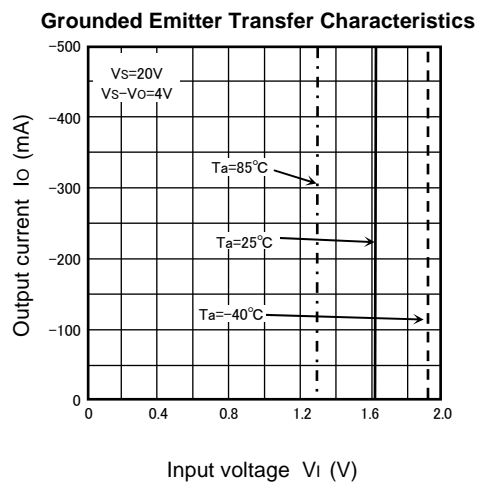
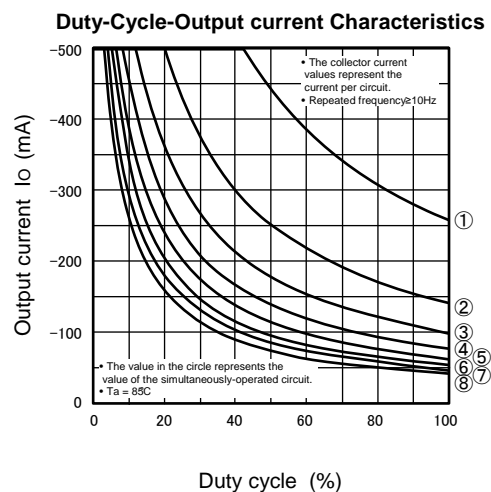
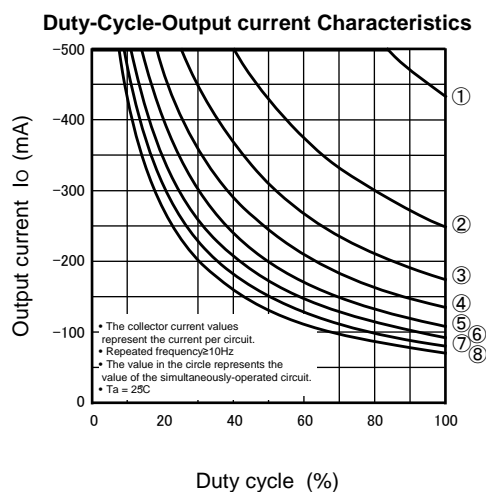
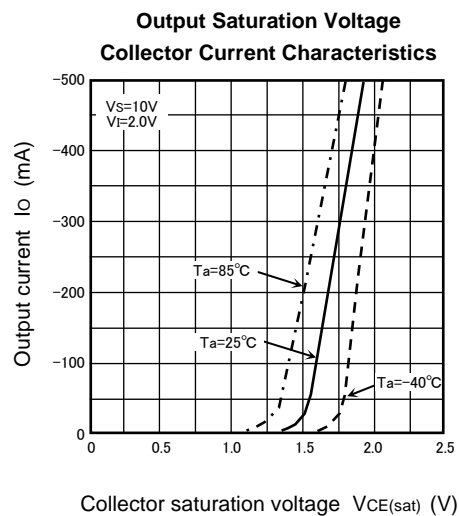
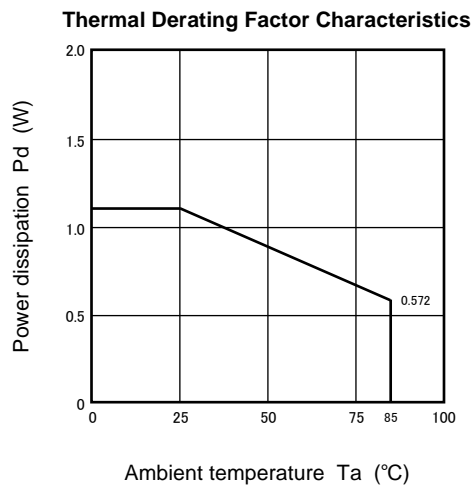
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
t_{on}	Turn-on time	$C_L = 15\text{pF}$ (note 1)	—	210	—	ns
t_{off}	Turn-off time		—	2200	—	ns

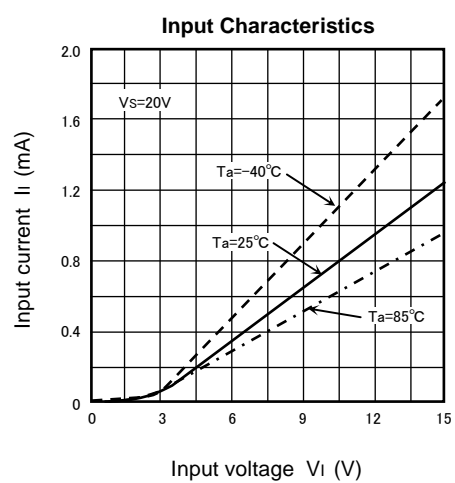
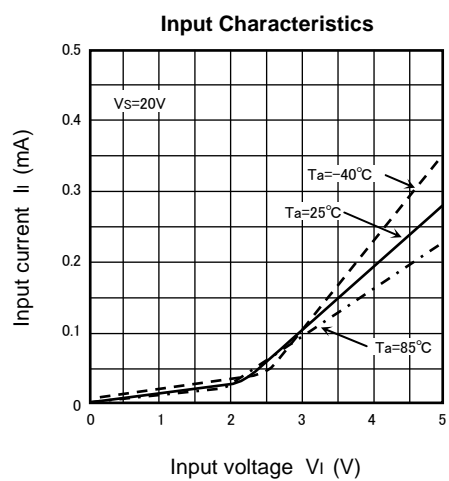
NOTE 1 TEST CIRCUIT

- (1) Pulse generator (PG) characteristics: PRR = 1kHz, $t_w = 10\text{ms}$, $t_r = 6\text{ns}$, $t_f = 6\text{ns}$, $Z_o = 50\Omega$, $V_i = 0$ to 2V
- (2) Input-output conditions : $R_L = 30\Omega$, $V_s = 10\text{V}$
- (3) Electrostatic capacity C_L includes floating capacitance at connections and input capacitance at probes

TIMING DIAGRAM

TYPICAL CHARACTERISTICS





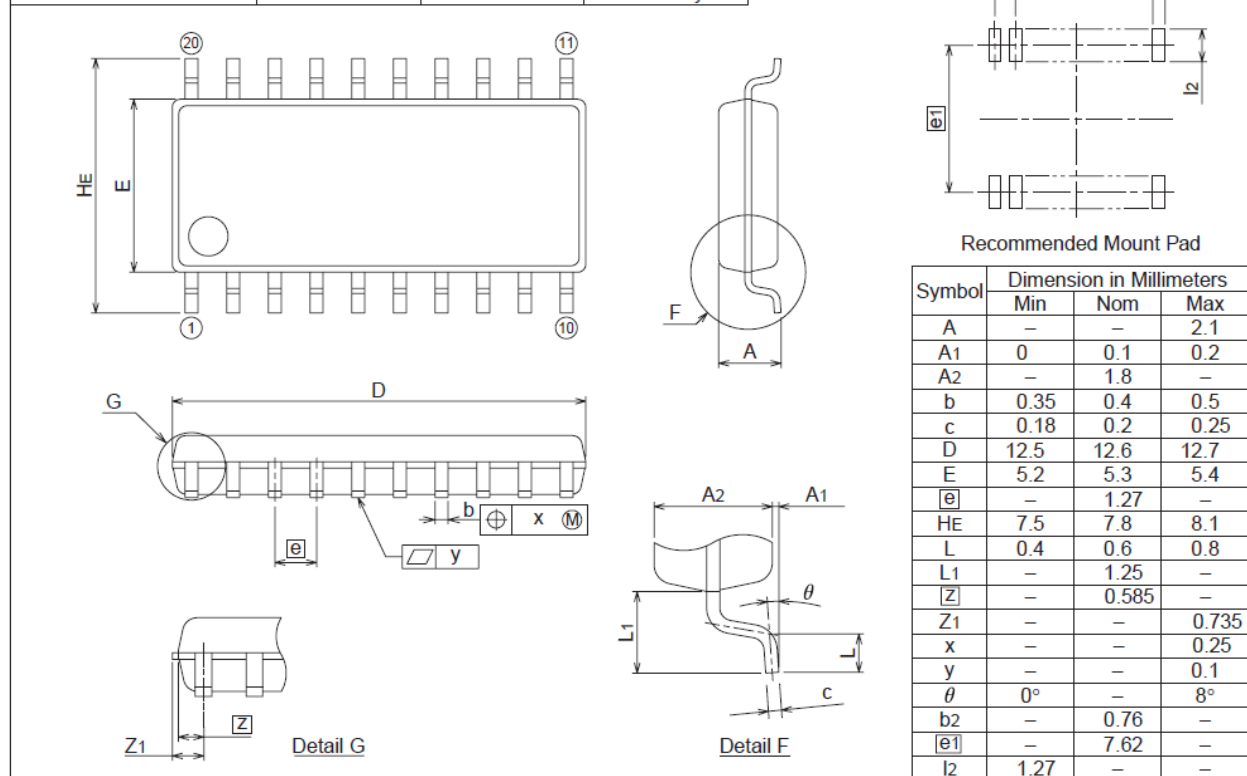
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SOURCE TYPE

PACKAGE OUTLINE

20P2N-A

JEITA Package Code	JEDEC Code	Weight(g)	Lead Material
P-SOP20-5.3x12.6-1.27	—	0.26	Cu Alloy



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