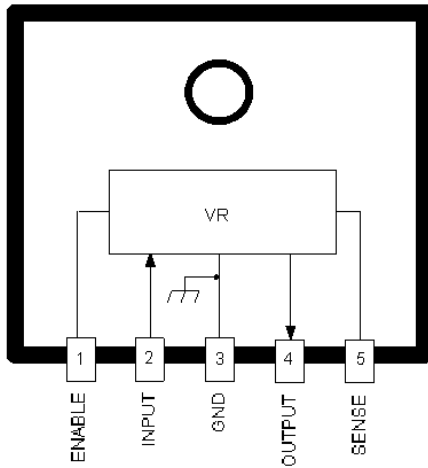


High-Current, Low-Dropout, 3.3 V Regulator



Dwg. PS-022-7



ABSOLUTE MAXIMUM RATINGS

Input Voltage, V_I	10 V
Output Current, I_O	3 A*
Enable Input Voltage, V_E	6 V
Junction Temperature, T_J	+125°C
Storage Temperature Range, T_S	-40°C to +125°C

* Output current rating is limited by input voltage, duty cycle, and ambient temperature. Under any set of conditions, do not exceed a junction temperature of +125°C.

Designed to meet the high-current requirements in industrial and consumer applications; embedded core, memory, or logic supplies; TVs, VCRs, and office equipment, the SI-3033ZF voltage regulator offers the reduced dropout voltage and low quiescent current essential for improved efficiency. This device delivers a regulated output at up to 3 A. Integrated thermal and overcurrent protection enhance overall system reliability. Devices with an adjustable 1.2 ~ 5 V output or a 2.5 V fixed output are also available.

Quiescent current does not increase significantly as the dropout voltage is approached, an ideal feature in standby/resume power systems where data integrity is crucial. Regulator accuracy and excellent temperature characteristics are provided by a bandgap reference. An LS-TTL/CMOS-compatible input gives the designer complete control over power up, standby, or power down. A pnp pass element provides a dropout voltage of less than 700 mV at 3 A of load current. Low output voltages eliminate the need for expensive PWM buck converters. The low dropout voltage permits more efficient regulation before output regulation is lost.

This device is supplied in a fully molded TO-220-style 5-lead flange-mounted, high power, isolated plastic package. A similar device in a lower-power surface-mount plastic package is the SI-3025ZD.

FEATURES

- 3 A Output Current at 3.3 V
- 0.7 V Maximum Dropout Voltage at $I_O = 3$ A
- 1 μ A Maximum Standby Current
- Remote Voltage Sensing
- Foldback Current Limiting
- Thermal Protection

APPLICATIONS

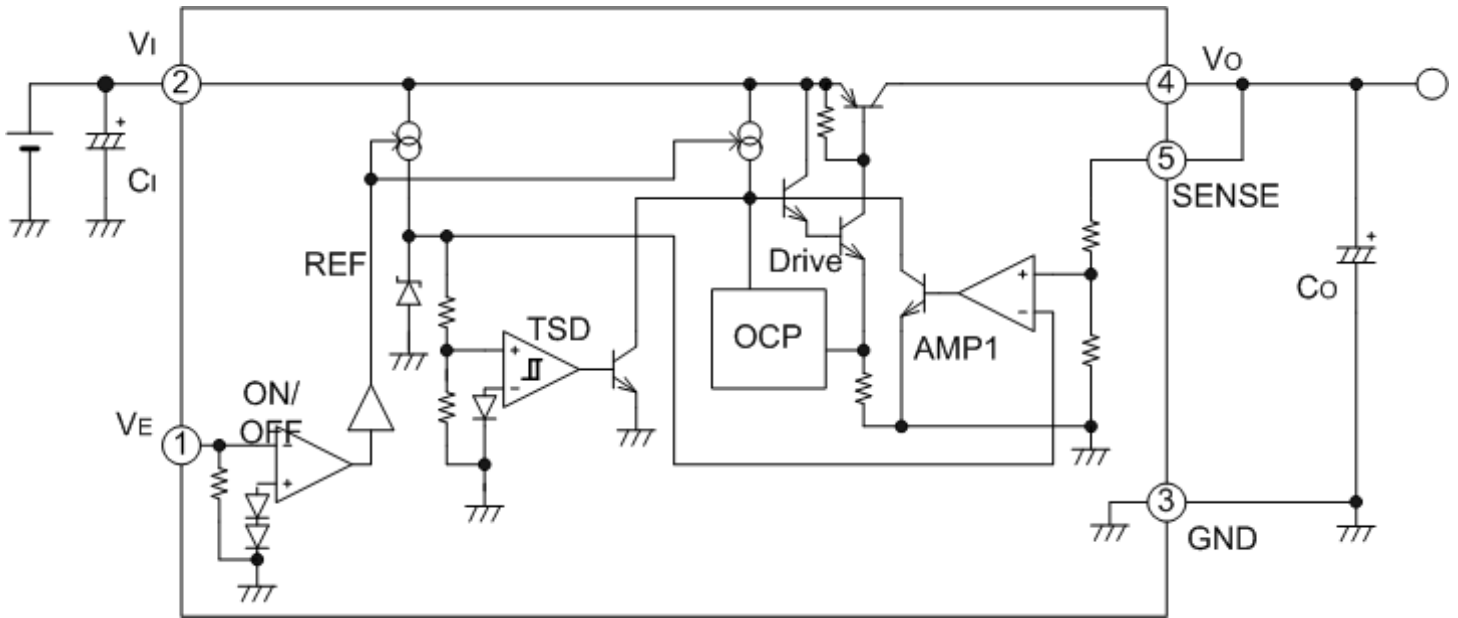
- TVs, VCRs, Electronic Games
- Embedded Core, Memory, or Logic Supplies
- Printers and Other Office Equipment
- Industrial Machinery
- Secondary-Side Stabilization of Multi-Output SMPS

Always order by complete part number, e.g., **SI-3033ZF**.

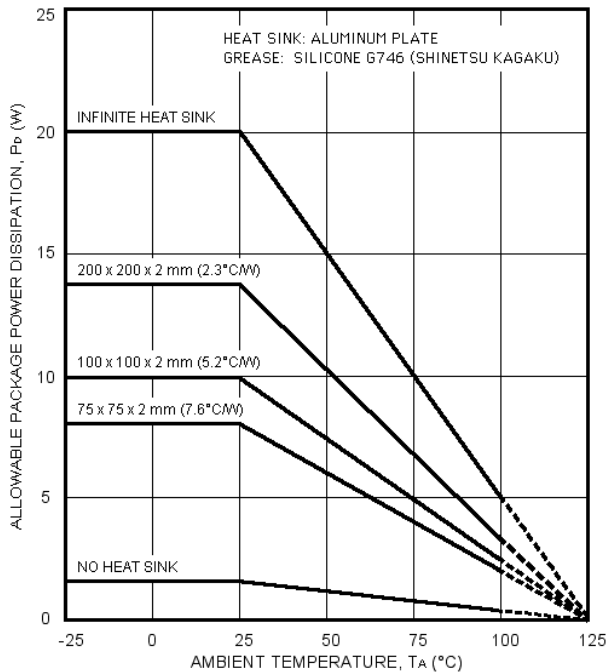
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FUNCTIONAL BLOCK DIAGRAM



Allowable Package Power Dissipation



Recommended Operating Conditions

	Min	Max	Units
DC Input Voltage	—	6	V
DC Output Current	0	3	A
Operating Junction Temp.	-20	+100	°C

For the availability of parts meeting -40°C requirements, contact Allegro's Sales Representative.

This data sheet is based on SanKen data sheet SSJ-02327

ELECTRICAL CHARACTERISTICS at $T_A = +25^\circ\text{C}$, $V_E = 2\text{ V}$ (unless otherwise noted).

Characteristic	Symbol	Test Conditions	Limits			
			Min.	Typ.	Max.	Units
Output Voltage	V_O	$V_I = 5\text{ V}$, $I_O = 10\text{ mA}$	3.234	3.300	3.366	V
	$V_{O(\text{off})}$	$V_E = 0\text{ V}$	—	—	0.5	V
Output Volt. Temp. Coeff.	a_{VO}	$0^\circ\text{C} \leq T_J \leq 100^\circ\text{C}$	—	± 0.3	—	mV/ $^\circ\text{C}$
Output Short-Circuit Current	I_{OM}	$V_I = 5\text{ V}$, see note	3.2	—	—	A
Line Regulation	$\Delta V_{O(\Delta V_I)}$	$V_I = 4.5 \sim 5.5\text{ V}$, $I_O = 10\text{ mA}$	—	—	10	mV
Load Regulation	$\Delta V_{O(\Delta I_O)}$	$V_I = 5\text{ V}$, $I_O = 0\text{ A} \sim 3.0\text{ A}$	—	—	40	mV
Dropout Voltage	$V_{I\text{min}} - V_O$	$I_O = 3.0\text{ A}$	—	—	0.7	V
Ground Terminal Current	I_{GND}	$V_I = 5\text{ V}$, $I_O = 0\text{ mA}$, $V_E = 2.0\text{ V}$	—	1.0	1.5	mA
		$V_I = 5\text{ V}$, $V_E = 0\text{ V}$	—	—	1.0	μA
Enable Input Voltage	V_{EH}	Output ON	2.0	—	—	V
	V_{EL}	Output OFF	—	—	0.8	V
Enable Input Current	I_{EH}	$V_E = 2.7\text{ V}$	—	—	100	μA
	I_{EL}	$V_E = 0\text{ V}$	—	0	-5.0	μA
Ripple Rejection Ratio	PSRR	$V_I = 5\text{ V}$, $100\text{ Hz} \leq f \leq 120\text{ Hz}$	—	60	—	dB
Thermal Shutdown	T_J		135	152	—	$^\circ\text{C}$

Typical values are given for circuit design information only.

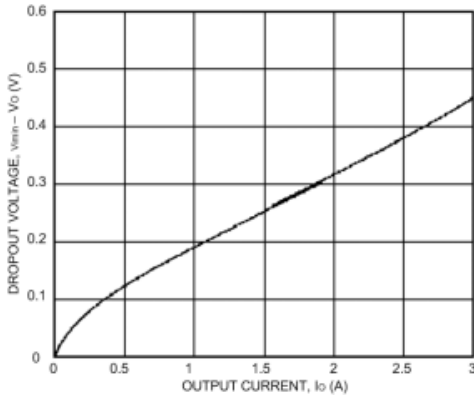
Note: Output short-circuit current is at point where output voltage has decreased 5% below $V_{O(\text{nom})}$.

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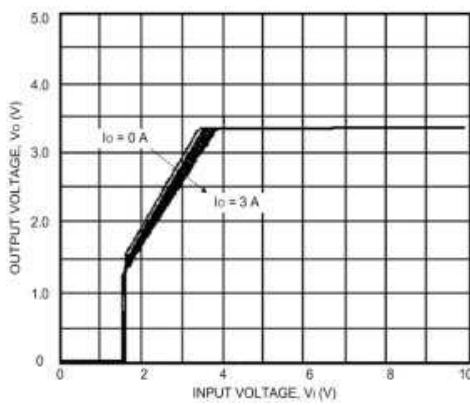
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Regulators*

TYPICAL CHARACTERISTICS
 (T_A = 25°C)

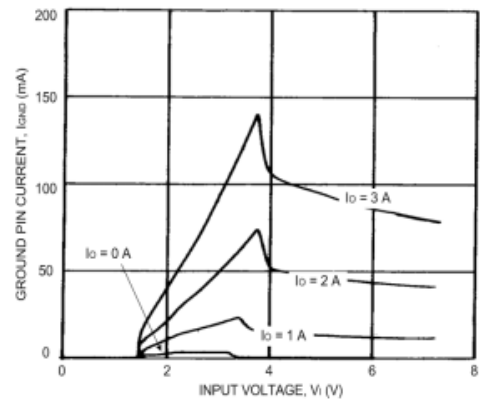
■ Dropout Voltage



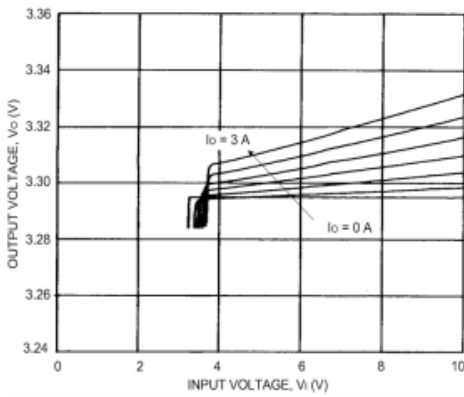
■ Low-Voltage Behavior



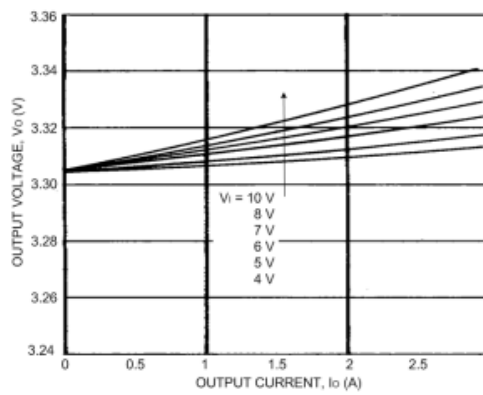
■ GND Pin Current



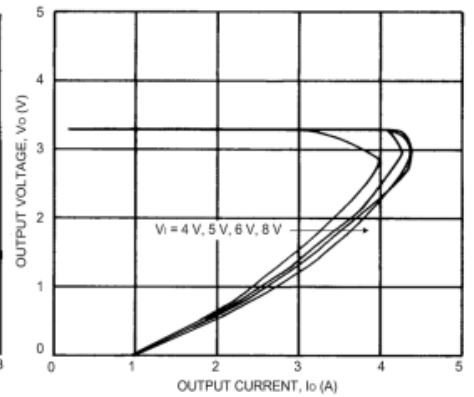
■ Line Regulation



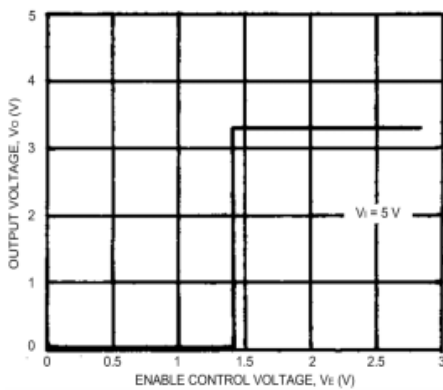
■ Load Regulation



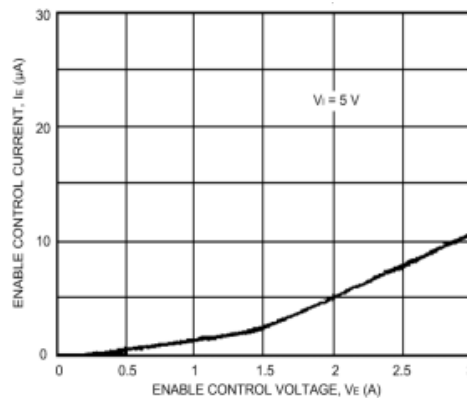
■ Overcurrent Protection



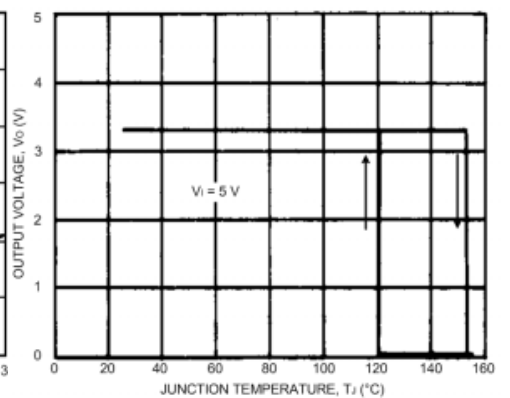
■ ENABLE Control Voltage



■ ENABLE Control Current



■ Thermal Protection



APPLICATIONS INFORMATION

Input Capacitor (C_I , approximately 10 μF). This is necessary either when the input line includes inductance or when the wiring is long.

Output Capacitor (C_O , > 47 μF). This device is not designed for a use with a very low ESR output capacitor such as a ceramic capacitor. Output oscillation may occur with that kind of capacitor.

Determination of DC Input Voltage. The minimum input voltage $V_I(\text{min})$ should be higher than the sum of the fixed output voltage and the maximum rated dropout voltage.

Increased Output Voltage. The output voltage (V_O) may be increased by inserting a resistor (R_{EXT}) between SENSE and OUTPUT. The current flowing into SENSE is typically 90 $\mu\text{A} \pm 30\%$. To minimize the effect of I_{SENSE} and temperature on R_{EXT} , it is recommended that a 9.1 k Ω resistor be added between SENSE and GND to increase the current in R_{EXT} . The value of R_{EXT} is then

$$R_{\text{EXT}} = (V_O - 3.3) \times 10^6 / 453$$

Overcurrent Protection. The SI-3000ZD series has a built-in fold-back type overcurrent protection circuit, which limits the output current at a start-up mode. It thus cannot be used in applications that require current at the start-up mode such as:

- (1) constant-current load,
- (2) power supply with positive and negative outputs to common load (a center-tap type power supply), or
- (3) raising the output voltage by putting a diode or a resistor between the device ground and system ground.

Thermal Protection. Circuitry turns off the pass transistor when the junction temperature rises above 135°C. It is intended only to protect the device from failures due to excessive junction temperatures and should not imply that output short circuits or continuous overloads are permitted.

Heat Radiation and Reliability. The reliability of the IC is directly related to the junction temperature (T_J) in its operation. Accordingly, careful consideration should be given to heat dissipation. The graph on page 2 illustrates the effect of thermal resistance on the allowable package power dissipation.

When mounting to a heat sink, apply silicone grease (Shin-Etsu Chemical G746, Dow Corning Toray Silicone SC102, or Toshiba Silicone SY6260). Recommended mounting hardware torque: 0.588 ~ 0.686 Nm or 6.0 ~ 7.0 kgf•cm (4.34 ~ 5.06 lbf•ft).

The junction temperature (T_J) can be determined from either of the following equations:

$$T_J = (P_D \times R_{\theta JA}) + T_A$$

or

$$T_J = (P_D \times R_{\theta JC}) + T_C$$

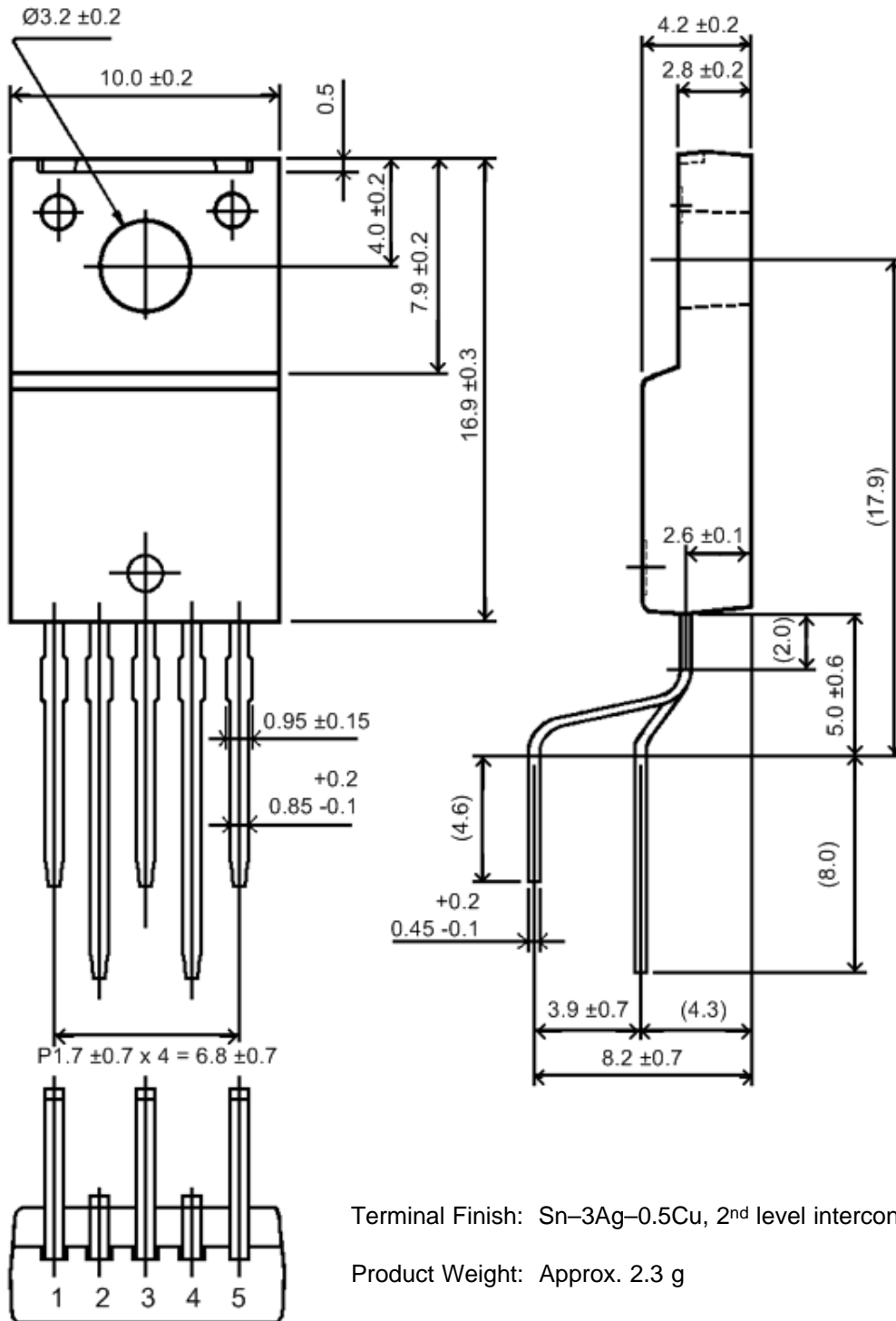
where $P_D = I_O \times (V_I - V_O)$ and
 $R_{\theta JC} = 5^\circ\text{C}/\text{W}$.

Parallel Operation. Parallel operation to increase load current is not permitted.

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Dimensions in Millimeters

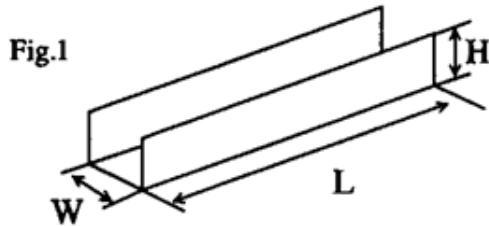


Terminal Finish: Sn-3Ag-0.5Cu, 2nd level interconnect category (e1)

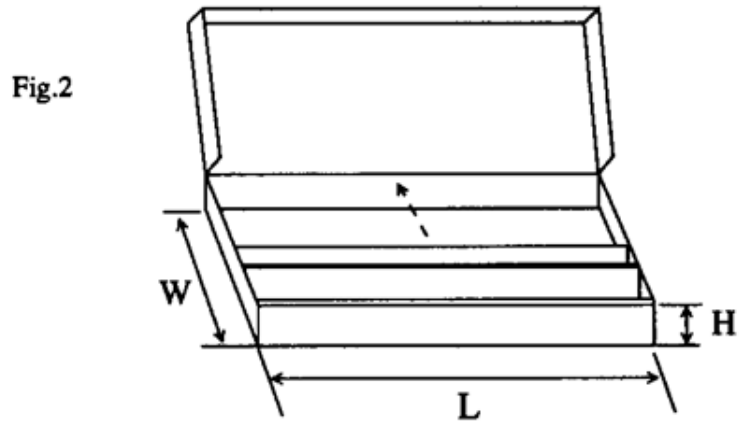
Product Weight: Approx. 2.3 g

Terminal spacing is measured at lead tips.

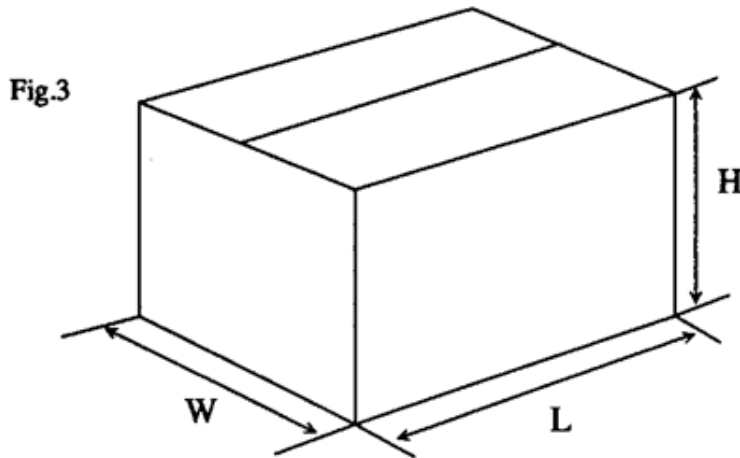
Shipping Container Dimensions in Millimeters



Separator Dimensions (mm)		
W	L	H
22	200	18



Thin Carton Box Dimensions (mm)		
W	L	H
148	210	40



Carton Box Dimensions (mm)		
W	L	H
240	386	170

Primary Packing: A U-shaped cardboard separator (Fig. 1) is used to accommodate a maximum of 100 pieces (5 × 20) and, if less than 100 pieces, a shock absorber shall be placed in the vacant space. Each thin primary carton (Fig. 2) will contain a maximum of 400 pieces in four rows.

Secondary Packing: The secondary carton (Fig. 3) contains nine thin cartons.

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