

NON-ISOLATED DC/DC CONVERTERS

10.8 Vdc - 13.2 Vdc Input, 0.82 Vdc - 1.6 Vdc Output, VRM11.0 Compatible



Sep. 08, 2009

Bel Power, Inc. , a subsidiary of Bel Fuse, Inc.

GRND-90A161

RoHS Compliant

Rev.A

Features

- Non-Isolated
- High Efficiency
- Fixed Frequency
- Wide Input
- Remote On/Off
- Class 1, Category 2, Non-Isolated DC/DC Converter (refer to IPC-9592)
- TUV EN60950-1 Recognized (Pending)
- UL60950-1 Recognized (UL/cUL) (Pending)
- Input Under-Voltage Lockout
- OCP/SCP
- 2-Wire Remote sense
- 7 bit VID Digital Voltage Programming
- I2C/SMBus Available – Consult Factory



Applications

- Networking
- Computers and peripherals
- Telecommunications

Description

The GRND-90A Series are non-isolated step down dc/dc converters providing up to 90 A of output current and designed to be compatible with the Intel VRM11 requirements. Standard features include remote on/off, over current protection, remote sense, VR_Hot signal and a power good signal. This product also makes use of adaptive positioning to improve transient response performance. These products may be used almost anywhere low-voltage silicon is being employed and a nominal 12 Vdc source is available. Typical applications include file servers, work stations and other computing applications.

Part Selection

Output Voltage	Input Voltage	Max. Output Current	Max. Output Power	Typical Efficiency	Model Number Active High
0.82 Vdc - 1.6 Vdc	10.8 Vdc - 13.2 Vdc	90 A	144 W	83%	GRND-90A161

Notes: Add "G" suffix at the end of the model number to indicate Tray Packaging.

Part Number Explanation

G R ND - 90 A 16 1
1 2 3 4 5 6 7

- 1---Goldfinger
- 2---RoHS 6, change "R" to "7" means RoHS 5
- 3---Series name
- 4---Series code
- 5---Wide input range (10.8-13.2V)
- 6---Wide output range (0.82-1.6V)
- 7---Enable, active high

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Absolute Maximum Ratings

Parameter	Min	Typ	Max	Unit	Notes
Continuous non-operating Input Voltage	-0.3	-	15	V	
Remote On/Off	-0.3	-	5.5	V	
Ambient Temperature	0	-	70	°C	
Storage Temperature	-55	-	125	°C	

Note: Ratings used beyond the maximum ratings may cause a reliability degradation of the converter or may permanently damage the device.

Input Specifications

Parameter	Min	Typ	Max	Unit	Notes
Operating Input Voltage	10.8	12	13.2	V	
Input Current (full load)	-	-	16	A	
Input Current (no load)	-	-	700	mA	
Remote Off Input Current	-	17	-	mA	
Input Reflected Ripple Current (rms)	-	50	100	mA	With simulated source impedance of 200nH, 5Hz to 20MHz. Use 2 * 220uF/16V Oscon capacitor
Input Reflected Ripple Current (pk-pk)	-	150	200	mA	
I ² t Inrush Current Transient	-	-	1	A ² s	
Turn-on Voltage Threshold	9.5	10	10.5	V	
Turn-off Voltage Threshold	8.4	8.9	9.4	V	

Note: All specifications are typical at 25 °C unless otherwise stated.

Output Specifications

Parameter	Min	Typ	Max	Unit	Notes
Output Voltage Set Point	1.27	1.285	1.3	V	No load, excluding adaptive positioning, VRM11 VID 0x32
Adaptive Positioning	-	1.25	-	mOhm	Droop Impedance
Line Regulation	-	±5	±10	mV	
Regulation Over Temperature (0deg.C-70deg.C)	-	±5	±10	mV	
Ripple and Noise (pk-pk)	-	-	25	mV	0-20 MHz BW
Ripple and Noise (rms)	-	-	10	mV	
Ripple and Noise (pk-pk) under worst case	-	-	30	mV	over all operating input voltage, load and temperature conditions.
Output Current Range	0	-	70	A	Thermal Design Current
	-	-	90	A	Peak Current Rating
Output DC Current Limit	95	-	110	A	

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Output Specifications (continued)

Parameter	Min	Typ	Max	Unit	Notes
Short Circuit Surge Transient	-	-	5	A ² s	
Turn on Time	-	3.5	5	mS	
Overshoot at Turn on	-	-	1	%	
Output Capacitance	See Note 1	3710	See Note 1	uF	Consult factory regarding external capacitance outside of this range
Transient Response					
△V50%~100% of Max Load	Overshoot	-	-	50	mV
	Settling Time	-	-	25	uS
△V100%~50% of Max Load	Overshoot	-	-	50	mV
	Settling Time	-	-	25	uS

di/dt=300A/us, Vin=12Vdc, Ta=25°C

Notes: 1. Measured with 7*470uF/7mOhm ESR SP-CAP, and 42*10uF/1206 ceramic capacitors on output.
2. All specifications are typical at nominal input, full load at 25°C unless otherwise stated.

General Specifications

Parameter	Min	Typ	Max	Unit	Notes
Efficiency	81	83	-	%	Vin =12V, Iout=70A; VID=0x32
Switching Frequency	-	400	-	kHz	
Over Temperature Alert	100	-	110	°C	
Over Voltage Protection	-	Vo,set + 0.175	-	V	
Weight	-	46	-	g	
FIT	500			-	Calculated Per Bell Core SR-332 (Vin=12 V, Io=70A, Ta = 25 °C, FIT=10 ⁹ /MTBF)
Dimensions Inches (L × W × H) Millimeters (L × W × H)	3.80 x 1.06 x 0.77 96.52 x 26.80 x 19.46			-	

Note: All specifications are typical at 25 °C unless otherwise stated.

Remote On/Off

Parameter	Min	Typ	Max	Unit	Notes
Signal Low (Unit Off)	Active High -0.3 0.8	-	0.4	V	The remote on/off pin open, Unit on.
Signal High (Unit On)		-	5.5	V	
Current Sink	0	-	1	mA	

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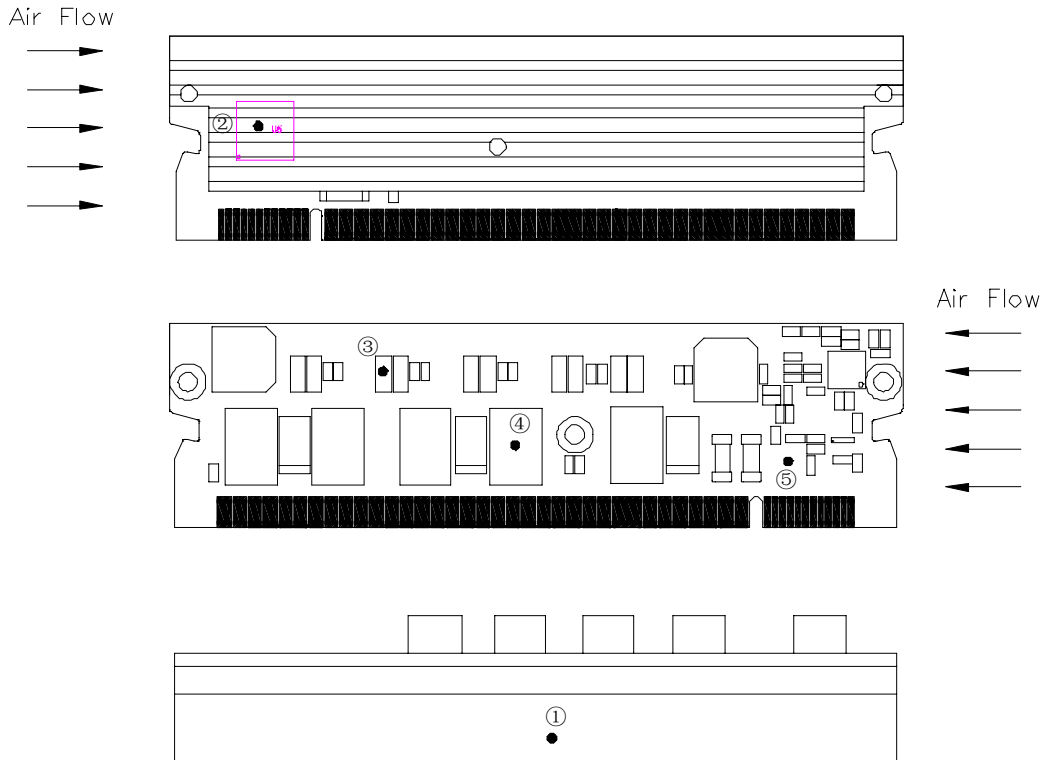
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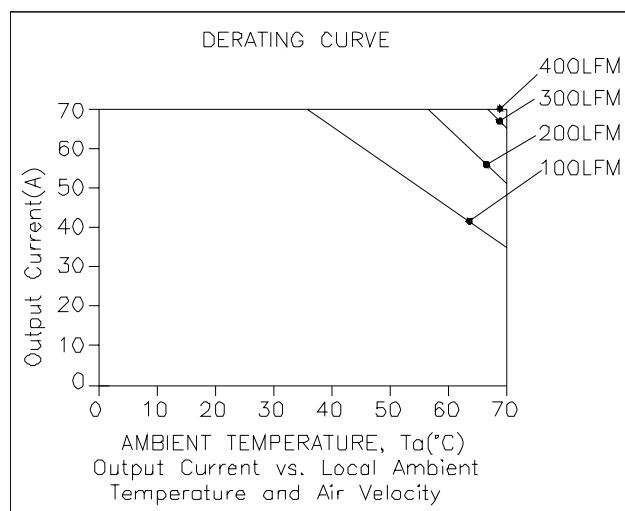
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Thermal Derating Curves



The thermal reference point Tref is shown above. For reliable operation this temperature should not exceed 110°C. The output power of the module should not exceed the rated power for the module.



Vin=12V, VID=0x32

Maximum junction temperature of semiconductors derated to 120 degree C.

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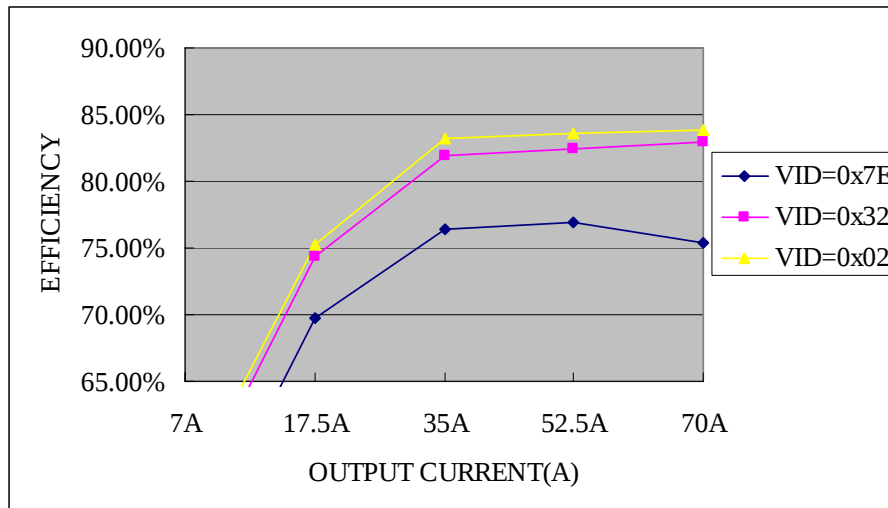
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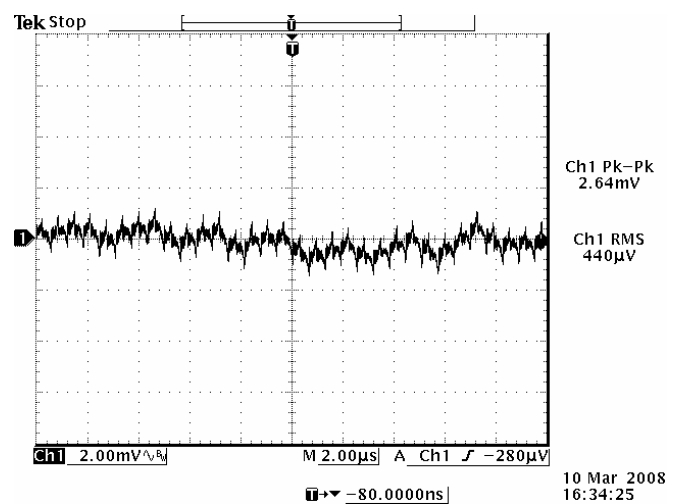
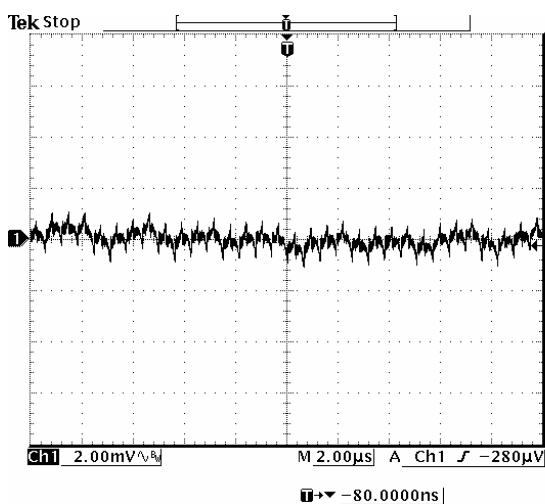
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Efficiency Data



Ripple and Noise Waveforms



Ripple and noise at 12Vdc input, VID=0x32, Iout=0A

Ripple and noise at 12Vdc input, VID=0x32, Iout=70A

Note: Ripple and noise at 0-20MHz BW, and Ta=25 deg C.

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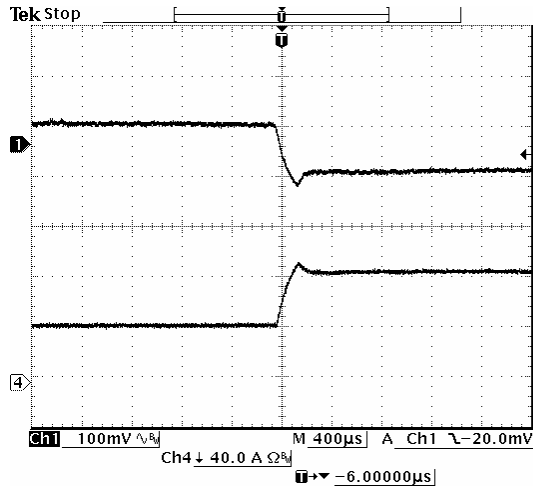
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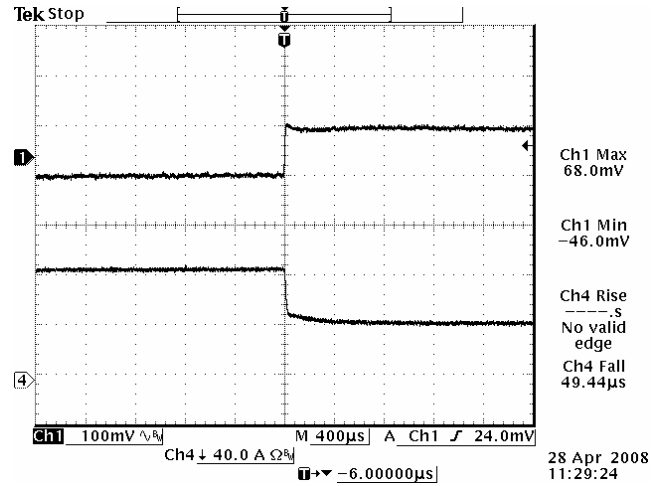
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Transient Response Waveforms



VID=0x32 45A-90A Load Transients

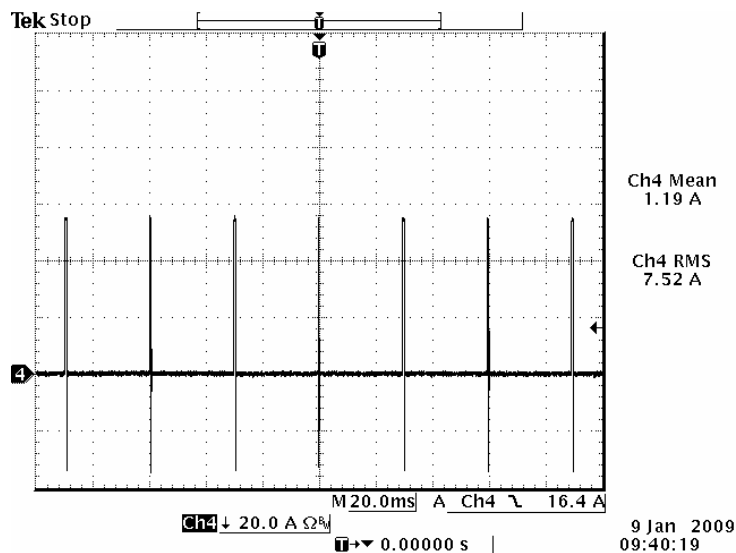


VID=0x32 90A-45A Load Transients

Note: Transient Response at $V_{in}=48V$, $di/dt=5A/\mu S$, with typical cap bank on output, and $T_a=25\text{ deg C}$.

Over Current Protection

To provide protection in a fault output overload condition, the module is equipped with internal current-limiting circuitry and can endure current limiting for a few milli-seconds. If the overcurrent condition persists beyond a few milliseconds, the module will shut down into hiccup mode. The module operates normally when the output current goes into specified range.



$V_{in}=12V$, VID=0x32

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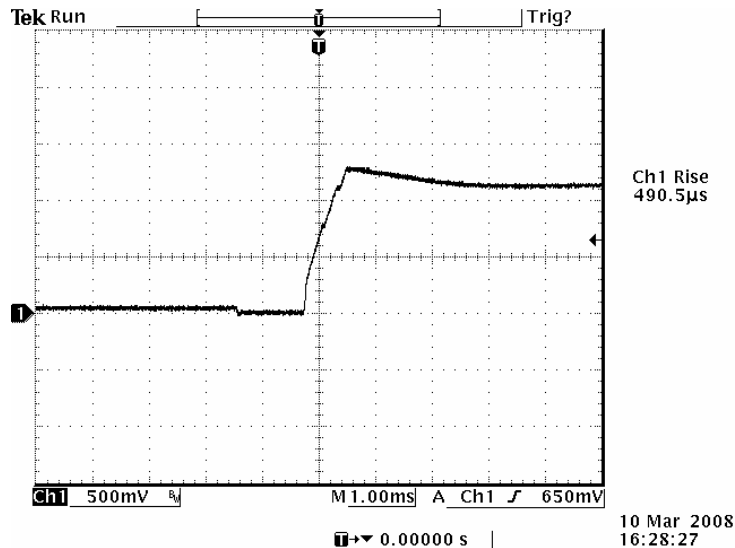


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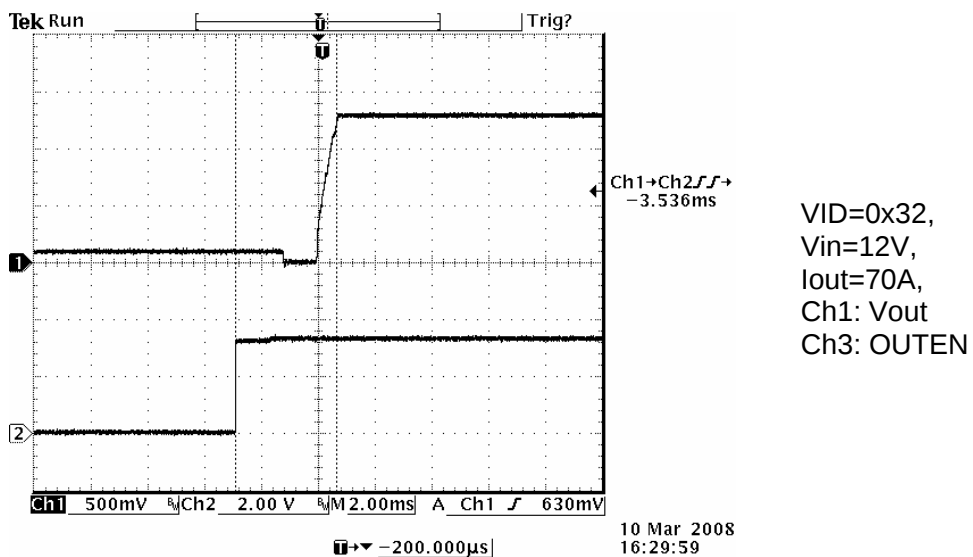
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Startup & Shutdown

Rise time



Startup time



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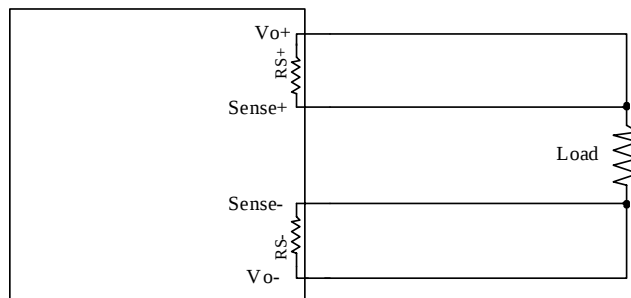
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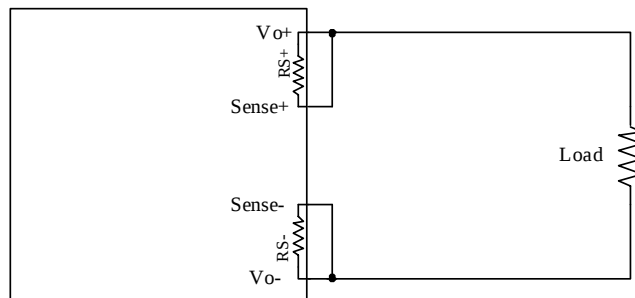
Remote Sense

This module has remote sense compensation feature. It can minimize the effects of resistance between module's output and load in system layout and facilitates accurate voltage regulation at load terminals or other selected point.

1. The remote sense lines carries very little current and hence do not require a large cross-sectional area.
2. This module compensates for a maximum drop of 10% of the nominal output voltage.
3. When using remote sense compensation, all the resistance, parasitic inductance and capacitance of the system are incorporated within the feedback loop of this module. It can make an effect on the module's compensation, affecting the stability and dynamic response. A 0.1uF ceramic capacitor can be connected at the point of load to de-couple noise on the sense wires.
4. Recommend the connection of remote sense compensation as below figure. There are a resistor RS+ (51.1 ohm) from Vo+ to Sense+ and a resistor RS- (51.1 ohm) from Vo- to Sense- inside of this module.



5. If not using remote sense compensation, please connect sense directly to output at module's pin, that is, connect sense+ to Vo+ and sense- to Vo- at module's pin, the shorter the better. See below figure.



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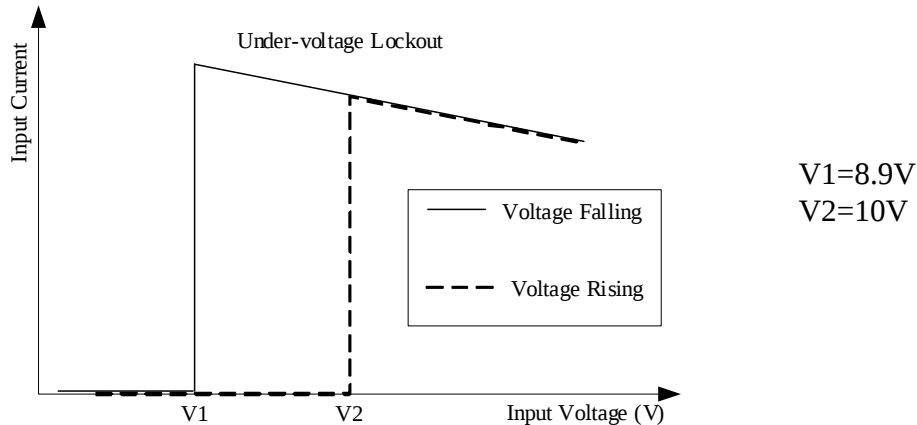
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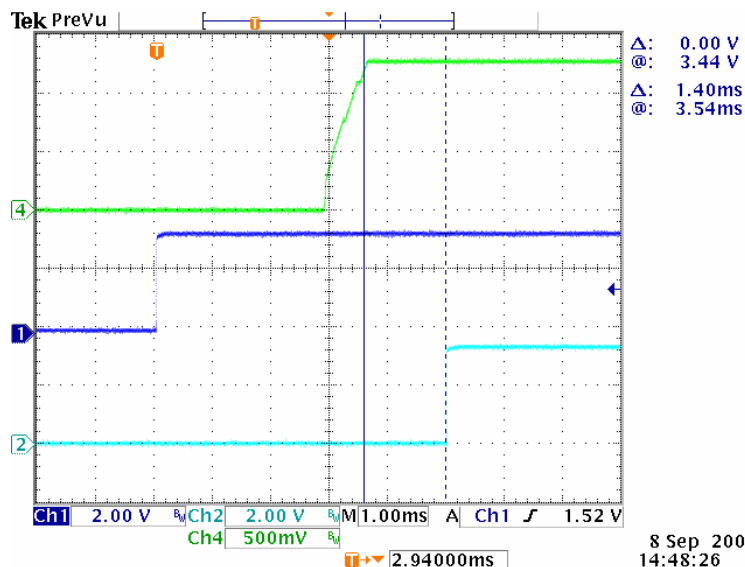
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Input Under-voltage Lockout



Power Good

1. This module has a power good indicator output. Power good pin used positive logic and is open collector.
2. Power good pin can sink 1mA.
3. The maximum voltage pulled up externally on Power Good pin should not exceed 5V.
4. When the output is within 10% of the VID setting, the power good pin will be pulled high.



Typical Start-up Using Remote ON/OFF($V_{in}=12.0V$, $VID=0x32$, $I_o=0A$)

Ch1: OUTEN

Ch2: PWRGD

Ch4: VOUT

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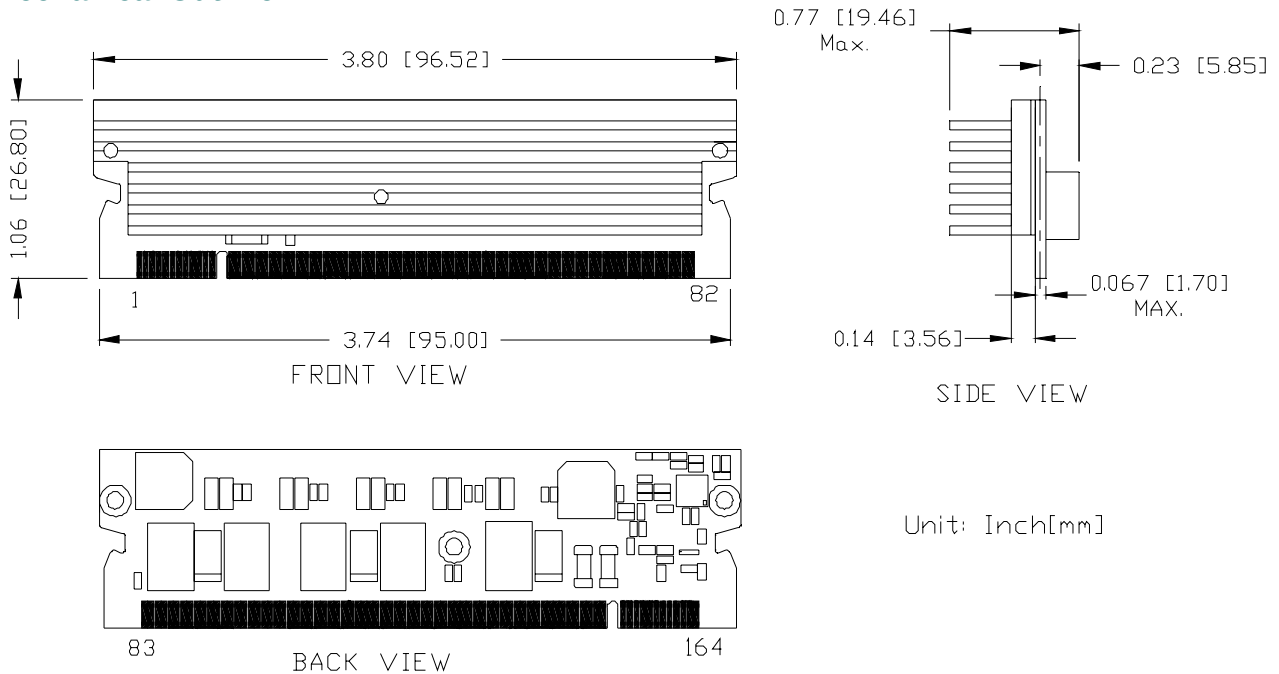
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Mechanical Outline



Row A				Row B			
Pin	Function	Pin	Function	Pin	Function	Pin	Function
1	PWRGD	25-26	GND	139-140	GND	164	VBIAS
2	SC	27-30	VOUT	135-138	VOUT	163	SD
3	OUTEN	31-34	GND	131-134	GND	162	LL0
4	LL1	35-38	VOUT	127-130	VOUT	161	VID6
5	VID5	39-42	GND	123-126	GND	160	VID4
6	VID3	43-46	VOUT	119-122	VOUT	159	VID2
7	VID1	47-48	GND	117-118	GND	158	VID0
8	Reserved	49-52	VOUT	113-116	VOUT	157	Reserved
9	+SENSE	53-56	GND	109-112	GND	156	-SENSE
10	SP	57-60	VOUT	105-108	VOUT	155	VIDSEL
11	VR_HOT	61-64	GND	101-104	GND	154	VRM_PRES
12	Reserved	65-68	VOUT	97-100	VOUT	153	BUSS ADD
-	Key	69-70	GND	95-96	GND	-	Key
13-16	VIN	71-74	VOUT	91-94	VOUT	149-152	VIN
17-20	GND	75-78	GND	87-90	GND	145-148	GND
21-24	VOUT	79-82	VOUT	83-86	VOUT	141-144	VOUT

Note: This module is recommended and compatible with Pb-Free Wave Soldering and must be soldered using a peak solder temperature of no more than 260 °C for less than 5 seconds.

Note:

- 1) Undimensioned components are shown for visual reference only.
- 2) All dimensions in inches (mm); Tolerances: x.xx +/-0.02 in. (x.x +/-0.5mm) x.xxx +/-0.010 in. (x.xx +/-0.25mm).

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Signal Definitions

Vin	Input power to the converter
GND	Common return for both input and output
VID0, VID1, VID2, VID3, VID4, VID5, VID6	Logic level inputs used to set the output voltage, refer to VID table. Connect VID0 thru VID6 pins to open-drain outputs with external pull-up resistors. Valid logic low is -0.3V to 0.4V, valid logic high level is 0.8V to 5.5V.
VIDSEL	Digital input used to select between extended VR10 and VR11 VID tables. Internally pulled up to 3.3V to select VRM11.
PWRGD	The open drain power good signal indicates the output voltage is within 10% of the VID setpoint.
Vsense+, Vsense-	Remote voltage sense lines. Connect these at the point of load, to VOUT and GND respectively.
VOUT	Output voltage available to the load.
OUTEN	Logic level input used to enable the converter when high. Valid logic low is -0.3V to 0.4V, valid logic high level is 0.8V to 5.5V
LL0/LL1	Load line select pins. Working in conjunction with VID_SEL, LL0/LL1 are used to select load line slope. Currently only 1.25mOhm slope is supported. Module can be configured to support multiple load line slopes.
SC SD	Optional serial clock and data pins respectively for SMBus interface.
SP	Optional programming pin for set point and over current protection mode.
VR_HOT#	Open-drain output signal, pulled actively low indicating a thermal event has been detected on the VRM.
VRM_PRES	Signal used by system to indicate the VRM is present in the design.
BUSS_ADD	Optional addressing pin when using SMBus feature.

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VID CODES (VRM11.0 VID CODES)

HEX (VID6 - VID0)	Vout (VDC)
0	0
0	1
0	2
0	3
0	4
0	5
0	6
0	7
0	8
0	9
0	A
0	B
0	C
0	D
0	E
0	F
1	0
1	1
1	2
1	3
1	4
1	5
1	6
1	7
1	8
1	9
1	A
1	B
1	C
1	D
1	E
1	F
2	0
2	1
2	2
2	3
2	4
2	5
2	6
2	7
2	8
2	9
2	A
2	B
2	C
2	D
2	E
2	F

HEX (VID7 - VID0)	Vout (VDC)
3	0
3	1
3	2
3	3
3	4
3	5
3	6
3	7
3	8
3	9
3	A
3	B
3	C
3	D
3	E
3	F
4	0
4	1
4	2
4	3
4	4
4	5
4	6
4	7
4	8
4	9
4	A
4	B
4	C
4	D
4	E
4	F
5	0
5	1
5	2
5	3
5	4
5	5
5	6
5	7
5	8
5	9
5	A
5	B
5	C
5	D
5	E
5	F

HEX (VID7 - VID0)	Vout (VDC)
6	0
6	1
6	2
6	3
6	4
6	5
6	6
6	7
6	8
6	9
6	A
6	B
6	C
6	D
6	E
6	F
7	0
7	1
7	2
7	3
7	4
7	5
7	6
7	7
7	8
7	9
7	A
7	B
7	C
7	D
7	E
7	F

Note: VID7=1 not shown because this VRM does not have a VID7 pin.

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Revision History

Date	Revision	Changes Detail	Approval
2009-9-8	A	First release	Jack Fan

RoHS Compliance

Complies with the European Directive 2002/95/EC, calling for the elimination of lead and other hazardous substances from electronic products.



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