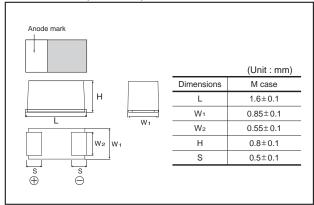


Chip tantalum capacitors (Bottom surface electrode type : Large capacitance)

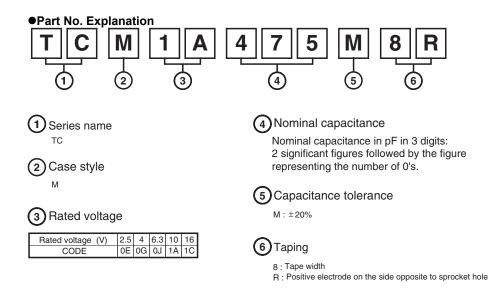
TC Series M Case

•Features (M)

- With an original bottom surface electrode structure.
- 1) Excellent adhesion.
- 2) Easy visual recognition of fillets.
- 3) Large capacitance, low ESR.



•Dimensions (Unit : mm)



Rated table

			Rate	d voltag	e (V)		
(μF)	2.5 0E	4 0G	6.3 0J	10 1A	16 1C	20 1D	25 1E
1.0 (105)				М	М		M*
1.5 (155)							
2.2 (225)				М	М	Μ*	
3.3 (335)							
4.7 (475)			М	М	Μ*		
6.8 (685)							
10 (106)		М	М	М			
15 (156)							
22 (226)		М	М				
33 (336)		М	М				
47 (476)	М						

Remark) Case size codes (M) in the above show products line-up.

* Under development

Marking

The indications listed below should be given on the surface of a capacitor.

(1) Polarity : The polarity should be shown by
bar. (on the anode side)

(1) For the polarity should be shown by a ball (of the anode side)
(2) Rated DC voltage : Due to the small size of M case, a voltage code is used as shown below.
(3) Visual typical example (1) voltage code (2) capacitance code

Voltage Code	Rated DC Voltage (V)	Ca
е	2.5	
g	4	
j	6.3	
А	10	
С	16	
D	20	
Е	25	

Capacitance Code	Nominal Capacitance (µF)			
А	1.0			
E	1.5			
J	2.2			
Ν	3.3			
S	4.7			
W	6.8			
а	10			
е	15			
j	22			
n	33			
S	47			

[M case] note 1)

$$\frac{A}{(1)}$$
 $\frac{s}{(2)}$



note 2) voltage code and capacitance code are variable with parts number

• Characteristics

Iter	Item Performance				Perfor	man	Test conditions (based on JIS C 5101-1 and JIS C 5101-							
Operating Tem	perature	-5	5°C	C to -	+125	°C)			Voltage reduction when temperature exceeds $+85^{\circ}C$				
Maximum operat temperature with derating	ing no voltage	+85°C												
Rated voltage (VDC)	2.5	4	6.3	10	1	6 20	25		at 85°C				
Category voltag	e (VDC)	1.6	2.5	5 4	6.3	1	0 13	16		at 125°C				
Surge voltage (VDC)	3.2	5.2	2 8	13	2	0 26	32		at 85°C				
DC Leakage cu	rrent			be sa Idard			the v	olta	ge on	As per 4.9 JIS C 5101-1 As per 4.5.1 JIS C 5101-3 Voltage : Rated voltage for 5min				
Capacitance tol	erance		all 0%		atisfi	ed	allow	anc	e range.	As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5.DC Measuring circuit : DC Equivalent series circuit				
Tangent of loss angle (Df, tan δ)				be sa Idard			the v	olta	ge on	As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5.DC Measuring circuit : DC Equivalent series circuit				
Impedance			Shall be satisfied the voltage on " Standard list "						ge on	As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency : 100±10kHz Measuring voltage : 0.5Vrms or less Measuring circuit : DC Equivalent series circuit				
Resistance to Soldering heat	Appearance						no sig should		cant abnormality. clear.	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3 Dip in the solder bath				
	L.C.	Le	ss	than	200	%	of init	ial li	mit	Dip in the solder bath Solder temp : 260±5°C				
	ΔC / C	TCM0J336□: Within ±30% of initial value. Others : Within ±20% of initial value.								Duration : 5±0.5s Repetition : 1 After the specimens, leave it at room temperature for				
	Df (tan δ)	Le	SS	than	200	%	of init	ial li	mit	over 24h and then measure the sample.				
Temperature cycle	Appearance						no sig		cant abnormality.	As per 4.16 JIS C 5101-1 As per 4.10 JIS C 5101-3				
	L.C.	Le	ss	than	200	%	of init	ial li	mit	Repetition : 5 cycles (1 cycle : steps 1 to 4) without discontinuation.				
	ΔC / C	TCM0J336 □: Within ±30% of initial value. Others : Within ±20% of initial value.							Temp. Time 1 -55±3°C 30±3min.					
Df (tan δ) Less than 200% of initial limit			2 Room temp. 3min.or less 3 125±2°C 30±3min. 4 Room temp. 3min.or less After the specimens, leave it at room temperature for over 24h and then measure the sample.											
Moisture resistance	Appearance		There should be no significant abnormality. The indications should be clear.					As per 4.22 JIS C 5101-1 As per 4.12 JIS C 5101-3						
	L.C.	Le	ss	than	200	%	of init	ial li	mit	After leaving the sample under such atmospheric condition that the temperature and humidity are				
	ΔC / C	тс							of initial value.	60±2°C and 90 to 95% RH,respectively, for 500±12h leave it at room				
	Df (tan δ)	Others : Within ±20% of initial value. Less than 200% of initial limit					of init	ial li	mit	temperature for over 24h and then measure the sample.				

Iter	n	Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)					
Temperature Stability	Temp.	–55°C	As per 4.29 JIS C 5101-1 As per 4.13 JIS C 5101-3					
Stability	ΔC / C	TCM0G336 : Within 0/-30% of initial value TCM0J226 : Within 0/-30% of initial value TCM0J336 : Within 0/-30% of initial value Others : Within 0/-15% of initial value						
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "						
	L.C.	_						
	Temp.	+85°C						
	ΔC / C	TCM0G336 : Within +15/-5% of initial value TCM0J226 : Within $\pm 15/-5\%$ of initial value TCM0J336 : Within $\pm 15/-5\%$ of initial value Others : Within $\pm 15/-5\%$ of initial value						
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "						
	L.C.	Less than 1000% of initial value						
	Temp.	+125°C						
	∆C / C	TCM0G336□ : Within +20/-5% of initial value Others : Within +20/0% of initial value						
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "						
	L.C.	Less than 1000% of initial value						
Surge voltage	Appearance	There should be no significant abnormality.	As per 4.26JIS C 5101-1					
	L.C.	Less than 200% of initial limit	 As per 4.14JIS C 5101-3 Apply the specified surge voltage every 5±0.5 min. for 30±5 s. each time in the atmospheric condition of 85±2°C Repeat this procedure 1,000 times. After the specimens, leave it at room temperature for over 24h and then measure the sample. 					
	ΔC / C	Within ±20% of initial value						
	Df (tan δ)	Less than 200% of initial limit						
Loading at	Appearance	There should be no significant abnormality.	As per 4.23 JIS C 5101-1					
High temperature	L.C.	Less than 200% of initial limit	As per 4.15 JIS C 5101-3 After applying the rated voltage for 1000+36/0 h without					
	ΔC / C	TCM0G336 □: Within ±30% of initial value TCM0J226 □: Within ±30% of initial value TCM0J336 □: Within ±30% of initial value Others : Within ±20% of initial value	discontinuation via the serial resistance of 3Ω or less at a temperature of $85\pm2^{\circ}$ C, leave the sample at room temperature / humidity for over 24h and measure the value.					
	Df (tan δ)	Less than 200% of initial limit	-					
Terminal	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1					
strength	Appearance	There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s. (See the figure below) 50^{20} F (Apply force) R230 thickness=1.6mm 45^{45} 45					

lt	em	Performance	Test conditions (JIS C 5101–1 and JIS C 5101–3)		
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board		
Dimensions		Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.		
Resistance to solvents		The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.		
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed= 25 ± 2.5 mm / s Pre-treatment(accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp. : $245\pm5^{\circ}$ C Duration : 3 ± 0.5 s Solder : M705 Flux : Rosin 25% IPA 75%		
Vibration	ration Capacitance Measure value should not fluctuate during the measurement.		As per 4.17 JIS C 5101-1 Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm		
	Appearance	There should be no significant abnormality.	Time : 2h each in X and Y directions Mounting : The terminal is soldered on a print circuit board		

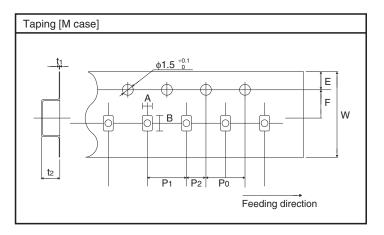
• Standard products list, TC series M case

Part No.	Rated voltage 85°C	Category voltage 125°C	Surge voltage 85°C	Cap. 120Hz	Tolerance	Leakage current 25°C		Df 120Hz (%)		Impedance 100kHz
	(V)	(V)	(V)	(μF)	(%)	1WV.300s (μA)	–55°C	25°C 85°C	125°C	(Ω)
TC M 0E 476 🗆	2.5	1.6	3.2	4.7	±20	0.5	30	20	30	9.0
TC M 0G 106□	4	2.5	5.2	10	±20	0.5	30	20	30	9.0
TC M 0G 226□	4	2.5	5.2	22	±20	0.9	30	20	30	9.0
TC M 0G 336□	4	2.5	5.2	33	+20	13.0	60	30	40	9.0
TC M 0J 475□	6.3	4	8	4.7	±20	0.5	30	20	30	9.0
TC M 0J 106□	6.3	4	8	10	±20	0.6	30	20	30	9.0
TC M 0J 226□	6.3	4	8	22	±20	13.0	60	30	40	9.0
TC M 0J 336□	6.3	4	8	33	±20	208	60	30	40	9.0
TC M 1A 105□	10	6.3	13	1.0	±20	0.5	15	10	15	15.0
TC M 1A 225□	10	6.3	13	2.2	±20	0.5	30	20	30	13.5
TC M 1A 475□	10	6.3	13	4.7	±20	0.5	30	20	30	9.0
TC M 1A 106□	10	6.3	13	10	±20	10.0	30	20	30	9.0
TC M 1C 105□	16	10	20	1.0	±20	0.5	15	10	15	15.0
TC M 1C 225□	16	10	20	2.2	±20	0.5	30	20	30	13.5

 \Box =Tolerance (M : ±20%)

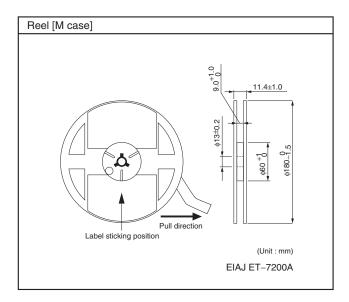
Packaging specifications

• I dond	99	opo	00						(Ui	nit : mm)
Case code	A±0.1	B±0.1	W±0.2	E±0.1	F±0.05	P1±0.1	P2±0.05	Po±0.1	t1±0.05	t2±0.1
М	1.0	1.8	8.0	1.75	3.5	4.0	2.0	4.0	0.20	1.0



• Packaging style

Case code	Packaging	Packag	ging style	Symbol	Basic ordering units
M case	Taping	plastic taping	φ180mm Reel	R	4,000pcs



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