

# 0.9V Drive Nch MOSFET

## RYM002N05

#### Structure

Silicon N-channel MOSFET

#### Features

- 1) High speed switing.
- 2) Small package(VMT3).
- 3) Ultra low voltage drive(0.9V drive).

## Application

Switching

## Packaging specifications

Type	Package	Taping	
	Code	T2L	
	Basic ordering unit (pieces)	8000	
RYM002N0	0		

## ● Absolute maximum ratings (Ta = 25°C)

Param	Symbol	Limits	Unit	
Drain-source voltage		$V_{DSS}$	50	V
Gate-source voltage		$V_{GSS}$	±8	V
Drain current	Continuous	$I_D$	±200	mA
	Pulsed	I <sub>DP</sub> *1	±800	mA
Source current	Continuous	I <sub>S</sub>	125	mA
(Body Diode)	Pulsed	I <sub>SP</sub> *1	800	mA
Power dissipation		P <sub>D</sub> *2	150	mW
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

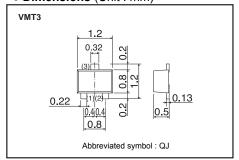
<sup>\*1</sup> Pw≤10µs, Duty cycle≤1%

#### • Thermal resistance

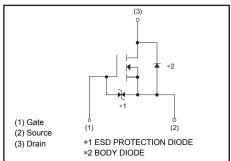
Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	833	°C/W

<sup>\*</sup> Each terminal mounted on a recommended land.

## Dimensions (Unit : mm)



#### • Inner circuit



<sup>\*2</sup> Each terminal mounted on a recommended land.

## ● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I <sub>GSS</sub>		-	±10	μA	$V_{GS}=\pm 8V, V_{DS}=0V$
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	50	-	-	V	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	1	-	1	μA	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	0.3	-	0.8	V	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA
		1	1.6	2.2	Ω	I <sub>D</sub> =200mA, V <sub>GS</sub> =4.5V
Otatia duain assuma an atata		1	1.7	2.4		I <sub>D</sub> =200mA, V <sub>GS</sub> =2.5V
Static drain-source on-state resistance	R <sub>DS (on)</sub>	-	2.0	2.8		I <sub>D</sub> =200mA, V <sub>GS</sub> =1.5V
recicianos		1	2.2	3.3		I <sub>D</sub> =100mA, V <sub>GS</sub> =1.2V
		-	3.0	9.0		I <sub>D</sub> =10mA, V <sub>GS</sub> =0.9V
Forward transfer admittance	I Y <sub>fs</sub> ľ*	0.2	-	-	S	I <sub>D</sub> =200mA, V <sub>DS</sub> =10V
Input capacitance	C <sub>iss</sub>	1	26	-	pF	V <sub>DS</sub> =10V
Output capacitance	C <sub>oss</sub>	1	6	-	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	1	3	-	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	1	5	-	ns	I <sub>D</sub> =100mA, V <sub>DD</sub> ≒25V
Rise time	t <sub>r</sub> *	-	8	-	ns	V <sub>GS</sub> =4.5V
Turn-off delay time	t <sub>d(off)</sub> *	1	17	-	ns	$R_L=250\Omega$
Fall time	t <sub>f</sub> *	-	43	-	ns	$R_G=10\Omega$

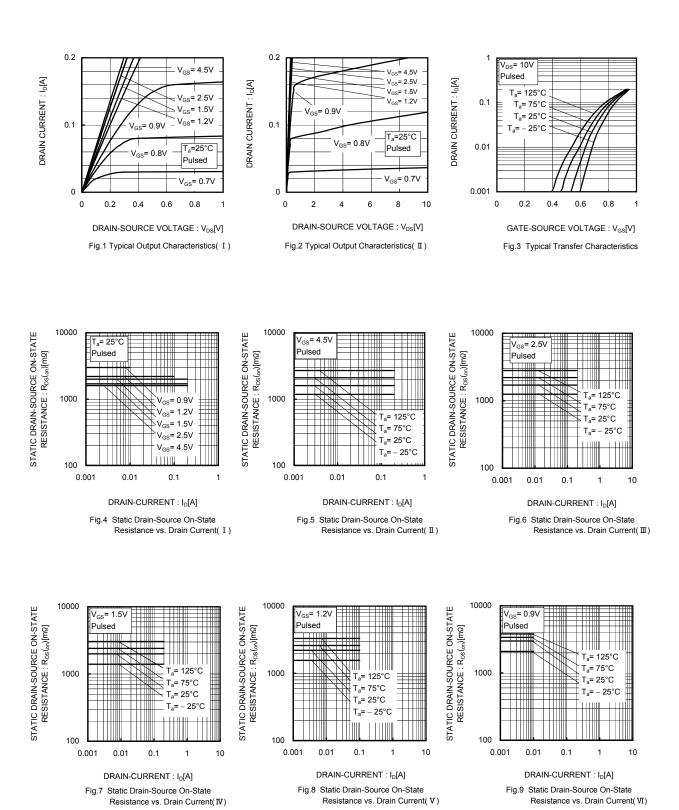
<sup>\*</sup>Pulsed

## ●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V <sub>SD</sub> *	-	-	1.2	V	$I_s$ =200mA, $V_{GS}$ =0V

<sup>\*</sup>Pulsed

### ● Electrical characteristics curves (Ta = 25°C)



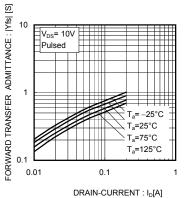


Fig.10 Forward Transfer Admittance vs. Drain Current

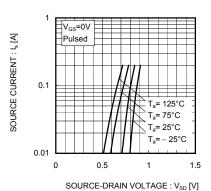


Fig.11 Reverse Drain Current vs. Sourse-Drain Voltage

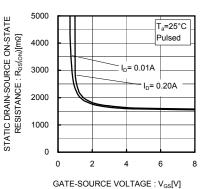


Fig.12 Static Drain-Source On-State Resistance vs. Gate Source Voltage

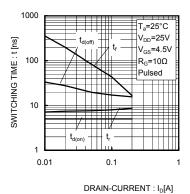


Fig.13 Switching Characteristics

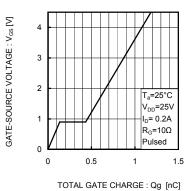


Fig.14 Typical Capacitance vs. Drain-Source Voltage

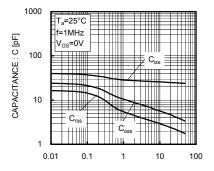


Fig.15 Typical Capacitance vs. Drain-Source Voltage

DRAIN-SOURCE VOLTAGE :  $V_{DS}[V]$ 

## Measurement circuits

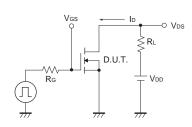


Fig.1-1 Switching time measurement circuit

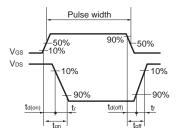


Fig.1-2 Switching waveforms

#### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

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