

100mA / 50V Digital transistors

(with built-in resistors)

DTC113ZUA / DTC113ZKA

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2)Each bias resistor is a thin-film resistor. Since they are completely insulated, the input can be negatively biased. The insulation also eliminates most of the parasitic effects.
- 3)Only the on / off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

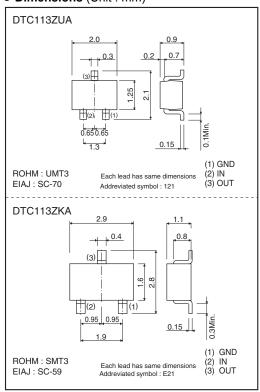
Packaging specifications

	Package	UMT3	SMT3	
	Packaging type	Taping	Taping	
	Code	T106	T146	
Part No.	Basic ordering unit (pieces)	3000	3000	
DTC113ZUA	1	0	-	
DTC113ZKA		_	0	

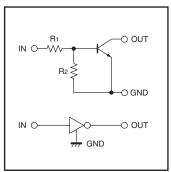
Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits		Unit	
- raiailletei	Symbol	DTC113ZUA	DTC113ZKA	Offic	
Supply voltage	Vcc	50		V	
Input voltage	Vin	−5 to +10		V	
Output ourrent	lo	100		mA	
Output current	Ic(Max.)	10	00	IIIA	
Power dissipation	Po	200		mW	
Junction temperature	Tj	150		°C	
Storage temperature	Tstg	-55 to +150		°C	

• Dimensions (Unit : mm)



Inner circuit



R₁=1.0kΩ, R₂=10kΩ

• Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	V _{I(off)}	-	-	0.3	V	Vcc=5V, Io=100μA
	V _{I(on)}	3	-	-		Vo=0.3V, Io=20mA
Output voltage	V _{O(on)}	-	0.1	0.3	V	lo/l⊫10mA/0.5mA
Input current	lı	-	-	7.2	mA	V _I =5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V
DC current gain	Gı	33	-	_	-	Vo=5V, Io=5mA
Input resistance	R ₁	0.7	1	1.3	kΩ	-
Resistance ratio	R2/R1	8	10	12	-	-
Transition frequency	f⊤ *	-	250	_	MHz	VcE=10V, IE= -5mA, f=100MHz

^{*} Characteristics of built-in transistor

• Electrical characteristic curves

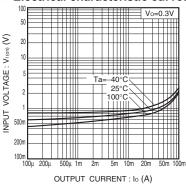


Fig.1 Input voltage vs. output current (ON characteristics)

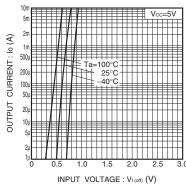


Fig.2 Output current vs. input voltage (OFF characteristics)

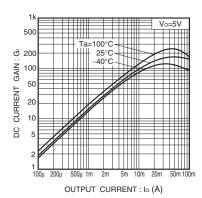


Fig.3 DC current gain vs. output current

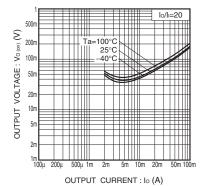


Fig.4 Output voltage vs. output current

Notes

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