

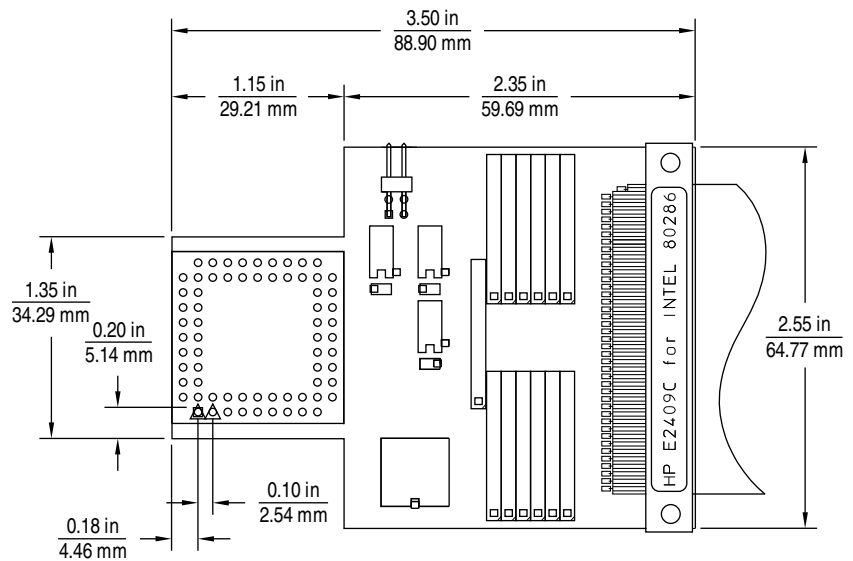
HP E2409C

Intel80C286™ Preprocessor Interface

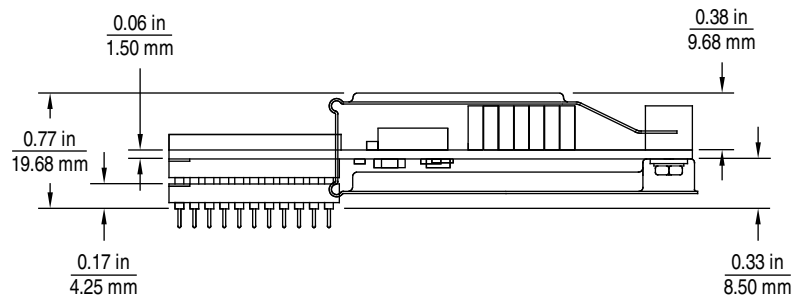
**For use with
HP logic analyzers**

The Hewlett-Packard E2409C preprocessor interface provides an easy way to connect a Hewlett-Packard logic analyzer to a target system using the Intel80C286™ microprocessor. All necessary clocks are passed through by the preprocessor to the logic analyzer, ensuring that data is captured at the correct time. Software is shipped with the product that automatically configures the logic analyzer, generating labels for address, data, and status signals. Included with the software is a disassembler which displays execution traces in 80C286 microprocessor mnemonics. Advanced disassembler features allow the user to either display or suppress the following:

- memory reads/writes
- i/o reads/writes
- unexpected prefetches
- branches, jumps
- calls and returns



(TOP VIEW)



(SIDE VIEW)

**HP E2409C
Intel80C286
preprocessor**

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Key Specifications

Timing Analysis Support

All signals are buffered by 74FCT646ATQ gates with a 1 ns channel-to-channel skew except for S0 and S1, which are buffered twice and have a 5 ns delay relative to all other signals.

Pods

The preprocessor has four pods available. For disassembly three logic analyzer pods must be used. Four pods would allow timing measurements of all processor signals.

Termination Adapters (TAs)

None are required as the pods are terminated on the preprocessor.

Logic Analyzers Supported

HP 1650B, 1652B, 1654B, 1660A/61A/62A, 16510B, 16511B, 16540/41 [A-D], 16542A, 16550A, 16555A

Processors Supported

Intel80C286 68-pin PGA package at a maximum clock speed of 25 MHz (50 MHz clock input), and all other microprocessors made by other manufacturers that comply with Intel 80286 specifications. Supports Harris 80286, 80C286 in a 68-pin PGA package at a maximum clock speed of 25 MHz (50 MHz clock input).

Optional Adapters Available

HP E2409C #1CA adapts the E2409C to an LCC version of the chip.

HP E2409C #1CB adapts the E2409C to a PLCC version of the chip.

Signal Line Loading

8 pF on all signals except for RESET and HLDA which have 16 pF.

Intel80C286™ is a U.S. trademark of Intel Corporation

For more information, call your local HP sales office listed in your telephone directory, or an HP regional office listed below:

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Test and Measurement Organization
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