

SEMICONDUCTOR TECHNICAL DATA

HIGH-SPEED(480Mbps) USB2.0 DPDT SWITCH

DESCRIPTION

The KUSB50QN is a High-Speed(480Mbps) USB2.0 signal switch. Configured as a double-pole double-throw (DPDT) switch, it is optimized for switching between 2 Hi-Speed sources or a Hi-Speed(480Mbps) and Full-Speed(12Mbps) source. Its wide Bandwidth (720MHz) is wide enough to pass High-Speed USB2.0 differential signals. Industry-leading advantages include a propagation delay of less than 250ps, resulting from its low channel resistance and low on capacitance. It is bidirectional and offers little or no attenuation of the High-Speed signal at the outputs.

Its high channel-to-channel crosstalk rejection results in minimal noise interference. The switch is designed to minimize current consumption even ge protection, all data I/O pins can. when the control voltage applied to the S pin, is lower than the V_{CC}. The KUSB50QN also offers over-voltage protection per the USB2.0 Specification. With the chip powered ON or OFF, all data I/O pins can withstand a short to Vbus(5.25V).

FEATURES