

D-TYPE FLIP FLOP WITH PRESET AND CLEAR

The KIC7W74FK is a high speed C²MOS D FLIP FLOP fabricated with silicon gate C²MOS technology. It achieves the high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation. The signal level applied to the D INPUT is transferred to Q OUTPUT during the positive going transition of the CLOCK pulse. CLEAR and PRESET are independent of the CLOCK and are accomplished by setting the appropriate input to an "L" level. Input is equipped with protection circuits against static discharge or transient excess voltage.

FEATURES

*Not recommended
for new design*