

2-CHANNEL MULTIPLEXER/DEMULTIPLEXER

The KIC7W53FK is a high speed C²MOS ANALOG MULTIPLEXER/DEMULTIPLEXER fabricated with silicon gate CMOS technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation.

The KIC7W53FK has a 2 channel configuration.

The digital signal to the control terminal turns "ON" the corresponding switch of each channel a large amplitude signal ($V_{CC}-V_{EE}$) can then be switched by the small logical amplitude ($V_{CC}-GND$) control signal. For example, in the case of $V_{CC}=5V$, $GND=0V$, $V_{EE}=-5V$, signals between $-5V$ and $+5V$ can be switched from the logical circuit with a single power supply of $5V$.

As the ON-resistance of each switch is low, they can be connected to circuit with low input impedance.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

FEATURES

Not recommended
for new design